ON Semiconductor

Is Now

Onsemi

To learn more about onsemi[™], please visit our website at <u>www.onsemi.com</u>

onsemi and ONSEMI: and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product factures, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application is the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application, Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application, Buyer shall indemnify and hold ons



ON Semiconductor®

FDG6324L Integrated Load Switch

General Description

This device is intended to be configured as a load switch and is particularly suited for compact computer peripheral switching applications where 3V to 20V input and 0.6A output current capability are needed. This device features a small N-Channel MOSFET (Q1) together with a large P-Channel Power MOSFET (Q2) in a single SC70-6 package.

Features

• $V_{DROP}=0.2V @ V_{IN}=12V, I_{L}=0.36A. R_{(ON)}=0.55\Omega. V_{DROP}=0.2V @ V_{IN}=5V, I_{L}=0.27A. R_{(ON)}=0.75\Omega.$

- Very small package outline (SC70-6).
- Control MOSFET (Q1) includes Zener protection for ESD ruggedness (> 6KV Human Body Model).
- High density cell design for extremely low on-resistance.

	2	÷				888
SC7	0-6	SOT-23	SuperSOT [™] -6	SuperSOT [™] -8	SO-8	SOT-223
pin	24 24		V IN , R1 4	3 Vour, 0 2 Vour, 0 4 01 1 R2		EQUIVALENT APPLICATION
	SC70-6			lication Circuit		
Absolu	ute Maxim		See App $T_A = 25^{\circ}$ C unless otherwise note	id	FDG6324L	Units
Absolı Symbol	Ite Maxim Parameter			id	FDG6324L 3 - 20	Units V
Absolı Symbol / _{IN}	Ite Maxim Parameter Input Voltag	e Range		id		
Absolu Symbol V _{IN}	Ite Maxim Parameter Input Voltag On/Off Volta	e Range age Range	$T_A = 25^{\circ}C$ unless otherwise note	id	3 - 20 2.5 - 8	V V
Absolu Symbol V _{IN}	Ite Maxim Parameter Input Voltag	e Range age Range	T _A = 25°C unless otherwise note	id	3 - 20 2.5 - 8 0.6	V
Absoli Symbol V _{IN} V _{ON/OFF} L	Ite Maxim Parameter Input Voltage On/Off Volta Load Curren	e Range age Range t	T _A = 25°C unless otherwise note - Continuous (Note 1) - Pulsed (Note 1 &	id	3 - 20 2.5 - 8 0.6 1.8	V V A
Absolu Symbol V _{IN} V _{ON/OFF}	Ite Maxim Parameter Input Voltage On/Off Volta Load Curren Maximum Po	e Range nge Range t t	T _A = 25°C unless otherwise note - Continuous (Note 1) - Pulsed (Note 1 & (Note 2)	id	3 - 20 2.5 - 8 0.6 1.8 0.3	V V A W
Absolu Symbol V _{IN} V _{ON/OFF} L L T _J , T _{STG}	Jte Maxim Parameter Input Voltage On/Off Volta Load Curren Maximum Por Operating ar	e Range age Range t t ower Dissipation nd Storage Tempera	T _A = 25°C unless otherwise note - Continuous (Note 1) - Pulsed (Note 1 & (Note 2)	id	3 - 20 2.5 - 8 0.6 1.8 0.3 -55 to 150	V V A W °C
Absolu Symbol V _{IN} V _{ON/OFF} I L P _D T _J , T _{STG}	Jte Maxim Parameter Input Voltage On/Off Volta Load Curren Maximum Per Operating ar Electrostatic	e Range nge Range t t	T _A = 25°C unless otherwise note - Continuous (Note 1) - Pulsed (Note 1 & (Note 2) ature Range	id	3 - 20 2.5 - 8 0.6 1.8 0.3	V V A W
Absolu Symbol V _{IN} V _{ON/OFF} I _L P _D T _J , T _{STG} ESD	Jte Maxim Parameter Input Voltage On/Off Volta Load Curren Maximum Per Operating ar Electrostatic	e Range age Range t bwer Dissipation nd Storage Tempera Discharge Rating y Model (100pf/150	T _A = 25°C unless otherwise note - Continuous (Note 1) - Pulsed (Note 1 & (Note 2) ature Range	id	3 - 20 2.5 - 8 0.6 1.8 0.3 -55 to 150	V V A W °C

©1999 Semiconductor Components Industries, LLC. October-2017, Rev. 4

Publication Order Number: FDG6324L/D

Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHAP	RACTERISTICS					
I _{FL}	Forward Leakage Current	$V_{IN} = 20 \text{ V}, V_{ON/OFF} = 0 \text{ V}$			1	μΑ
ON CHAR	ACTERISTICS (Note 3)	•	•		•	
V _{DROP}	Conduction Voltage Drop	V_{IN} = 12 V, $V_{ON/OFF}$ = 3.3 V, I_L = 0.36 A		0.14	0.2	V
		$V_{IN} = 5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_{L} = 0.27 \text{ A}$		0.16	0.2	
R _(ON)	Q ₂ - Static On-Resistance	V _{GS} = -12 V, I _D = -0.6 A		0.37	0.55	Ω
		V _{GS} = -5 V, I _D = -0.5 A		0.58	0.75	
IL	Load Current	V_{DROP} = 0.2 V, V_{IN} = 12 V, $V_{\text{ON/OFF}}$ = 3.3 V	0.36			А
		V _{DROP} = 0.2 V, V _{IN} = 5 V, V _{ON/OFF} = 3.3 V	0.27			1

Notes:

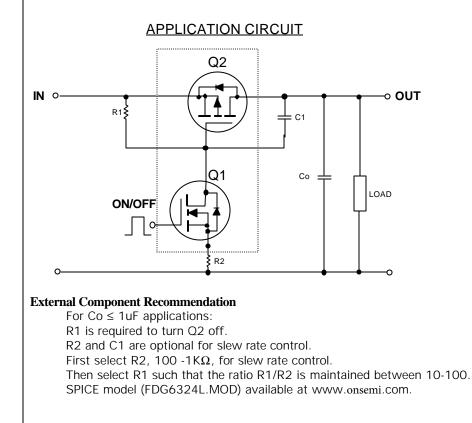
1. Range of V_{in} can be up to 25V, but R₁ and R₂ must be scaled such that V_{GS} of Q2 does not exceed -20V.

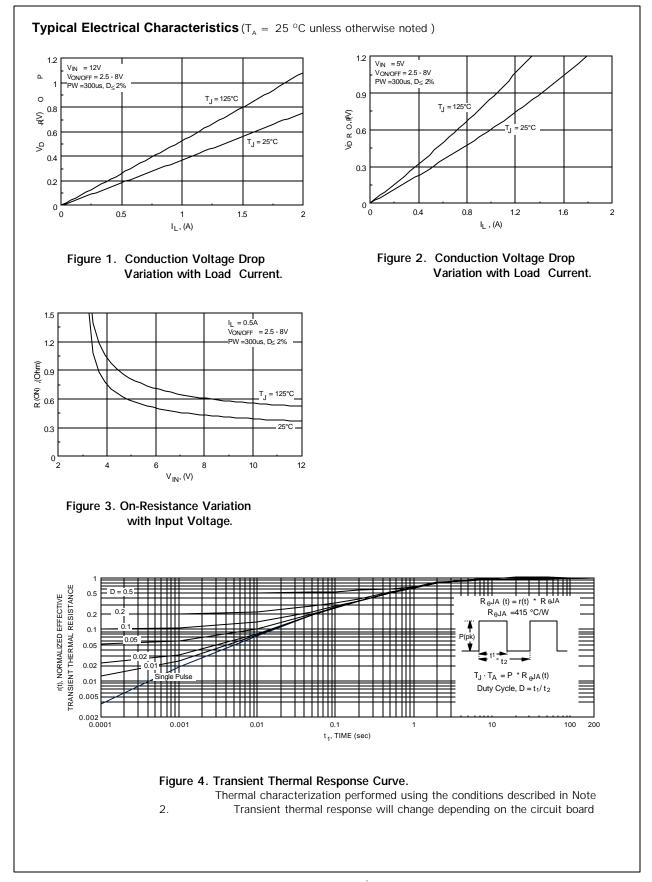
2. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is

guaranteed by design while R_{BCA} is determined by the user's board design. Thermal ratings based on minimum mounting pad.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2.0%

FDG6324L Load Switch Application





www.onsemi.com 3

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor haves against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death a

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Semiconductor Components Industries, LLC