BCW68 series 45 V, 800 mA PNP general-purpose transistor Rev. 1 – 21 April 2017

Product data sheet

1 General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complements: BCW66F/G/H

2 Features and benefits

- High current
- AEC-Q101 qualified

3 Applications

• General-purpose switching and amplification

4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-45	V
I _C	collector current			-	-	-800	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-1	А
h _{FE}	DC current gain	V_{CE} = -1 V; I _C = -100 mA; T _{amb} = 25 °C	[1]				
	BCW68F			100	-	250	
	BCW68G			160	-	400	
	BCW68H			250	-	600	

[1] pulsed: $t_p \le 300 \ \mu s, \ \delta \le 0.02$



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5 Pinning information

Table 2. Pinning						
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	В	base				
2	E	emitter	3	c l		
3	C	collector		B E sym132		

6 Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
BCW68F	TO-236AB	plastic surface-mounted package; 3 leads	SOT23		
BCW68G					
BCW68H					

7 Marking

Table 4. Marking

Type number		Marking code
BCW68F	[1]	ET%
BCW68G	[1]	EU%
BCW68H	[1]	EV%

[1] % = placeholder for manufacturing site code

8 Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	-50	V
V _{CEO}	collector-emitter voltage	open base	-	-45	V
V _{EBO}	emitter-base voltage	open collector	-	-5	V
I _C	collector current		-	-800	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-1	А
I _B	base current		-	-100	mA

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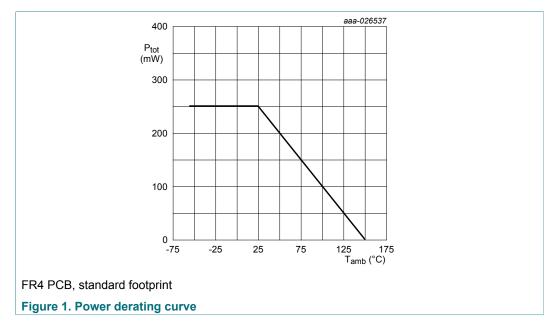
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Symbol	Parameter	Conditions		Min	Max	Unit
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Т _ј	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB), single-sided chopper, tin-plated and standard footprint.



9 Thermal characteristics

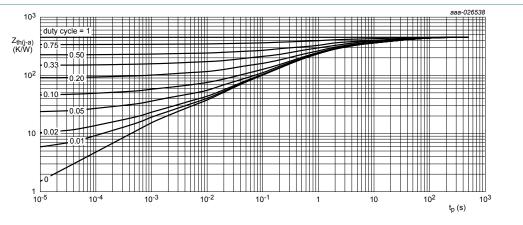
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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FR4 PCB, standard footprint

Figure 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10 Electrical characteristics

Table 7. Electrical characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO} collector-base		V _{CB} = -40 V; I _E = 0 A		-	-	-20	nA
	cut-off current	V _{CB} = -40 V; I _E = 0 A; T _j = 150 °C		-	-	-5	μA
I _{EBO}	emitter-base cut-off current	/ _{EB} = -5 V; I _C = 0 A		-	-	-20	nA
h _{FE} DC current gain		1		1			
	BCW68F/G/H	V _{CE} = -1 V; I _C = -100 μA		100	-	-	
	BCW68F/G/H	V _{CE} = -1 V; I _C = -1 mA		100	-	-	
BC	BCW68F/G/H	V _{CE} = -1 V; I _C = -10 mA		100	-	-	
	BCW68F	V _{CE} = -1 V; I _C = -100 mA		100	-	250	
	BCW68G			160	-	400	
	BCW68H			250	-	600	
	BCW68F	V _{CE} = -2 V; I _C = -500 mA		35	-	-	
	BCW68G		60	-	-		
	BCW68H	_		100	-	-	
V _{CEsat}	collector-emitter	I _C = -100 mA; I _B = -10 mA	[1]	-	-	-350	mV
	saturation voltage	I _C = -500 mA; I _B = -50 mA	[1]	-	-	-450	mV
V _{BEsat}	base-emitter	I _C = -100 mA; I _B = -10 mA	[1]	-	-	-1.25	V
	saturation voltage	I _C = -500 mA; I _B = -50 mA	[1]	-	-	-1.25	V
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz		80	-	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	5	-	pF

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

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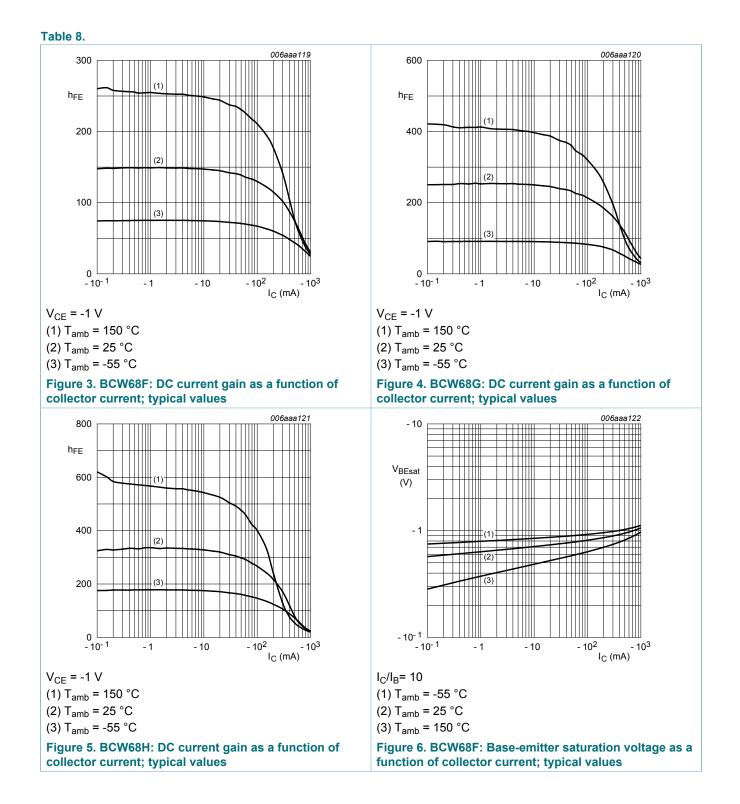
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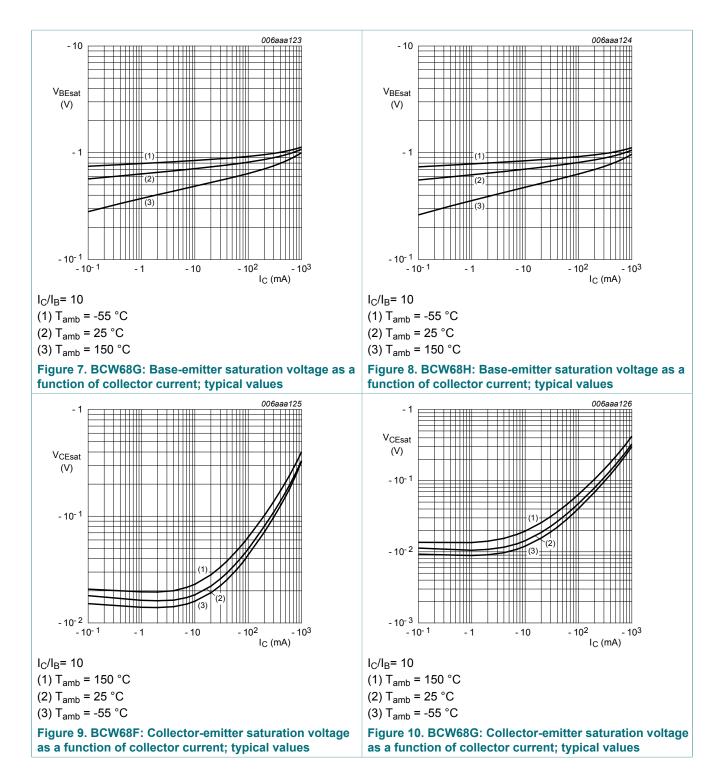
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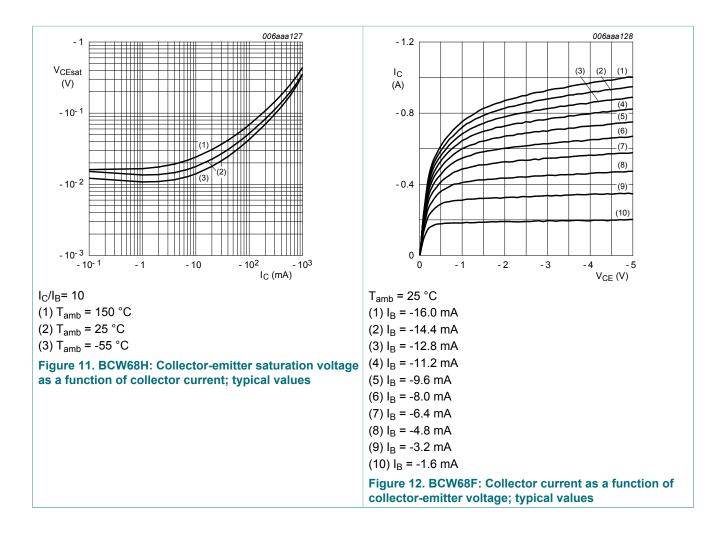
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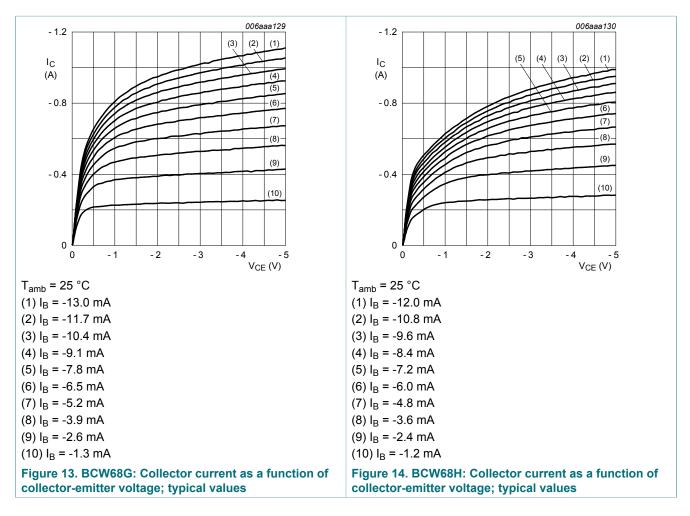
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11 Test information

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

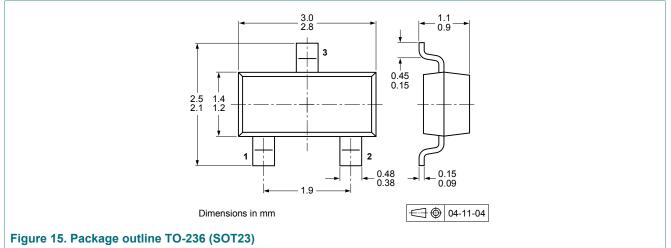
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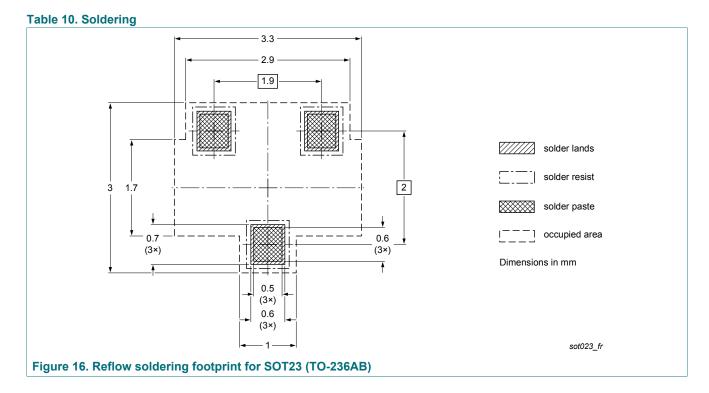
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12 Package outline

Table 9. Package outline



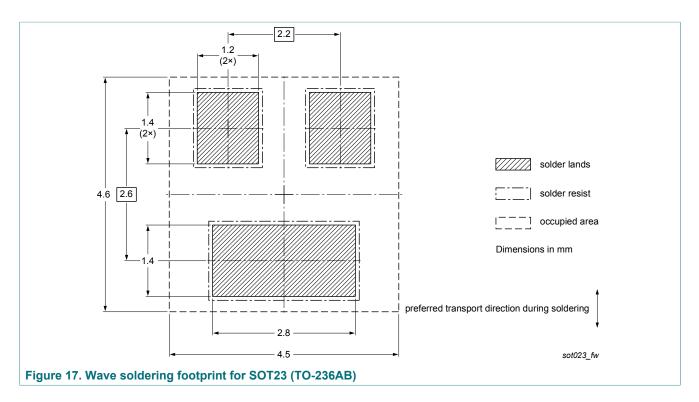
13 Soldering



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14 Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCW68X_SER v.1	20170421	Product data sheet	-	-

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15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

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