BCW68 series 45 V, 800 mA PNP general-purpose transistor Rev. 1 – 21 April 2017

Product data sheet

## **1** General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complements: BCW66F/G/H

## 2 Features and benefits

- High current
- AEC-Q101 qualified

## **3** Applications

• General-purpose switching and amplification

## 4 Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-45	V
I <sub>C</sub>	collector current			-	-	-800	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	-1	А
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; I <sub>C</sub> = -100 mA; T <sub>amb</sub> = 25 °C	[1]				
	BCW68F			100	-	250	
	BCW68G			160	-	400	
	BCW68H			250	-	600	

[1] pulsed:  $t_p \le 300 \ \mu s, \ \delta \le 0.02$ 



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# 5 Pinning information

Table 2. Pinning						
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	В	base				
2	E	emitter	3	c l		
3	C	collector		B E sym132		

# **6** Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
BCW68F	TO-236AB	plastic surface-mounted package; 3 leads	SOT23		
BCW68G					
BCW68H					

## 7 Marking

#### Table 4. Marking

Type number		Marking code
BCW68F	[1]	ET%
BCW68G	[1]	EU%
BCW68H	[1]	EV%

[1] % = placeholder for manufacturing site code

## 8 Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-45	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-5	V
I <sub>C</sub>	collector current		-	-800	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-1	А
I <sub>B</sub>	base current		-	-100	mA

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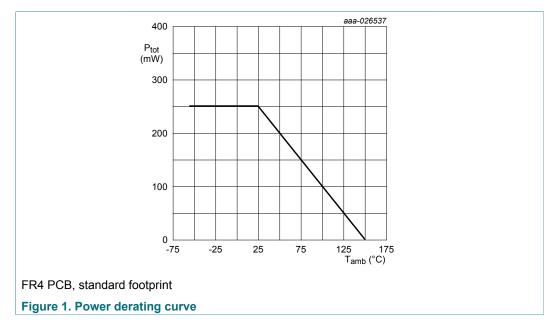
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Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Т <sub>ј</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB), single-sided chopper, tin-plated and standard footprint.



## **9** Thermal characteristics

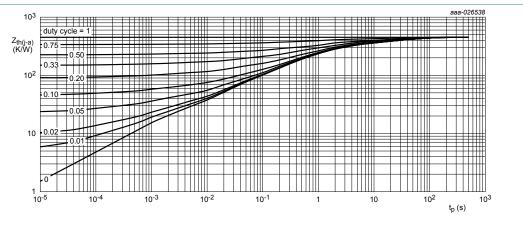
#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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FR4 PCB, standard footprint

Figure 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## **10 Electrical characteristics**

#### **Table 7. Electrical characteristics**

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub> collector-base		V <sub>CB</sub> = -40 V; I <sub>E</sub> = 0 A		-	-	-20	nA
	cut-off current	V <sub>CB</sub> = -40 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	/ <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A		-	-	-20	nA
h <sub>FE</sub> DC current gain		1		1			
	BCW68F/G/H	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 μA		100	-	-	
	BCW68F/G/H	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -1 mA		100	-	-	
BC	BCW68F/G/H	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -10 mA		100	-	-	
	BCW68F	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA		100	-	250	
	BCW68G			160	-	400	
	BCW68H			250	-	600	
	BCW68F	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA		35	-	-	
	BCW68G		60	-	-		
	BCW68H	_		100	-	-	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = -100 mA; I <sub>B</sub> = -10 mA	[1]	-	-	-350	mV
	saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA	[1]	-	-	-450	mV
V <sub>BEsat</sub>	base-emitter	I <sub>C</sub> = -100 mA; I <sub>B</sub> = -10 mA	[1]	-	-	-1.25	V
	saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA	[1]	-	-	-1.25	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz		80	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	5	-	pF

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

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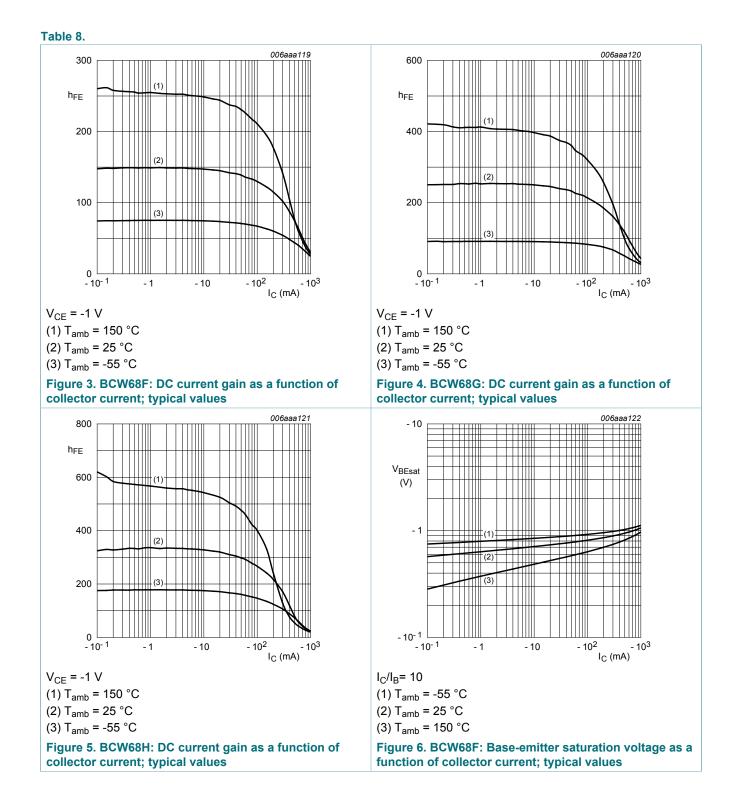
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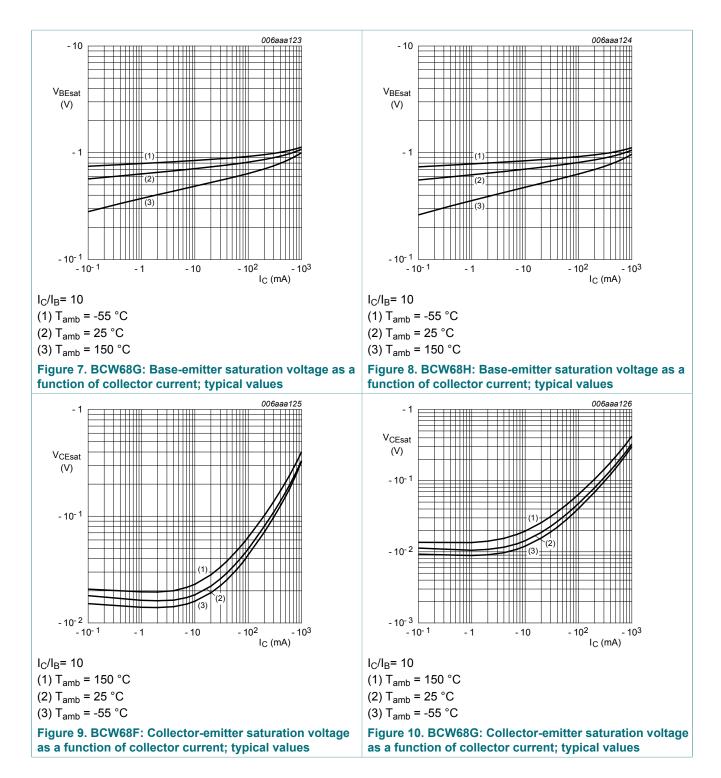
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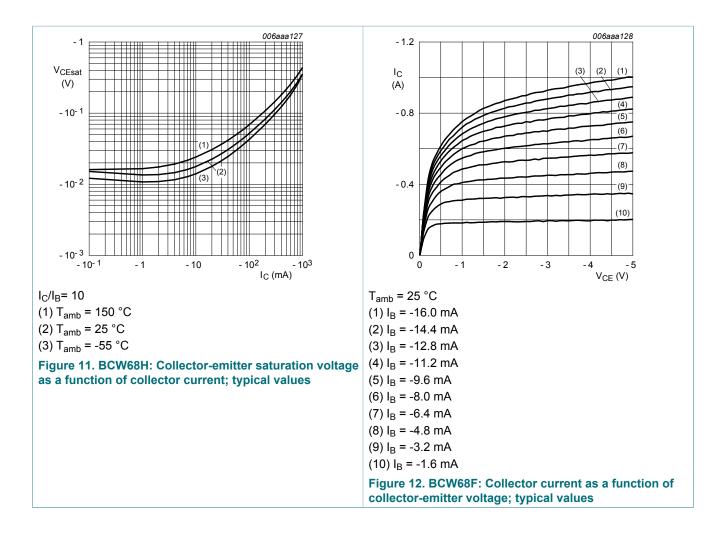
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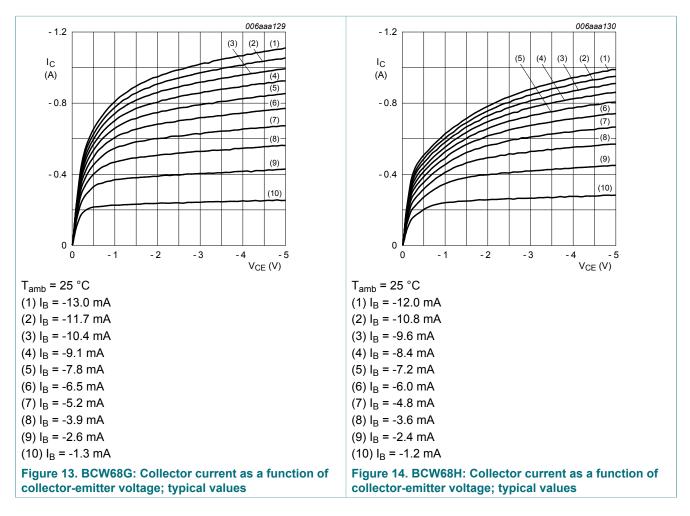
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## **11 Test information**

#### **11.1 Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

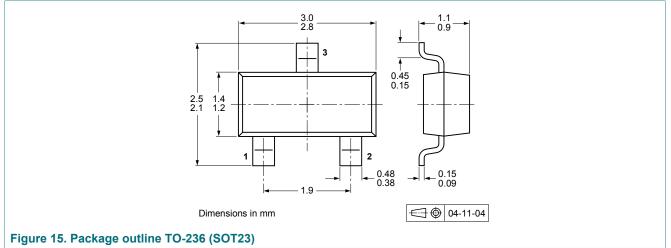
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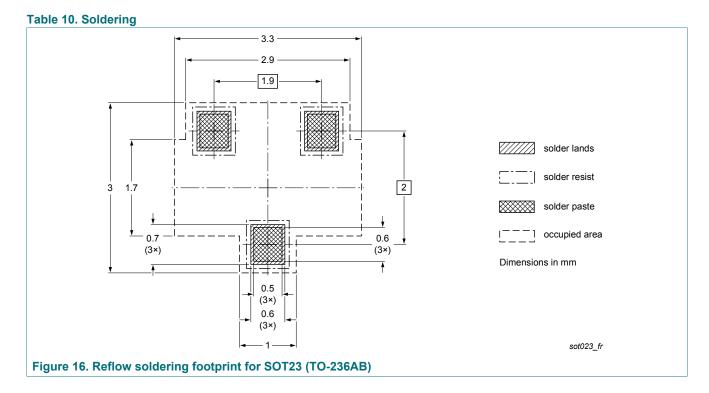
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# 12 Package outline

#### Table 9. Package outline



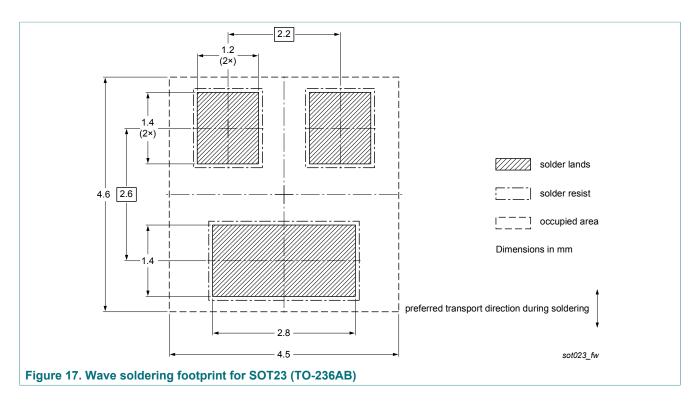
## **13 Soldering**



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# 14 Revision history

#### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCW68X_SER v.1	20170421	Product data sheet	-	-

45 V, 800 mA PNP general-purpose transistor

# 15 Legal information

### 15.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

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