

MT29F64G08ABCBBH6-6

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Orderable Part Information

Status	Production	Alternative Part	N/A
FBGA Code	NW600	SPD Data	N/A
MBQual Data	N/A	Shipping Media	N/A
PLP	No	Start Date	N/A

Specs

Density	64Gb	Status	Production
RoHS	Yes	Width	x8
Voltage	3.3V	Package	VBGA
Pin Count	152-ball	MT/s	333 MT/s
I/O	Common		

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FAQs

- » Do you support small block devices?
- » How much ECC do I need to support your devices?
- » I am using the correct amount of error correction code (ECC) for the NAND device, but I'm still seeing bitbyte errors in data I read back from the NAND device.
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Title & Description	Secure	ID	Updated
HSpice: 64Gb 128Gb 256Gb 512Gb Async Sync NAND: Rev.2.2		M84C	11/2014
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- + [How much ECC do I need to support your devices?](#)
- + [I am using the correct amount of error correction code \(ECC\) for the NAND device, but I'm still seeing bitbyte errors in data I read back from the NAND device.](#)

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- + [My design was based on a specification stating the JTAG was relative to VDD \(1.8V\), but now we've discovered that JTAG is actually relative to VDDQ \(1.5V\). It's a fairly significant board spin to change this; what do I risk by leaving the design as-is? I assume that the specification is still for VDDQ + 0.3V = 1.8V, but with CMOS parts there's no way I can guarantee that it won't swing past that on transitions.](#)
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