

# LPY550AL

Preliminary data

## MEMS motion sensor:

dual axis pitch and yaw ±500°/s analog output gyroscope

#### **Features**

- 2.7 V to 3.6 V single supply operation
- Very extended operating temperature range (-40°C to +85°C)
- High stability overtemperature
- Absolute analog rate output
- Two separate outputs for each axis (1x and 4x amplified)
- Integrated low-pass filters
- Low power consumption
- Embedded power-down
- Embedded self-test
- High shock and vibration survivability
- ECOPACK<sup>®</sup> RoHS and "Green" compliant (see Section 5)

## Applications

- Pointing devices, remote and geme controllers
- Gaming applications
- Motion control with user interface
- Industrial and robotics

## Description

There Y550AL is a low-power two-axis nicromachined gyroscope able to measure angular rate along pitch and yaw axes.

It provides excellent temperature stability and high resolution over extended operating temperature range (-40°C to +85°C).

#### Table 1. Device summary



LGA-16L (5x5x1.5mm)

The LPY550AL has a full scale of  $\pm 5.0$  % and is capable of detecting rates with a  $\pm$  dB bandwidth up to 140 Hz.

The gyroscope is the combination of one actuator and one accelerometer integrated in a single micromachined structure.

It includes e sensing element composed by single drivin(incass, kept in continuos oscillating movement and able to react when an angular rate is applied based on the Coriolis principle.

A CMOS IC provides the measured angular rate to the external world through an analog output voltage, allowing high level of integration and production trimming to better match sensing element characteristics.

ST gyroscope family leverages on robust and mature manufacturing process already used for the production of micromachined accelerometers.

ST is already in the field with several hundreds million sensors with excellent acceptance from the market in terms of quality, reliability and performance.

LPY550AL is provided in plastic land grid array (LGA) package. Several years ago ST pioneered successfully the usage of this package for accelerometers. Today ST has the widest manufacturing capability and strongest expertise in the world for production of sensor in plastic LGA package.

Order code	r code Temperature range (°C) Package		Packing	
LPY550AL	-40 to +85	LGA-16 (5x5x1.5)	Tray	
LPY550ALTR	-40 to +85	LGA-16 (5x5x1.5)	Tape and reel	

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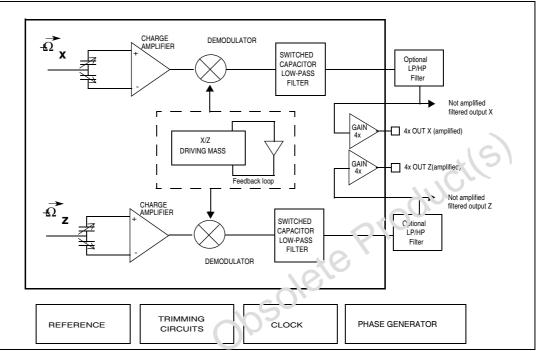
This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

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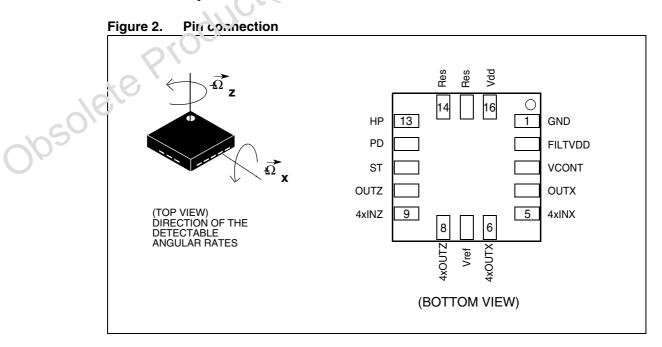


## 1 Block diagram and pin description





## 1.1 Pin description





2FILTVDDPLI3VCONTPLI4OUTXNor54xINXInp64xOUTXX ra7VrefRei84xOUTZZ ra94xINZInp10OUTZNor11STSei12PDPov13HPHiglogNor	Analog function       supply voltage       L filter connection pin #2       L filter connection pin #1       t amplified output       ut of 4x amplifier       ate signal output voltage (amplified)       ference voltage       ate signal output voltage (amplified)       nut of 4x amplifier       t amplified output       ut of 4x amplifier       t amplifier       t amplifier       at amplifier       at amplifier
2FILTVDDPLI3VCONTPLI4OUTXNor54xINXInp64xOUTXX ra7VrefRef84xOUTZZ ra94xINZInp10OUTZNor11STSel12PDPov13HPHiglogNor	L filter connection pin #2 L filter connection pin #1 t amplified output t amplified output t amplified output voltage (amplified) ference voltage ate signal output voltage (amplified) t of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
3       VCONT       PLI         4       OUTX       Noi         5       4xINX       Inp         6       4xOUTX       X ra         7       Vref       Rei         8       4xOUTZ       Z ra         9       4xINZ       Inp         10       OUTZ       Noi         11       ST       Sei         12       PD       Pov mo         13       HP       Hig log	L filter connection pin #1 t amplified output ut of 4x amplifier ate signal output voltage (amplified) ference voltage ate signal output voltage (amplified) ut of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
4OUTXNot54xINXInp64xOUTXX rate7VrefRei84xOUTZZ rate94xINZInp10OUTZNot11STSet12PDPov13HPHiglog	t amplified output ut of 4x amplifier ate signal output voltage (amplified) ference voltage ate signal output voltage (amplified) ut of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
54xINXInp64xOUTXX rate7VrefRef84xOUTZZ rate94xINZInp10OUTZNot11STSet12PDPow13HPHiglog	ate signal output voltage (amplified) ference voltage ate signal output voltage (amplified) ate signal outpu
6       4xOUTX       X radius         7       Vref       Ref         8       4xOUTZ       Z radius         9       4xINZ       Inp         10       OUTZ       Not         11       ST       Set         12       PD       Pov mo         13       HP       Hig log	ate signal output voltage (amplified) ference voltage ate signal output voltage (amplified) out of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
7VrefRet84xOUTZZ rat94xINZInp10OUTZNot11STSet12PDPov13HPHiglogInp	ference voltage ate signal output voltage (amplified) nut of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
84xOUTZZ radius94xINZInp10OUTZNot11STSet12PDPov mo13HPHig log	ate signal output voltage (amplified) ut of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
94xINZInp10OUTZNot11STSel12PDPox mo13HPHig log	ut of 4x amplifier t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: normal mode; logic 1: power-down
10   OUTZ   Nor     11   ST   Sel     12   PD   Pov mo     13   HP   Hig log	t amplified output If-test (logic 0: normal mode, logic 1: self-test) wer-down (logic 0: norma' mode; logic 1: power-down
11STSel12PDPov mo13HPHig log	lf-test (logic 0: normal moc'e, logic 1: self-test) wer-down (logic 0: กอากล' mode; logic 1: power-down
12PDPov mo13HPHig log	wer-down (logic 0: porma' mode; logic 1: power-down
12 PD mo   13 HP Hig log	
14.15 Bes 56	h pass filter רבכשל (logic 0: normal operation mode; ic1: פגופרחפר high pass filter is reset)
	Served. Connect to Vdd
16 Vdd For	wer supply
16 Vdd For	

Table 2.Pin description



## 2 Mechanical and electrical specifications

#### 2.1 Mechanical characteristics

Table 5.	$\sim$ Mechanical characteristics $@$ vod = 3 v, 1 = 25 °C unless otherwise noted				wise noteu.	,
Symbol	Parameter	Test condition	Min.	Typ. <sup>(2)</sup>	Max.	Unit
FSA	Maggurament range	4x OUT (amplified)		±500		°/s
FS	Measurement range	OUT (not amplified)		±2000		°/s
SoA	Sensitivity <sup>(3)</sup>	4x OUT (amplified)		2		mV/ °/s
So	Sensitivity	OUT (not amplified)		0.5		mV/ °/s
SoDr	Sensitivity change vs temperature	Delta from 25°C		0.037		%/°C
Voff	Zero-rate level <sup>(3)</sup>			1.23	0.	V
Vref	Reference voltage			1.23		V
OffDr	Zero-rate level change Vs temperature	Delta from 25°C		0.03		°/s/°C
NL	Non linearity	Best fit straight line	76	±1		% FS
BW	Bandwidth <sup>(4)</sup>		60,	140		Hz
Rn	Rate noise density		D	0.059		°/s / √Hz
Тор	Operating temperature range		-40		+85	°C

#### Table 3. Mechanical characteristics @ Vdd = 3 V, T = 25 °C unless otherwise noted<sup>(1)</sup>

1. The product is factory calibrated at 3 V. The opera ional power supply range is specified in Table 4.

2. Typical specifications are not guarantee a

Obsolete

3. Sensitivity and Zero-rate Offset a e nor ratiometric to supply voltage

4. The product is capable of measuring angular rates extending from DC to the selected BW.



#### 2.2 **Electrical characteristics**

Electrical characteristics @ Vdd =3 V, T=25 °C unless otherwise noted<sup>(1)</sup> Table 4.

Symbol	Parameter	Parameter Test condition		Typ. <sup>(2)</sup>	Max.	Unit	
Vdd	Supply voltage		2.7	3	3.6	V	
ldd	Supply current	PD pin connected to GND		6.8		mA	
lddPdn	Supply current in power-down mode	PD pin connected to Vdd		1	5	μA	
Vst	Colf toot input	Logic 0 level	0		0.2*Vdd	v	
VST Self-test input		Logic 1 level	0.8*Vdd		Vdd	ľ	
VPD Power-down input		Logic 0 level	0		0.2*\'לגי		
VPD	Power-down input	Logic 1 level	0.8*Vdd		\'dr.	v	
Тор	Operating temperature range		-40	200	+85	°C	
. The product is factory calibrated at 3 V							
2. Typical specifications are not guaranteed							
2. Typical specifications are not guaranteed							
2.3 Absolute maximum ratings							

#### Absolute maximum ratings 2.3

Stresses above those listed as "Abso ute maximum ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device under these conditions is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

	G
Table 5.	Absolute maximum ratings

Symbol		Ratings	Maximum value	Unit
	Vdd	Supply voltage	-0.3 to 6	V
	ריע	Input voltage on any control pin (PD, ST)	-0.3 to Vdd +0.3	V
Obsole	T <sub>STG</sub>	Storage temperature range	-40 to +125	°C
	Α	Acceleration)	3000 <i>g</i> for 0.5 ms	
	A	Acceleration)	10000 <i>g</i> for 0.1 ms	
	ESD	Electrostatic discharge protection	2 (HBM)	kV



This is a mechanical shock sensitive device, improper handling can cause permanent damage to the part



This is an ESD sensitive device, improper handling can cause permanent damage to the part



## 3 Terminology

#### 3.1 Sensitivity

An angular rate gyroscope is a device that produces a positive-going output voltage for counterclockwise rotation around the sensible axis considered. Sensitivity describes the gain of the sensor and can be determined by applying a defined angular velocity to it. This value changes very little over temperature and time.

#### 3.2 Zero-rate level

Zero-rate level describes the actual output signal if there is no angular rate present. Zerorate level of precise MEMS sensors is, to some extent, a result of stress to the sensor and therefore zero-rate level can slightly change after mounting the sensor onto a printed circuit board or after exposing it to extensive mechanical stress. This value changes very little over temperature and time.

#### 3.3 Self-test

Self-test allows testing the mechanical and electrical part of the sensor, allowing the seismic mass to be moved by means of an electrostatic test-force. The self-test function is off when the ST pin is connected to GND. When the ST pin is tied to Vdd, an actuation force is applied to the sensor, emulating a definite. Coriolis force. In this case the sensor output will exhibit a voltage change in its DC level which is also dependent on the supply voltage. When ST is active, the device output level is given by the algebraic sum of the signals produced by the velocity acting on the sensor and by the electrostatic test-force. If the output signals change within the amplitude specified in *Table 3*, then the mechanical element is working properly and the parameters of the interface chip are within the defined specification

### 3.4 High pass filter reset (HP)

LPY550AL provides the possibility to reset the optional external high pass filter by applying high logic value to HP pad. This procedure ensures faster response expecially during overload conditions. Moreover, this operation is suggested each time the device is powered.



## 4 Application hints

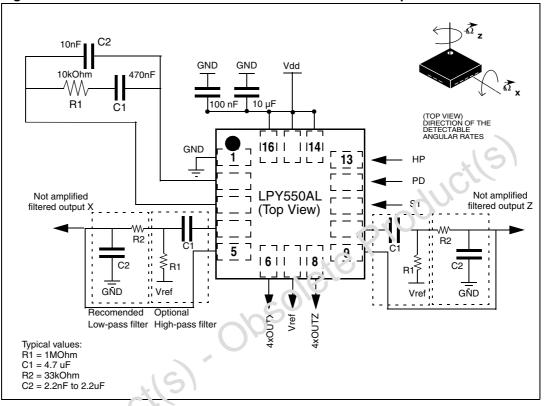


Figure 3. LPY550AL electrical connections and external components values

Power supply decoupling capacitors (100 nF ceramic or polyester + 10  $\mu$ F Aluminum) should be placed as near as possible to the device (common design practice).

The LFY.5tOAL allows band limiting the output rate response through the use of an external low pass filter (suggested) and/or high pass filter (optional) in addition to the embedded low pass filter ( $f_t = 140 \text{ Hz}$ ).

4xOUTX and 4xOUTZ are respectively OUTX and OUTZ amplified outputs lines, internally buffered to ensure low output impedance.

If external high pass or low pass filtering is not applied it is mandatory to short-circuit respectively pad 4 to pad 5 and pad 9 to pad 10 when amplified outputs are used.

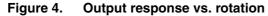
When only not-amplified outputs are used (OUTX/Z), it is suggested to set pads 5 and 9 to fixed reference voltage (Vref).

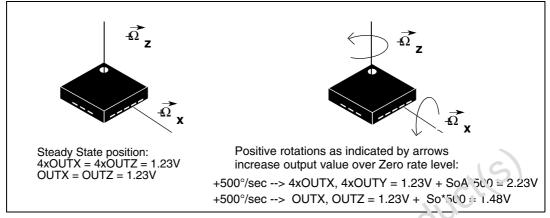
When high pass filter is applied to not amplified output (OUTx), it is recommended to buffer the line before entering ADC for performance optimization.

The LPY550AL IC includes a PLL (phase locked loop) circuit to synchronize driving and sensing interfaces. Capacitors and resistors must be added at **FILTVDD** and **VCONT** pins (as shown in *Figure 3*) to implement a low-pass filter.



#### 4.1 Output response vs. rotation





### 4.2 Soldering information

The LGA package is compliant with the ECOPACi<sup>3</sup> ToHS and "Green" standard. It is qualified for soldering heat resistance ac proding to JEDEC J-STD-020C.

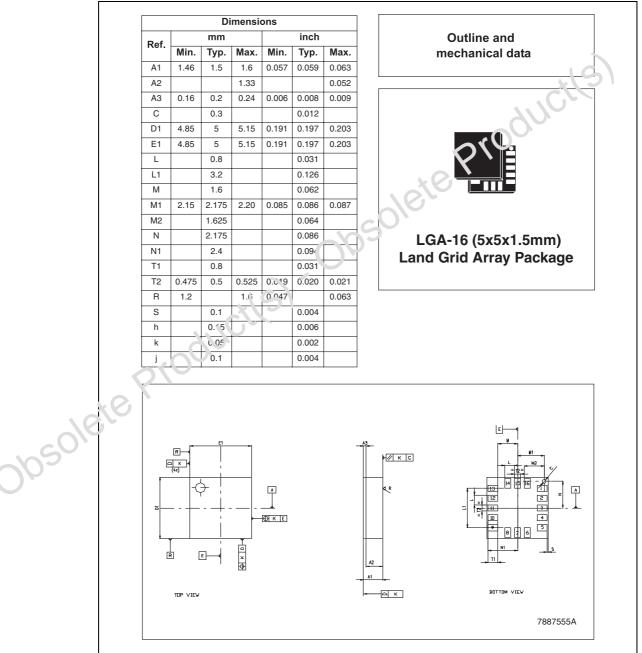
Leave "pin 1 indicator" unconnected (uri) g coldering.

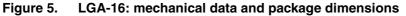
Land pattern and soldering recommendations are available at www.st.com



## 5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK<sup>®</sup> is an ST trademark.





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## 6 Revision history

#### Table 6.Document revision history

	Date	Revision	Changes
	04-Jun-2009	1	Initial release
	06-Jul-2009	2	Small text changes to improve readability. Updated <i>Table 4</i>
obsole	te Pro	ducil	obsolete Product(s)



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