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Orderable Parts for: EDFA232A2PB-GD-F SEE ALL LPDRAM PARTS								E ALL LPDRAM PARTS
	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
EDFA232A2PB-GD-F- D	Obsolete	Dry Pack	N/A	N/A	N/A	No		N/A
EDFA232A2PB-GD-F- R	Obsolete	Tape & Reel	N/A	N/A	N/A	No		N/A

Detailed Specifications

	Technology	LPDDR3	Density	16Gb	Part Status	Obsolete	RoHS	Yes
	Depth	512Mb	Width	x32	Voltage	1.2V	Package	VFBGA
	Pin Count	216-ball	Clock Rate	800 MHz	Data Rate	DDR3-1600	Cycle Time	
Dowi	nloaded from Arrow.	com.						

Op. Ten	р.	-30C to +85C	Brand	Micron			
			Si	m Models	s & Softw	are	
				Sim I	Models		
Verilog	∩ T						
Verilog: 40	ib Mo	bile LPDDR3 2E	DF (ZIP)				DRAM Mobile LPDDR3
Verilog sim	Ilation	model for 4Gb Mo	obile LPDDR3				
File Type: ZIP		Updated: 09/07/20	016				DOWNLOAD
IBIS 🔒							
IBIS: 216-E	all 2E	0F QDP 12x12 1	ch Mobile LPD	DR3 SDRAM (ZIP)			DRAM Mobile
Rev 1.0 216	Ball, Q	DP, Single-Channe	el, LPDDR3 SDRA	М			Mobile LPDDR3

RoHS Certificates

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RoHS Certificate of Compliance (PDF) Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive. File Type: (PDF) Updated: 09/2017	
2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.	
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RoHS Certificates	
China RoHS Certificate (PDF)	
Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.	
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Updated: 09/02/2014

Documentation & Support

See All LPDRAM Documentation

Technical Notes

SEARCH (3) LPDRAM TECHNICAL NOTES

Technical Notes	
TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)	DDR SDRAM DDR2 SDRAM DDR3 SDRAM
(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.	DDR3L-RS See More Tags
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Technical Notes	
TN-00-19: Thinning Considerations for Wafer Products (PDF)	DDR SDRAM DDR2 SDRAM DDR3 SDRAM
(TN-00-19) This technical note provides information on optimal wafer-thinning processes to meet specific customer requirements.	DRAM See More Tags
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Technical Notes	
TN-46-12: Mobile DRAM Power-Saving Features and Calculations (PDF)	DRAM LPDRAM Mobile
(TN-46-12) This technical note addresses the power-saving features and power calculations of low- power Mobile LPDRAM memory.	Power Efficiency
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SEARCH (3) LPDRAM TECHNICAL NOTES

Parts with the same Data Sheet (5)

	EDFA232A2PB-GD-F (Current)	EDFA232A2PB-JD-F	EDF8132A3PB-GD-F	EDF8132A3PB-JD-F		
	Part Status Obsolete	Obsolete	Contact Factory	Obsolete		
Dow	Technology Arrow.com.	LPDDR3	LPDDR3	LPDDR3		

Density	16Gb	16Gb	8Gb	8Gb		
RoHS	Yes	Yes	Yes	Yes		
Depth	512Mb	512Mb	256Mb	256Mb		
Width	x32	x32	x32	x32		
Voltage	e 1.2V 1.2V		1.2V	1.2V		
Package	VFBGA	VFBGA	WFBGA	WFBGA		
Pin Count	216-ball	216-ball	216-ball	216-ball		
Clock Rate	800 MHz	933 MHz	800 MHz	933 MHz		
Data Rate	DDR3-1600	DDR3-1866	DDR3-1600	DDR3-1866		
	Cycle Time					
Op. Temp.	-30C to +85C	-30C to +85C	-30C to +85C	-30C to +85C		
Brand	Micron	Micron	Micron	Micron		

EDFA232A2PB-GD-F

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(Current)

Part Status	Obsolete	Where to Buy										
Technology	LPDDR3	Where to bay										
Density	16Gb											
RoHS	Yes	S	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part			
Depth	512Mb											
Width	x32	te	Dry Pack	N/A	N/A	N/A	No		N/A			
Voltage	1.2V	te	N/A									
Package	VFBGA											
Pin Count	216-ball											
Clock Rate	800 MHz		- OR - Check with Distributors									
Data Rate	DDR3-1600				Your Re	egion: Americas	6 -					
	Cycle Time											
Op. Temp.	-30C to +85C											
Brand	Micron											
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