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MT29F64G08CFAAAWC-IT

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Data Sheets (1)

Data Sheet 🔒

L63A datasheet 32Gb, 64Gb, 128Gb: NAND Flash (NDA)

L63A datasheetC

China
MLC NAND
NAND Flash
Taiwan
USA

File Type: PDF   **Updated:** 04/2009

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Specs

Orderable Parts for: MT29F64G08CFAAAWC-IT

SEE ALL MLC NAND PARTS

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F64G08CFAAAWC-IT:A	Obsolete	N/A	N/A	N/A	N/A	No		N/A

Detailed Specifications

Density	64Gb	Status	Obsolete	NAND Type	2D	RoHS	Yes
Width	x8	Voltage	3.3V	Package	TSOP	Pin Count	48-pin
Op. Temp.	-40C to +85C	MT/s		I/O	Common	Product Name	

Sim Models & Software

Sim Models

Verilog 🔒

Verilog: NAND 32/64/128Gb, MLC (rev A) L63A (ZIP)

MLC NAND
NAND Flash

32/64/128Gb Verilog Model Rev 7.26

File Type: ZIP   **Updated:** 08/02/2010

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IBIS 🔒

IBIS: NAND 32/64/128Gb MLC (Rev A) L63A (ZIP)

MLC NAND
NAND Flash

Rev. 2.2

File Type: ZIP   **Updated:** 11/03/2009

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HSpice 🔒

HSpice: NAND 32/64/128Gb MLC (Rev A) L63A (ZIP)

MLC NAND
NAND Flash

Rev. 2.2

File Type: ZIP   **Updated:** 11/03/2009

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Software

Software Support Pack

mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)

MLC NAND
NAND Flash

MLC NANDs have more bit flips that SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND
NAND Flash
SLC NAND

Reset NAND before write protect check.

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

fixup-ubi-cannot-recover-master node issue (ZIP)

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be dameged in one single master block.

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

UBI power loss issue for paired page Ver2.0 (ZIP)

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

YAFFS mark bad block issue (ZIP)

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP   **Updated:** 03/31/2016

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F64G08CFAAAWC-IT

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)   **Updated:** 05/2017

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F64G08CFAAAWC-IT

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)   **Updated:** 05/2017

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Documentation & Support

See All MLC NAND Documentation

Technical Notes

Technical Notes

TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

File Type: PDF   **Updated:** 01/25/2016

3D NAND
DDR3 SDRAM
DDR4 SDRAM
DRAM
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Technical Notes

TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

File Type: PDF   **Updated:** 02/14/2013

DDR SDRAM
DDR2 SDRAM
DDR3 SDRAM
DDR3L-RS
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Technical Notes

TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory (PDF)

(TN-29-65) Download the low-level driver described in this document here.

File Type: PDF   **Updated:** 10/15/2010

MLC NAND
NAND Flash
SLC NAND

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Technical Notes

TN-29-61: Wear Leveling in NAND Flash Memory (PDF)

(TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory.

File Type: PDF   **Updated:** 10/15/2010

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NAND Flash
SLC NAND

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Parts with the same Data Sheet (10)

MT29F64G08CFAAAWC-IT (Current)	MT29F128G08CJAAAWC	MT29F128G08CJAAAWC-IT	MT29F128G08CKAAAC5
Status — **Obsolete**	**Obsolete**	**Obsolete**	**Obsolete**
Density — **64Gb**	**128Gb**	**128Gb**	**128Gb**
NAND Type — **2D**	2D	2D	2D
RoHS — **Yes**	Yes	Yes	Yes
Width — **x8**	x8	x8	x8
Voltage — **3.3V**	3.3V	3.3V	3.3V
Package — **TSOP**	TSOP	TSOP	**VLGA**
Pin Count — **48-pin**	48-pin	48-pin	**52-pad**
Op. Temp. — **-40C to +85C**	**0C to +70C**	-40C to +85C	**0C to +70C**
MT/s			
I/O — **Common**	Common	Common	**Separate**
Product Name			

MT29F64G08CFAAAWC-IT
(Current)

Status	**Obsolete**
Density	**64Gb**
NAND Type	**2D**
RoHS	**Yes**
Width	**x8**
Voltage	**3.3V**
Package	**TSOP**
Pin Count	**48-pin**
Op. Temp.	**-40C to +85C**
MT/s	
I/O	**Common**
Product Name	

Where to Buy

Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
Obsolete	N/A	N/A	N/A	N/A	No		N/A

- OR - Check with Distributors

Your Region: Americas ▼

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