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MT29F512G08CUCDBJ6-6R

Data Sheets (2)

Data Sheet

Data Sheet 🔓
Updated 10/2014

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Data Sheet

Data Sheet 🔓
Updated 04/2014

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Specs

Orderable Parts for: MT29F512G08CUCDBJ6-6R

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F512G08CUCDBJ6-6R:D	Production	N/A	NW561	N/A	N/A	No		N/A

Detailed Specifications

Density	512Gb	Status	Production	RoHS	Yes	Width	x8
Voltage	3.3V	Package	LBGA	Pin Count	132-ball	MT/s	333 MT/s
I/O	Common	Product Name	FortisFlash				

RoHS Certificates

Title & Description	Updated	Type	
RoHS Certificate of Compliance (PDF) 🔓 Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.	04/2015	RoHS Certification	ADD EMAIL
China RoHS Certificate (PDF) 🔓 Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.	04/2015	RoHS Certification	ADD EMAIL

Documentation & Support

Technical Note (50)

Title & Description	Updated	Type		
NAND Flash Controller on Spartan-3 (pdf) This technical note describes the Micron NAND Flash controller, techniques for interfacing the NAND Flash device with a processor and use of the Micron glueless interface to interface a processor with NAND Flash memory. (TN-29-06)	07/2014	Technical Note	ADD	EMAIL
Technical Note: Using Chip Enable Pin Reduction in NAND Flash Memory Devices (pdf) 🔒 This technical note describes how to enable and use the chip enable pin reduction feature in Micron's NAND Flash memory devices and discusses reduction limitations based on NAND package and system configuration. (TN-29-77)	05/2014	Technical Note	ADD	EMAIL
SEMI Wafer Map Format (pdf) Micron has adopted the wafer map file format approved by Semiconductor Equipment and Materials International (SEMI). With SEMI formatting, Micron's customers can be confident they will always receive consistent, compatible, reliable map files. (TN-00-21)	03/2014	Technical Note	ADD	EMAIL
Technical note: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (pdf) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices. (TN-2976)	02/2014	Technical Note	ADD	EMAIL
Enabling On-Die ECC NAND with JFFS2 (pdf) This technical note addresses altering the the MTD/NAND layer for the on-die ECC NAND device. (TN-29-75)	08/2013	Technical Note	ADD	EMAIL

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Customer Service Note (5)

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Title & Description	Updated	Type	
Shipping Quantities (pdf) Provides standard part quantities for shipping. (CSN-04)	01/2015	Customer Service Note	ADD EMAIL
Micron BGA Manufacturer's User Guide (pdf) Provides information to enable customers to easily integrate both leading-edge and legacy Micron's ball grid array (BGA) packages into their manufacturing processes. It is intended as a set of high-level guidelines and a reference manual describing typical package-related and manufacturing process-flow practices. (CSN-33)	12/2014	Customer Service Note	ADD EMAIL
Micron Component and Module Packaging (pdf) Explanation of Micron packaging labels and procedures. (CSN-16)	11/2014	Customer Service Note	ADD EMAIL
Product Marks/Product and Packaging Labels (pdf) Explains product part marking, and product and packaging labels. (CSN-11)	10/2014	Customer Service Note	ADD EMAIL
PCN/EOL Systems (pdf) Explains Micron's product change notification and end-of-life systems. (CSN-12)	04/2012	Customer Service Note	ADD EMAIL

Tool (6)

Title & Description	Updated	Type	
FBGA Decoder Micron's FBGA Part Marking Decoder makes it easier to understand part marking.	12/2014	Tool	ADD EMAIL
PISMO2 Boards (html) The platform-independent storage module PISMO™ specifications provides a standard for convenient, interchangeable memory test boards. PISMO boards significantly reduce design inefficiencies, providing a huge benefit for the short development cycles of consumer products.	10/2008	Tool	ADD EMAIL
SLC, MLC, TLC – What's the Difference? In this 4-minute, whiteboard video, Director of NAND Strategic Marketing, Kevin Kilbuck, provides a brief description of the technical differences among SLC, MLC, and TLC.	01/0001	Tool	ADD EMAIL

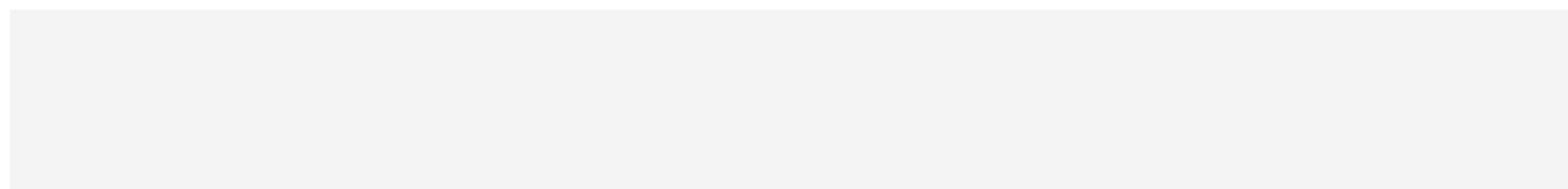|			
Choosing the Right NAND Flash memory manufacturers are offering a variety of NAND Flash memory products—including some specialized NAND solutions—with significantly different performance capabilities and features across a number of process nodes. This NAND diversification means that designers cannot simply select any NAND Flash device for their applications, but rather need to have a basic understanding of the features and benefits of each type of NAND in order to select both the proper device and the proper supplier.	01/0001	Tool	ADD EMAIL
ONFI Advantages The Open NAND Flash Interface (ONFI) is a collaboration among Micron and many other NAND Flash suppliers, controller manufacturers, and designers. The primary goal of the ONFI working group is to develop open standards for the interface used to communicate with NAND Flash memory —standards that will increase compatibility and interoperability among conforming NAND devices from different vendors. This, in turn, will increase the supply base for standard devices, reduce design time, and speed time to market.	01/0001	Tool	ADD EMAIL

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Parts with the same Data Sheet (6)

MT29F512G08CUCDBJ6-6R (Current)	MT29F64G08CBCDBJ4-6R	MT29F128G08CECDBJ4-6R	MT29F192G08CGCDBG1-6R
Production	**Production**	**Production**	**Contact Factory**
512Gb	**64Gb**	**128Gb**	**64Gb**
Yes	Yes	Yes	Yes
x8	x8	x8	x8

3.3V	3.3V	3.3V	3.3V
LBGA	**VBGA**	**VBGA**	**VBGA**
132-ball	132-ball	132-ball	**272-ball**
333 MT/s	333 MT/s	333 MT/s	333 MT/s
Common	Common	Common	Common
FortisFlash	FortisFlash	FortisFlash	FortisFlash

Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F512G08CUCDBJ6-6R:D	**Production**	N/A	NW561	N/A	N/A	No		N/A

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Supercomputing Memory

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NAND Flash

Managed NAND

NOR Flash

Hybrid Memory Cube

Multichip Packages

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