×

Home > Support > Design Resources & Documents > Evaluation/Development Tools

STR-NIS5020-GEVB: Strata Enabled NIS5020 12V, 14 m Ω , 10A eFuse

The STR-NIS5020-GEVB provides an evaluation board for the NIS5020 eFuse within the Strata Development Environment. The NIS5020 offers a programmable trip current limit, a programmable slew rate, and thermal protection features. The evaluation board provides an on board short circuit load to test the short circuit protection features of the eFuse. This evaluation board must be used with Strata to control the eFuse and monitor telemetry such as input/output voltage, input/output current, and temperature. See the Strata webpage at www.onsemi.com/strata for more details!



Features and Applications

Features

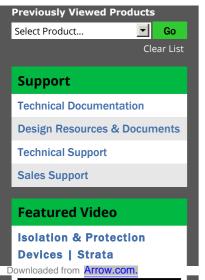
- Vin Range from 9.2V to 18V
- 2 independently controlled eFuses that can be placed in parallel
- Multiple overload current options
- Programmable slew rate

Applications

- Motherboards
- Hard Drives
- Servers
- Fan Drives

Evaluation/Development Tool Information							
Product	Status	Compliance	Short Description	Parts Used	Action		
STR-NIS5020-GEVB	Active	Pb-free	Strata Enabled NIS5020 12V, 14 mΩ, 10A eFuse	NIS5020MT1TXG	"Contact Local Sales Office		

Technical Documents					
Туре	Document Title	Document ID/Size	Rev		
Eval Board: BOM	STR-NIS5020-GEVB Bill of Materials (ROHS Compliant)	STR-NIS5020-GEVB_BOM_ROHS - 137 KB	1		
Eval Board: Gerber	STR-NIS5020-GEVB Gerber Layout Files (Zip Format)	STR-NIS5020-GEVB_GERBER - 427 KB	1		
Eval Board: Schematic	STR-NIS5020-GEVB Schematic	STR-NIS5020-GEVB_SCHEMATIC - 321 KB	1		
Video	Isolation & Protection Devices Strata	WVD17375/D			





Careers | Contact Us | Social Responsibility | Site Map | Subscribe



Copyright © 1999-2020 Semiconductor Components Industries, LLC | 沪ICP备18032505号 Do Not Sell My Personal Information | Privacy Policy | Cookie Policy | Terms of Use | Terms of Sale