

MT29F128G08CFAAWP-Z

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Data Sheet

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Orderable Part Information

Status	Contact Factory	Alternative Part	view
FBGA Code	N/A	SPD Data	N/A
MBQual Data	N/A	Shipping Media	N/A
PLP	No	Start Date	N/A

Specs

Density	128Gb	Status	Contact Factory
RoHS	Yes	Width	x8
Voltage	3.3V	Package	TSOP
Pin Count	48-pin	MT/s	
I/O	Common	Product Name	

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FAQs

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Date	What was added
01/2014	Datasheet: NAND 64/128/256/512Gb, MLC, Async/Sync, L74A
02/2011	HSpice: NAND 64/128/256/512Gb MLC (Rev A) L74A

FAQs

- » Do you support small block devices?
- » How much ECC do I need to support your devices?
- » I am using the correct amount of error correction code (ECC) for the NAND device, but I'm still seeing bit/byte errors in data I read back from the NAND device.
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Title & Description	Secure	ID	Updated
HSpice: NAND 64/128/256/512Gb MLC (Rev A) L74A Rev. 2.1		L74A	02/2011
IBIS: NAND 64/128/256/512Gb MLC (Rev A) L74A Rev. 2.1		L74A	02/2011

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- [+](#) Do you support small block devices?
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- [+](#) I am using the correct amount of error correction code (ECC) for the NAND device, but I'm still seeing bit/byte errors in data I read back from the NAND device.

- [+](#) How do I achieve greater PROGRAM/READ throughput for the NAND device?
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- [+](#) Where can I find simulation models for NAND Flash devices?
- [+](#) Why am I getting a bit/byte error reading back the information I programmed into the NAND device?
- [+](#) Why doesn't the NAND Flash device respond correctly to commands issued to it?
- [+](#) What is a "bank"?
- [+](#) What is the impedance tolerance of the driver in match-impedance mode relative to the expected value base on the perfect reference resistor connected to ZQ pin?
- [+](#) Does thermal information change for IT parts?
- [+](#) My design was based on a specification stating the JTAG was relative to VDD (1.8V), but now we've discovered that JTAG is actually relative to VDDQ (1.5V). It's a fairly significant board spin to change this: what do I risk by leaving the design as-is? I assume that the specification is still for VDDQ + 0.3V = 1.8V, but with CMOS parts there's no way I can guarantee that it won't swing past that on transitions.
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