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FAIRCHILD

SEMICONDUCTOR

November 1984 Revised September 2000

74F04 Hex Inverter

General Description

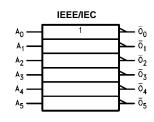
This device contains six independent gates, each of which performs the logic INVERT function.

Ordering Code:

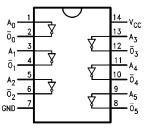
Order Number	Package Number	Package Description			
74F04SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow			
74F04SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide			
74F04PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide			

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Logic Symbol



Connection Diagram



Unit Loading/Fan Out

Pin Names	Description	U.L.	Input I _{IH} /I _{IL}	
Fill Names	Description	HIGH/LOW Output I _{OH} /I	Output I _{OH} /I _{OL}	
A _n	Inputs	1.0/1.0	20 µA/-0.6 mA	
Ōn	Outputs	50/33.3	-1 mA/20 mA	

74F04 Hex Inverter

74F04

Absolute Maximum Ratings(Note 1)

-65°C to +150°C
$-55^{\circ}C$ to $+125^{\circ}C$
$-55^{\circ}C$ to $+150^{\circ}C$
-0.5V to +7.0V
-0.5V to +7.0V
-30 mA to +5.0 mA
–0.5V to V _{CC}
-0.5V to +5.5V
twice the rated I_{OL} (mA)
4000V

Recommended Operating Conditions

Free Air Ambient	Temperature
Supply Voltage	

0°C to +70°C +4.5V to +5.5V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

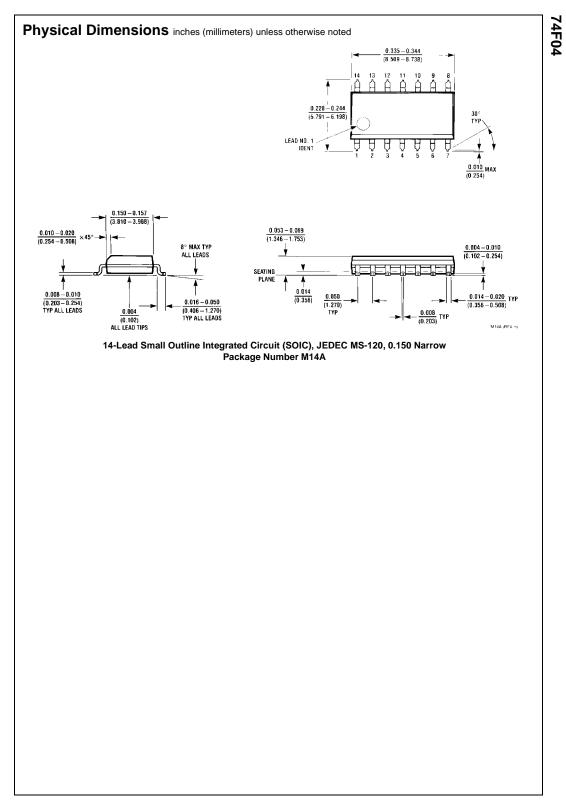
DC Electrical Characteristics

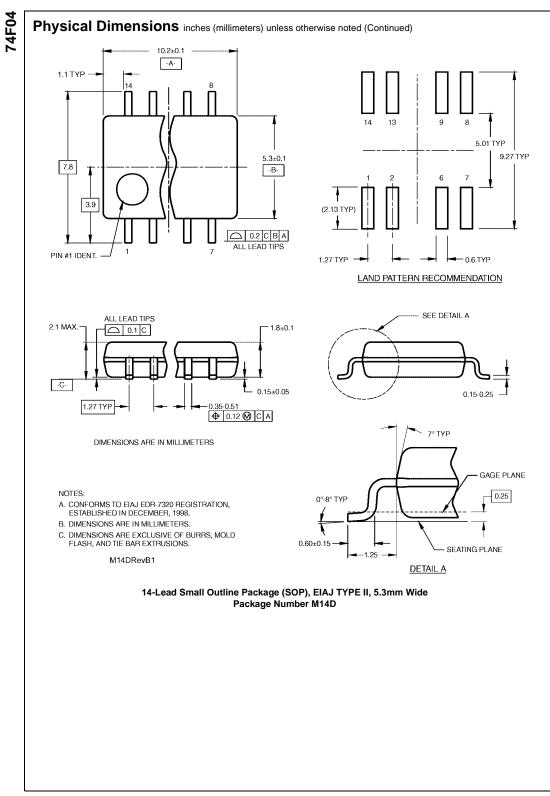
Symbol	Parameter	Min	Тур	Max	Units	Vcc	Conditions	
V _{IH}	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal	
VIL	Input LOW Voltage			0.8	V		Recognized as a LOW Signal	
V _{CD}	Input Clamp Diode Voltage			-1.2	V	Min	I _{IN} = -18 mA	
V _{OH}	Output HIGH 10% V _{CC} Voltage 5% V _{CC}	2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW 10% V _{CC} Voltage			0.5	v	Min	$I_{OL} = 20 \text{ mA}$	
IIH	Input HIGH Current			5.0	μΑ	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current Breakdown Test			7.0	μΑ	Max	V _{IN} = 7.0V	
ICEX	Output HIGH Leakage Current			50	μΑ	Max	V _{OUT} = V _{CC}	
V _{ID}	Input Leakage Test	4.75			v	0.0	$I_{ID} = 1.9 \ \mu A$ All other pins grounded	
I _{OD}	Output Leakage Circuit Current			3.75	μΑ	0.0	V _{IOD} = 150 mV All other pins grounded	
IIL	Input LOW Current			-0.6	mA	Max	$V_{IN} = 0.5V$	
I _{OS}	Output Short-Circuit Current	-60		-150	mA	Max	$V_{OUT} = 0V$	
ICCH	Power Supply Current		2.8	4.2	mA	Max	V _O = HIGH	
I _{CCL}	Power Supply Current		10.2	15.3	mA	Max	V _O = LOW	

AC Electrical Characteristics

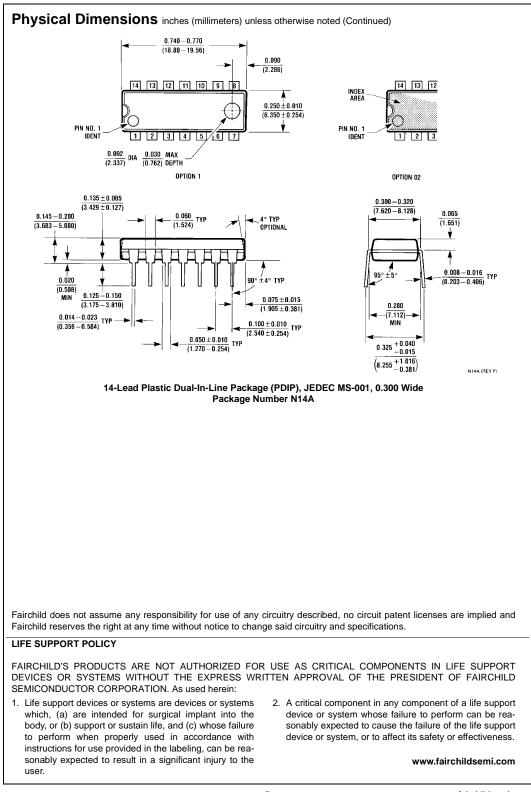
Symbol	Parameter	$T_{A} = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$			$T_A = -55^{\circ}C \text{ to } +125^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$		$T_A = 0^{\circ}C \text{ to } +70^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$		Units	
		Min	Тур	Max	Min	Max	Min	Max		
t _{PLH}	Propagation Delay	2.4	3.7	5.0	2.0	7.0	2.4	6.0		
t _{PHL}	A_n to \overline{O}_n	1.5	3.2	4.3	1.5	6.5	1.5	5.3	ns	

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