

MT29F128G08CECDBJ4-6R

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Orderable Part Information

Status	Production	Alternative Part	N/A
FBGA Code	NW558	SPD Data	N/A
MBQual Data	N/A	Shipping Media	N/A
PLP	No	Start Date	N/A

Specs

Density	128Gb	Status	Production
RoHS	Yes	Width	x8
Voltage	3.3V	Package	VBGA
Pin Count	132-ball	MT/s	333 MT/s
I/O	Common	Product Name	MLC+

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- + [My design was based on a specification stating the JTAG was relative to VDD \(1.8V\), but now we've discovered that JTAG is actually relative to VDDQ \(1.5V\). It's a fairly significant board spin to change this, what do I risk by leaving the design as-is? I assume that the specification is still for VDDQ + 0.3V = 1.8V, but with CMOS parts there's no way I can guarantee that it won't swing past that on transitions.](#)
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