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MT29F32G08CBACAWP-ITZ

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Data Sheets (1)

Data Sheet

L73A NAND datasheet

L73A NAND datasheet

China
MLC NAND
NAND Flash
Taiwan
USA

File Type: PDF **Updated:** 08/2015

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Specs

Orderable Parts for: MT29F32G08CBACAWP-ITZ

[SEE ALL MLC NAND PARTS](#)

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F32G08CBACAWP-ITZ:C	Contact Factory	N/A	N/A	N/A	N/A	No		VIEW

Detailed Specifications

Density	32Gb	Status	Contact Factory	NAND Type	2D	RoHS	Yes
Width	x8	Voltage	3.3V	Package	TSOP	Pin Count	48-pin
Op. Temp.	-40C to +85C	MT/s		I/O	Common	Product Name	

Sim Models & Software

Sim Models

HSpice



HSpice: NAND 32/64/128/256Gb MLC (Rev C) L73A (ZIP)

MLC NAND
NAND Flash

REV 2.1

File Type: ZIP

Updated: 01/13/2012

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IBIS



IBIS: NAND 32/64/128/256Gb MLC (Rev C) L73A (ZIP)

MLC NAND
NAND Flash

Rev. 2.1

File Type: ZIP

Updated: 01/13/2012

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Software

Software Support Pack

mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)

MLC NAND
NAND Flash

MLC NANDs have more bit flips than SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND
NAND Flash
SLC NAND

Reset NAND before write protect check.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

fixup-ubi-cannot-recover-master node issue (ZIP)

MLC NAND
NAND Flash

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be damaged in one single master block.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

UBI power loss issue for paired page Ver2.0 (ZIP)

MLC NAND
NAND Flash

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

YAFFS mark bad block issue (ZIP)

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

MLC NAND
NAND Flash
SLC NAND

File Type: ZIP

Updated: 03/31/2016

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F32G08CBACAWP-ITZ

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 11/2016

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F32G08CBACAWP-ITZ

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 11/2016

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Documentation & Support

[See All MLC NAND Documentation](#)

Technical Notes

SEARCH (4) MLC NAND TECHNICAL NOTES

Technical Notes

TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)

3D NAND
DDR3 SDRAM

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

File Type: PDF **Updated:** 01/25/2016

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Technical Notes

TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

DDR SDRAM
 DDR2 SDRAM
 DDR3 SDRAM
 DDR3L-RS
[See More Tags](#)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

File Type: PDF **Updated:** 02/14/2013

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Technical Notes

TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory (PDF)

MLC NAND
 NAND Flash
 SLC NAND

(TN-29-65) Download the low-level driver described in this document [here](#).

File Type: PDF **Updated:** 10/15/2010

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Technical Notes

TN-29-61: Wear Leveling in NAND Flash Memory (PDF)

MLC NAND
 NAND Flash
 SLC NAND

(TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory.

File Type: PDF **Updated:** 10/15/2010

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[SEARCH \(4\) MLC NAND TECHNICAL NOTES](#)

Parts with the same Data Sheet (4)

MT29F32G08CBACAWP-ITZ (Current)	MT29F32G08CBACAWP-Z	MT29F32G08CBCCBH1-12Z	MT29F32G08CBCCBH1-12ITZ
Status Contact Factory	Contact Factory	Contact Factory	Production
Density 32Gb	32Gb	32Gb	32Gb
NAND Type 2D	2D	2D	

RoHS	Yes	Yes	Yes	Yes
Width	x8	x8	x8	x8
Voltage	3.3V	3.3V	3.3V	3.3V
Package	TSOP	TSOP	VBGA	VBGA
Pin Count	48-pin	48-pin	100-ball	100-ball
Op. Temp.	-40C to +85C	0C to +70C	0C to +70C	-40C to +85C
	MT/s		166 MT/s	166 MT/s
I/O	Common	Common	Common	Common
Product Name				

Where to Buy

Orderable Parts


	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F32G08CBACAWP-ITZ:C	Contact Factory	N/A	N/A	N/A	N/A	No		VIEW


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