

EMIF01-TV01F3

Single line IPAD™, EMI filter and ESD protection

Features

- EMI symmetrical (I/O) low-pass filter
- High efficiency EMI filtering
- Lead-free package
- 400 µm pitch
- Very low PCB space occupation: 0.6 mm²
- Very thin package: 0.6 mm
- High reliability offered by monolithic integration
- Reduction of parasitic elements through CSP integration

Complies with the following standards

- IEC 61000-4-2 level 4 on internal and external pins:
 - 15 kV (air discharge)
 - 8 kV (contact discharge)
- MIL STD 883F Method 3015.7 Class 3

Application

■ TV analog signal in TV_OUT interface

Description

The EMIF01-TV01F3 is a highly integrated array designed to suppress EMI/RFI noise and provide impedance matching for mobile phone and portable applications. The EMIF01-TV01F3 is in a Flip Chip package to offer space saving and high RF performance.

This low pass filter includes ESD protection circuitry which prevents damage to the protected device when subjected to ESD surges up to 15 kV.

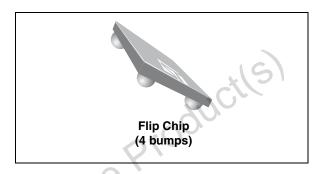


Figure 1. Pin configuration (bump side)

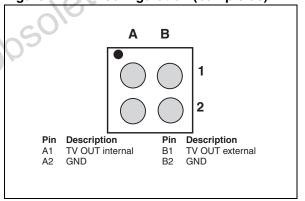
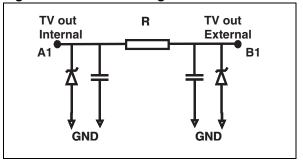


Figure 2. Device configuration



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Characteristics EMIF01-TV01F3

1 Characteristics

Table 1. Absolute maximum ratings

Symbol	Parameter and test conditions	Value	Unit
V _{PP}	Internal pins (A1) and external pin (B1): ESD discharge IEC 61000-4-2, air discharge	15	kV
	ESD discharge IEC 61000-4-2, contact discharge	8	
T _j	Maximum junction temperature	125	°C
T _{op}	Operating temperature range	-30 to +85	°C
Р	Maximum power dissipation	80	°C
T _{stg}	Storage temperature range	-55 to 150	°C

Table 2. Electrical characteristics $(T_{amb} = 25 \, ^{\circ}C)$

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Symbol	Parameters	S,				
V _{BR}	Breakdown voltage		' ↑	1		
I _{RM}	Leakage current @ V _{RM}		l _F			
V _{RM}	Stand-off voltage					
V _{CL}	Clamping voltage	Vcl V _{BR}	VDM	VF		
R _d	Dynamic impedance	VCL VBR VRM IRM V				
I _{PP}	Peak pulse current					
R _{I/O}	Series resistance between Input & Output	Slope = 1/Rd				
C _{line}	Input capacitance per line	ļ				
Symbol	Test conditions	Min	Тур	Max	Unit	
V_{BR}	I _R = 1 mA	6		8	V	
I _{RM}	V _{RM} = 3 V			0.2	μΑ	
R	Tolerance ± 5 %		75		Ω	
C _{line}	@ 0 V		30	35	pF	

EMIF01-TV01F3 Characteristics

Figure 3. S21 (db) attenuation measurement Figure 4. ESD response to IEC61000-4-2 (+15 kV air discharge)

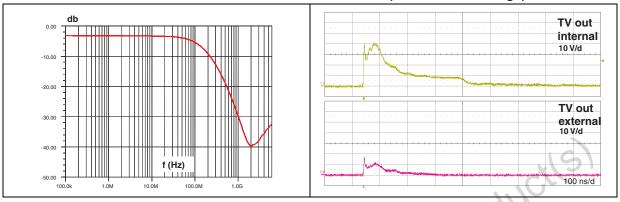
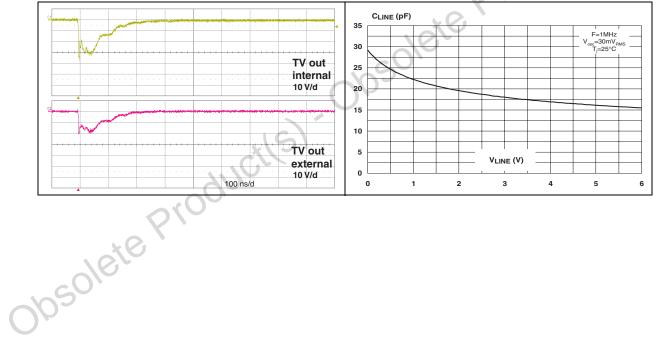


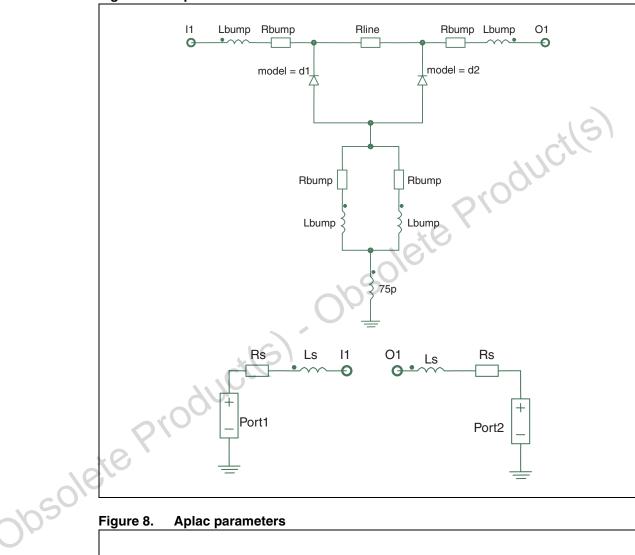
Figure 5. ESD response to IEC61000-4-2 (-15 kV air discharge)

Figure 6. Line capacitance versus applied voltage



Application information 2

Figure 7. Aplac model



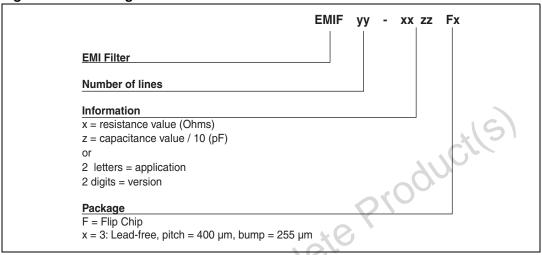
Aplac parameters

Variables aplacvar Rline 75 aplacvar C_d1 17.5p aplacvar C_d2 17.5p aplacvar Ls 950pH aplacvar Rs 150m aplacvar Lbump 96pH aplacvar Rbump 20m aplacvar Lgnd 75pH	Diode D1 BV=7 IBV=1m CJO=C_d1 M=0.28 RS=0.48 VJ=0.6 TT=100n	Diode D2 BV=7 IBV=1m CJO=C_d2 M=0.28 RS=0.7 VJ=0.6 TT=100n
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3 Ordering information scheme

Figure 9. Ordering information scheme



4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Figure 10. Package dimensions

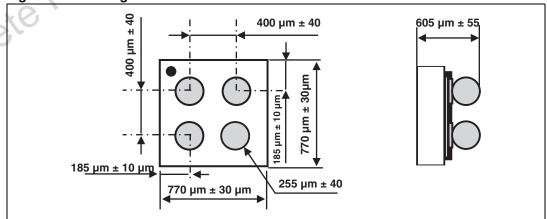


Figure 11. Footprint

Figure 12. Marking

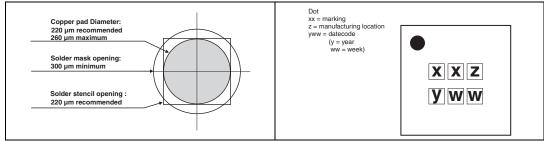
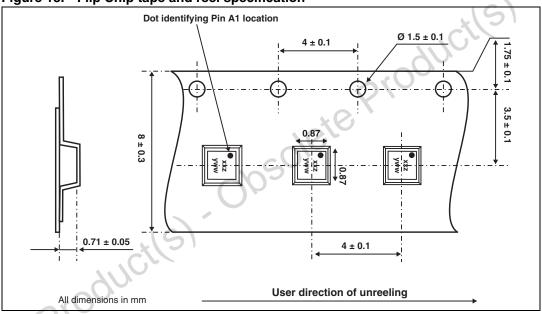


Figure 13. Flip Chip tape and reel specification



Note:

More information is available in the application notes:

AN2348: "STMicroelectronics 400 micro-metre Flip Chip: Package description and recommendation for use"

AN1751: "EMI filters: Recommendations and measurements"

5 Ordering information

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF01-TV01F3	HC	Flip Chip	0.79 mg	5000	Tape and reel 7"

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EMIF01-TV01F3 Revision history

6 Revision history

Table 4. Document revision history

	Date	Revision	Changes
	09-Feb-2006	1	Initial release.
	28-Apr-2008	2	Updated ECOPACK statement. Updated <i>Figure 9</i> , <i>Figure 10</i> , and <i>Figure 13</i> . Reformatted to current standards.
	26-May-2011	3	Updated figure in <i>Table 2</i> . Updated ECOPACK statement.
Obsole	ie Pro	duc	Updated figure in Table 2. Updated ECOPACK statement.

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