



NCP362GEVB: USB Positive OVP Evaluation Board

The NCP362 disconnects systems at its output when wrong VBUS operating conditions are detected at its input. The system is positive overvoltage protected up to +20 V, overcurrent protected up to 750 mA, and receives protection from ESD diodes for the high speed USB data and VBUS lines. Thanks to an integrated PMOS FET, no external device is necessary, reducing the system cost and the PCB area of the application board.

The NCP362 is able to instantaneously disconnect the output from the input if the input voltage exceeds the overvoltage threshold OVLO. Thanks to an overcurrent protection, the integrated PMOS turns off when the charge current exceeds the current limit (see options in ordering information).

The NCP362 provides a negative going flag (FLAG) output, which alerts the system that voltage, current or overtemperature faults have occurred.

In addition, the device integrates ESD diodes for VBUS and data lines which are IEC61000-4-2, level 4 compliant. The ESD diodes for D+ and D- are compatible with high speed USB thanks to an ultra low capacitance of 0.5 pF.



Features and Applications

Features

- Overvoltage Protection up to 20 V
- Undervoltage and Overvoltage Lockout (UVLO/OVLO)
- Overcurrent Protection
- Transient Voltage Suppressor for VBUS Pin
- Ultra Low Capacitance ESD for Data Lines
- Alert FLAG Output and EN Enable Pin
- Thermal Shutdown
- Compliance to IEC61000-4-2 (Level 4)
- Compliance Machine Model and Human Body Model
- 10 Lead UDFN 2x2.5 mm Package
- This is a Pb-Free Device
- Peripheral

Applications

- USB Devices
- Mobile Phones
- Personal Digital Assistant
- MP3/MP4 Players
- TV and Set Top Boxes

Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
NCP362GEVB	Active	Pb-free	USB Positive OVP Evaluation Board	NCP362CMUTBG	» Contact Local Sales Office

Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCP362GEVB Bill of Materials ROHS Compliant	NCP362GEVB_BOM_ROHS.PDF - 119.0 KB	0
Eval Board: Gerber	NCP362GEVB Gerber Layout Files (Zip Format)	NCP362GEVB_GERBER.ZIP - 42.0 KB	0
Eval Board: Schematic	NCP362GEVB Schematic	NCP362GEVB_SCHEMATIC.PDF - 121.0 KB	0
Eval Board: Test Procedure	NCP362GEVB Test Procedure	NCP362GEVB_TEST_PROCEDURE.PDF - 141.0 KB	0

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