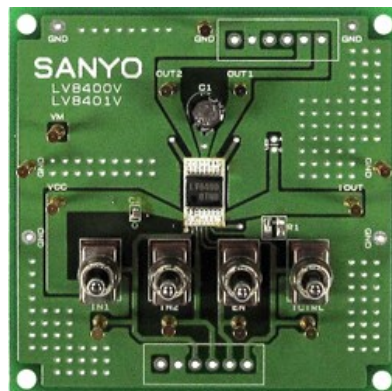




## LV8400VEVB: 1-Channel Motor Driver IC Evaluation Board

The LV8400V is a 1-channel motor driver IC using D-MOS FET for output stage and is able to control 4 modes of forward, reverse, brake, and standby. As the P/N-channel structure is used in the H-bridge output stage, the LV8400V features minimal number of external component and low on-resistance (0.33 Ohms typical). This IC is optimal for driving motors that requires high current.



### Features and Applications

#### Features

- 1-channel forward/reverse motor driver
- Low power consumption
- Low output ON resistance 0.33 Ohms
- Built-in constant current output circuit
- Built-in low voltage reset and thermal shutdown circuit
- Four mode function forward/reverse, brake, standby

### Evaluation/Development Tool Information

| Product    | Status | Compliance | Short Description                          | Parts Used    | Action  |
|------------|--------|------------|--|---------------|---|
| LV8400VEVB | Active |            | 1-Channel Motor Driver IC Evaluation Board | LV8400V-TLM-E | <a href="#">» Contact Local Sales Office</a><br><a href="#">» Inventory</a> |

### Technical Documents

| Type                       | Document Title                              | Document ID/Size                       | Rev |
|----------------------------|---|--|-----|
| Eval Board: BOM            | LV8400VEVB Bill of Materials                | LV8400VEVB_BOM.PDF - 60 KB             | 0   |
| Eval Board: Gerber         | LV8400VEVB Gerber Layout Files (Zip Format) | LV8400VEVB_GERBER.ZIP - 96.0 KB        | 0   |
| Eval Board: Schematic      | LV8400VEVB Schematic                        | LV8400VEVB_SCHEMATIC.PDF - 142 KB      | 0   |
| Eval Board: Test Procedure | LV8400VEVB Test Procedure                   | LV8400VEVB_TEST_PROCEDURE.PDF - 118 KB | 1   |

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