DUSEU

Dual Inverter

Features

MARKING

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DIAGRAMS **NL27WZ04** 6 ппп SC-88 XXXM • The NL27WZ04 is a high performance dual inverter operating from **DF SUFFIX** a 1.65 to 5.5 V supply. CASE 419B 0 • Designed for 1.65 V to 5.5 V V_{CC} Operation SC-74 • 2.0 ns t_{PD} at $V_{CC} = 5 V (Typ)$ XXX M= CASE 318F-05 • Inputs/Outputs Overvoltage Tolerant up to 5.5 V • IOFF Supports Partial Power Down Protection • Sink 32 mA at 4.5 V • Available in SC-88, SC-74 and UDFN6 Packages TSOP-6 XXX M= • Chip Complexity < 100 FETs CASE 318G-02 • NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable UDFN6 • These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS 1.45 x 1.0 CASE 517AQ Compliant UDFN6 1 1.0 x 1.0 A1 Y1 CASE 517BX

A2 1 Y2

Figure 1. Logic Symbol

ΧМ

XM

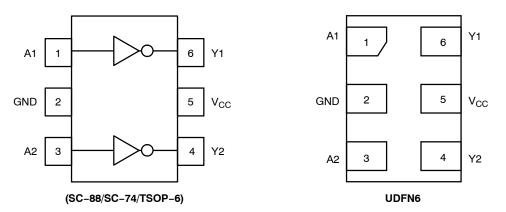
- X, XXX = Specific Device Code
- Μ = Date Code*
- = Pb-Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or position may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

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PIN ASSIGNMENT

Pin	Function
1	A1
2	GND
3	A2
4	Y2
5	V _{CC}
6	Y1

FUNCTION TABLE

A Input	Y Output
L	Н
Н	L

MAXIMUM RATINGS

Symbol	Chara	Value	Unit	
V_{CC}	DC Supply Voltage	SC-88 (NLV) SC-88, SC-74, UDFN6	−0.5 to +7.0 −0.5 to +6.5	V
V _{IN}	DC Input Voltage	−0.5 to +7.0 −0.5 to +6.5	V	
V _{OUT}	DC Output Voltage SC-88 (NLV)	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +7.0 -0.5 to +7.0	V
	DC Output Voltage SC-88, SC-74, UDFN6	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +6.5 -0.5 to +6.5	V
I _{IK}	DC Input Diode Current	V _{IN} < GND	-50	mA
I _{ОК}	DC Output Diode Current	V _{OUT} < GND	-50	mA
I _{OUT}	DC Output Source/Sink Current	±50	mA	
I_{CC} or I_{GND}	DC Supply Current per Supply Pin or	±100	mA	
T _{STG}	Storage Temperature Range		-65 to +150	°C
TL	Lead Temperature, 1 mm from Case f	or 10 secs	260	°C
TJ	Junction Temperature Under Bias		+150	°C
θ_{JA}	Thermal Resistance (Note 2)	SC-88 SC-74 UDFN6	377 320 154	°C/W
P _D	Power Dissipation in Still Air	SC-88 SC-74 UDFN6	332 390 812	mW
MSL	Moisture Sensitivity		Level 1	-
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V _{ESD}	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model (NLV) Charged Device Model	2000 1000 N/A	V
I _{Latchup}	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 Applicable to devices with outputs that may be tri-stated.
 Measured with minimum pad spacing on an FR4 board, using 10mm-by-1inch, 2 ounce copper trace no air flow per JESD51-7.
 HBM tested to ANSI/ESDA/JEDEC JS-001-2017. CDM tested to EIA/JESD22-C101-F. JEDEC recommends that ESD qualification to EIA/JESD22-A115-A (Machine Model) be discontinued per JEDEC/JEP172A.

4. Tested to EIA/JESD78 Class II.

RECOMMENDED OPERATING CONDITIONS

Symbol	Cha	racteristics	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage		1.65	5.5	V
V _{IN}	DC Input Voltage		0	5.5	V
V _{OUT}	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	0 0 0	V _{CC} 5.5 5.5	
T _A	Operating Temperature Range		-55	+125	°C
t _r , t _f	Input Rise and Fall Time	$\begin{array}{l} {\sf V}_{\rm CC} = 1.65 \; {\sf V} \; {\rm to} \; 1.95 \; {\sf V} \\ {\sf V}_{\rm CC} = 2.3 \; {\sf V} \; {\rm to} \; 2.7 \; {\sf V} \\ {\sf V}_{\rm CC} = 3.0 \; {\sf V} \; {\rm to} \; 3.6 \; {\sf V} \\ {\sf V}_{\rm CC} = 4.5 \; {\sf V} \; {\rm to} \; 5.5 \; {\sf V} \end{array}$	0 0 0	20 20 10 5	ns

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

			V _{cc}	Т	T _A = 25°C			Γ _A ≤ 125°C	
Symbol	Parameter	Condition	(V)	Min	Тур	Max	Min	Max	Units
V _{IH}	High-Level Input		1.65 to 1.95	$0.75 \times V_{CC}$	-	-	$0.75 \times V_{CC}$	-	V
	Voltage (NLV)		2.3 to 5.5	$0.70 \times V_{CC}$	-	-	$0.70 \times V_{CC}$	_	1
	High-Level Input		1.65 to 1.95	$0.65 \times V_{CC}$	-	_	$0.65 \times V_{CC}$	_	V
	Voltage		2.3 to 5.5	$0.70 \times V_{CC}$	-	-	$0.70 \times V_{CC}$	_	
V _{IL}	Low-Level Input		1.65 to 1.95	-	-	$0.25 \times V_{CC}$	-	$0.25 \times V_{CC}$	V
	Voltage (NLV)		2.3 to 5.5	-	-	$0.30 \times V_{CC}$	-	$0.30 \times V_{CC}$	
	Low-Level Input		1.65 to 1.95	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	V
	Voltage		2.3 to 5.5	-	-	$0.30 \times V_{CC}$	-	$0.30 \times V_{CC}$	
V _{OH}	High-Level Output Voltage	$\begin{array}{l} V_{IN} = V_{IH} \mbox{ or } V_{IL} \\ l_{OH} = -100 \ \mu A \\ l_{OH} = -3 \ m A \\ l_{OH} = -8 \ m A \\ l_{OH} = -12 \ m A \\ l_{OH} = -16 \ m A \\ l_{OH} = -24 \ m A \\ l_{OH} = -32 \ m A \end{array}$	1.65 to 5.5 1.65 2.3 2.7 3 3 4.5	V _{CC} - 0.1 1.29 1.9 2.2 2.4 2.3 3.8	V _{CC} 1.52 2.1 2.4 2.7 2.5 4	- - - - -	V _{CC} - 0.1 1.29 1.9 2.2 2.4 2.3 3.8	- - - - - -	V
V _{OL}	Low-Level Output Voltage		1.65 to 5.5 1.65 2.3 2.7 3 3 4.5	- - - - -	- 0.08 0.12 0.2 0.24 0.26 0.31	0.1 0.24 0.3 0.4 0.4 0.55 0.55	- - - - -	0.1 0.24 0.3 0.4 0.4 0.55 0.55	V
I _{IN}	Input Leakage Current	V _{IN} = 5.5 V or GND	1.65 to 5.5	-	_	±0.1	-	±1.0	μΑ
I _{OFF}	Power Off Leakage Current	V _{IN} = 5.5 V or V _{OUT} = 5.5 V	0	_	_	1.0	_	10	μΑ
I _{CC}	Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5	-	-	1.0	-	10	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

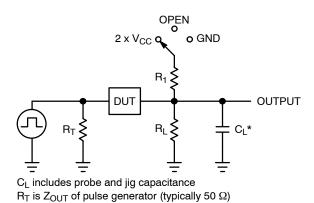
AC ELECTRICAL CHARACTERISTICS

				T _A = 25°C		– 55°C ≤ 1			
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Units
t _{PLH,}	Propagation Delay	R_L = 1 MΩ, C_L = 15 pF	1.65 to 1.95	-	2.3	9.2	-	11.0	ns
t _{PHL}	A to Y (Figure 3 and 4)	R_L = 1 MΩ, C_L = 15 pF	2.3 to 2.7	-	3.0	5.1	-	5.6	
	· • /	$R_L = 1 M\Omega$, $C_L = 15 pF$	3.0 to 3.6	-	2.2	3.4	-	3.8	
		R_L = 500 Ω , C_L = 50 pF		-	2.9	4.5	-	5.0	
		R_L = 1 MΩ, C_L = 15 pF	4.5 to 5.5	-	1.8	2.8	-	3.1	
		R_L = 500 Ω , C_L = 50 pF		-	2.3	3.6	-	4.0	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Units
C _{IN}	Input Capacitance	V_{CC} = 5.5 V, V_{IN} = 0 V or V_{CC}	2.5	pF
C _{OUT}	Output Capacitance	V_{CC} = 5.5 V, V_{IN} = 0 V or V_{CC}	4.0	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	10 MHz, V _{CC} = 5.5 V, V _{IN} = 0 V or V _{CC}	4.0	pF

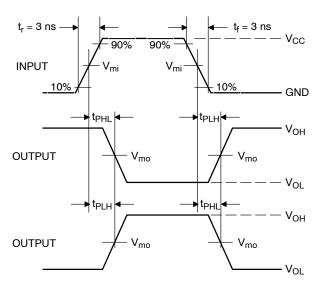
5. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$. C_{PD} is used to determine the no–load dynamic power consumption; $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$.



Test	Switch Position	C _L , pF	R_L, Ω	R ₁ , Ω		
t _{PLH} / t _{PHL}	Open	See AC Characteristics Table				
t _{PLZ} / t _{PZL}	$2 \times V_{CC}$	50	500	500		
t _{PHZ} / t _{PZH}	GND	50	500	500		
X = Don't Car	e					

Figure 3. Test Circuit

f = 1 MHz



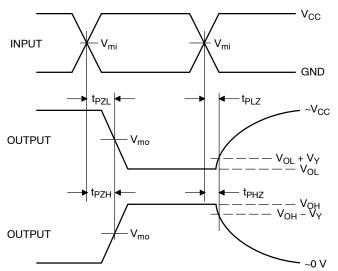


Figure 4. Switching Waveforms

		Vm		
V _{CC} , V	V _{mi} , V	t _{PLH} , t _{PHL}	t _{PZL} , t _{PLZ} , t _{PZH} , t _{PHZ}	V _Y , V
1.65 to 1.95	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.15
2.3 to 2.7	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.15
3.0 to 3.6	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.3
4.5 to 5.5	V _{CC} /2	V _{CC} /2	V _{CC} /2	0.3

DEVICE ORDERING INFORMATION

Device	Packages	Specific Device Code	Pin 1 Orientation (See below)	Shipping [†]
NL27WZ04DFT2G	SC-88	M5	Q4	3000 / Tape & Reel
NL27WZ04DFT2G-L22348**	SC-88	M5	Q4	3000 / Tape & Reel
NLV27WZ04DFT2G*	SC-88	M5	Q4	3000 / Tape & Reel
NL27WZ04DFT1G	SC-88	M5	Q2	3000 / Tape & Reel
NLV27WZ04DFT1G*	SC-88	M5	Q2	3000 / Tape & Reel
NL27WZ04DBVT1G	SC-74	M5	Q4	3000 / Tape & Reel
NL27WZ04DTT1G**	TSOP-6	M5	Q4	3000 / Tape & Reel
NL27WZ04MU1TCG (In Development)	UDFN6, 1.45 x 1.0, 0.5P	2 (Rotated 90° CW)	Q4	3000 / Tape & Reel
NL27WZ04MU3TCG (In Development)	UDFN6, 1.0 x 1.0, 0.35P	E (Rotated 180° CW)	Q4	3000 / Tape & Reel

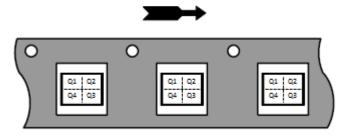
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

**Please refer to NLV specifications for this device.

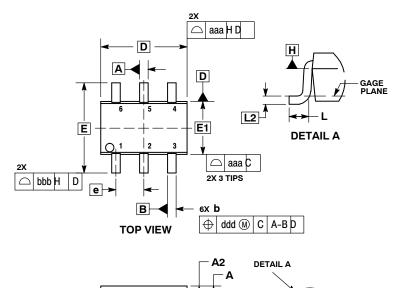
Pin 1 Orientation in Tape and Reel

Direction of Feed



PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363 CASE 419B-02 **ISSUE Y**



NOTES:

С

END VIEW

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRU-SIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
 4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
 5. DATUMS A AND B ARE DETERMINED AT DATUM H.
 6. DIMENSIONS & AND c. APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
 7. DIMENSION & DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION & AT MAXIMUM MATERIAL CONDI-TION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT. RADIUS OF THE FOOT.

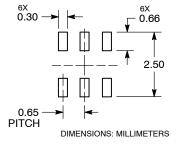
	MILLIMETERS				INCHES	3
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α			+ +.10			0.043
A1	0.00		0.10	0.000		0.004
A2	0.70	0.90	1.00	0.027	0.035	0.039
b	0.15	0.20	0.25	0.006	0.008	0.010
С	0.08	0.15	0.22	0.003	0.006	0.009
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	2.00	2.10	2.20	0.078	0.082	0.086
E1	1.15	1.25	1.35	0.045	0.049	0.053
e	(0.65 BS	С	0.026 BSC		
Г	0.26	0.36	0.46	0.010	0.014	0.018
L2		0.15 BS	SC	(0.006 BS	SC
aaa	0.15				0.006	
bbb	0.30			0.012		
ccc	0.10			0.004		
ddd		0.10			0.004	
ddd		0.10			0.004	

RECOMMENDED **SOLDERING FOOTPRINT***

SIDE VIEW

A1

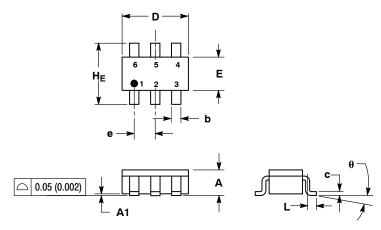
бX 🗀 ССС С



*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

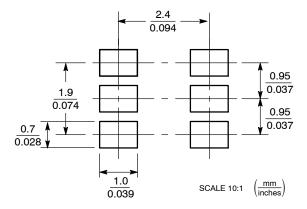
SC-74 CASE 318F-05 **ISSUE N**



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 4. 318F-01, -02, -03, -04 OBSOLETE. NEW STANDARD 318F-05.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
С	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
Е	1.30	1.50	1.70	0.051	0.059	0.067
е	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	_	10°	0°	-	10°
	0-	-	10-	0-	-	10-

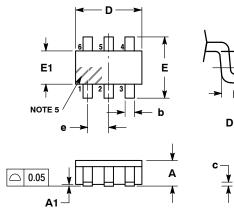
SOLDERING FOOTPRINT*

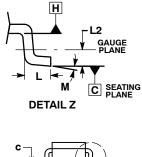


^{*}For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TSOP-6 CASE 318G-02 **ISSUE V**





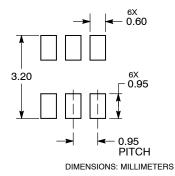
DETAIL Z

NOTES:

- 1. 2. 3.
- TES: DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSIONS D AND E1 ABE DETERMINED AT DATIM H 4. AND E1 ARE DETERMINED AT DATUM H. 5. PIN ONE INDICATOR MUST BE LOCATED IN THE INDICATED ZONE.

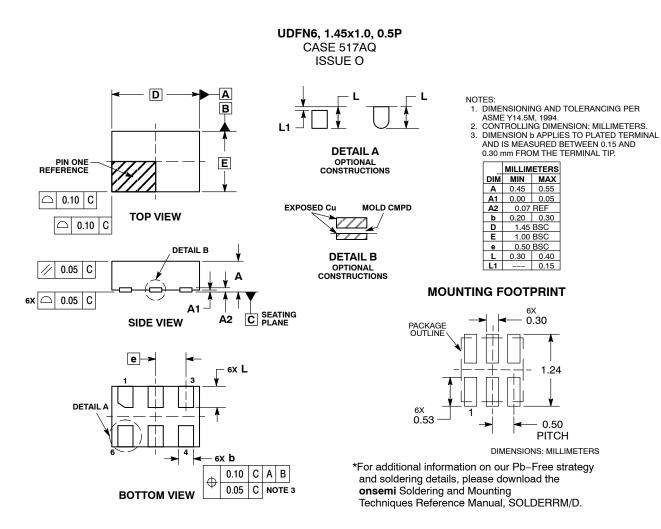
	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.90	1.00	1.10
A1	0.01	0.06	0.10
b	0.25	0.38	0.50
С	0.10	0.18	0.26
D	2.90	3.00	3.10
Е	2.50	2.75	3.00
E1	1.30	1.50	1.70
е	0.85	0.95	1.05
L	0.20	0.40	0.60
L2	0.25 BSC		
М	0°	-	10°

RECOMMENDED **SOLDERING FOOTPRINT***

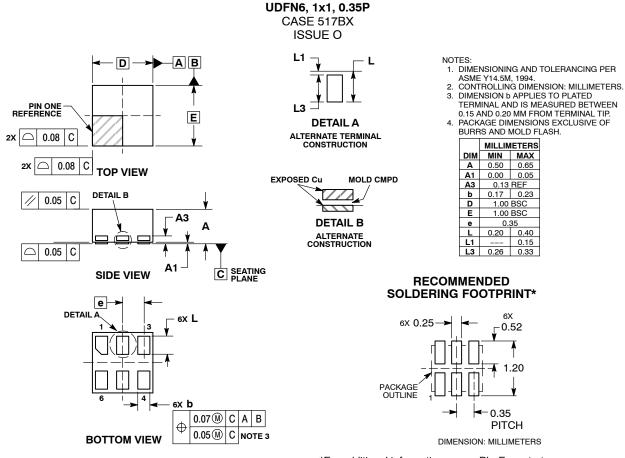


*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS



PACKAGE DIMENSIONS



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TECHNICAL SUPPORT

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North American Technical Support: Voice Mail: 1 800–282–9855 Toll Free USA/Canada Phone: 011 421 33 790 2910 Europe, Middle East and Africa Technical Support: Phone: 00421 33 790 2910 For additional information, please contact your local Sales Representative

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