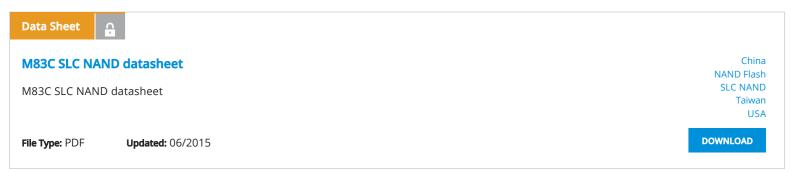


## Data Sheets (1)



# Specs

## Orderable Parts for: MT29F32G08ABEDBJ4-12

**SEE ALL SLC NAND PARTS FBGA SPD** Chipset Start Alternative Media PI P Data **Validation** Code Date Part MT29F32G08ABEDBJ4-NW642 N/A N/A N/A No N/A 12:D

## **Detailed Specifications**

Density	32Gb	Status	Contact Factory	RoHS	Yes	Width	x8
Voltage	3.3V	Package	VBGA	Pin Count	132-ball	MT/s	166 MT/s
1/0	Common	Op Temp	0C to +70C				

## Sim Models & Software

#### Sim Models





#### Verilog: NAND 32Gb to 256Gb Synchronous NAND SLC M83C (ZIP)

NAND Flash

32/256 Gb Verilog Model Rev 7.30

File Type: ZIP

**Updated:** 03/06/2017

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#### IBIS: 32Gb 64Gb 128Gb 256Gb Async Sync NAND (ZIP)

NAND Flash SLC NAND

M83C, Rev 2.0

File Type: ZIP

**Updated:** 01/30/2014

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#### HSpice: 32Gb 64Gb 128Gb 256Gb Async Sync NAND (ZIP)

NAND Flash SLC NAND

M83C, Rev 2.0

File Type: ZIP

**Updated:** 01/30/2014

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### Software

#### **Software Support Pack**

#### **NAND FLASH lockunlock bug fix (ZIP)**

MLC NAND NAND Flash SLC NAND

Reset NAND before write protect check.

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File Type: ZIP

**Updated:** 03/31/2016

#### **Software Support Pack**

#### YAFFS mark bad block issue (ZIP)

MLC NAND NAND Flash SLC NAND

If the bitflips num over mtd->bitflip\_threshold the mtd\_read\_oob will return -EUCLEAN and tags->ecc\_result > YAFFS\_ECC\_RESULT\_NO\_ERROR. Then we will call yaffs\_handle\_chunk\_error.

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File Type: ZIP

**Updated:** 03/31/2016





Downloaded from Arrow.com.

#### **NANDcode Flash Translation Layer (ZIP)**

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

File Type: ZIP

**Updated:** 08/08/2014

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NAND Flash

**SLC NAND** 

**Software Driver** 

# MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)

NAND Flash SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver here.

File Type: ZIP

**Updated:** 04/05/2013

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**Software Driver** 



MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)

NAND Flash Serial NAND SLC NAND

M68A, M69A, M60A

File Type: ZIP

**Updated:** 12/28/2011

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## **RoHS Certificates**

**RoHS Certificates** 

#### **RoHS Certificate of Compliance (PDF)**

MT29F32G08ABEDBJ4 12

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

**Updated:** 04/2017

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**RoHS Certificates** 

#### China RoHS Certificate (PDF)

MT29F32G08ABEDBJ4 12

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

**Updated:** 04/2017

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#### **Technical Notes**

#### **SEARCH (17) SLC NAND TECHNICAL NOTES**

**Technical Notes** 



#### TN-13-49: SLC NAND Device Refresh (PDF)

NAND Flash SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF

**Updated:** 10/31/2016

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**Technical Notes** 



#### TN-29-58: ONFI NV-DDR2 Design Guide (PDF)

NAND Flash SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF

**Updated:** 09/29/2016

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#### **Technical Notes**

#### TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)

NAND Flash SLC NAND

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF

**Updated:** 08/16/2016

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#### **Technical Notes**

# TN-26-03: How to Read, Program, and Manage Small Page NAND OTP Memory Area (PDF)

NAND Flash SLC NAND

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF

**Updated:** 08/16/2016

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#### **Technical Notes**

#### TN-21-27: How to Read the Unique Identifier in Small Page NAND Memory (PDF)

NAND Flash SLC NAND

(TN-21-27) This technical note explains how to read the unique ID in small page NAND memory.

Downloaded from Arrow.com.

File Type: PDF Updated: 08/16/2016

#### **SEARCH (17) SLC NAND TECHNICAL NOTES**

# Parts with the same Data Sheet (3)

MT29F32G08ABEDBJ4-12 ( Current )		MT29F64G08AEEDBJ4-12	MT29F128G08AMEDBJ5-12	MT29F256G08AUEDBJ6-12	
Status	Contact Factory	Contact Factory	Contact Factory	Contact Factory	
Density	32Gb	64Gb	128Gb	256Gb	
RoHS	Yes	Yes	Yes	Yes	
Width	х8	x8	x8	x8	
Voltage	3.3V	3.3V	3.3V	3.3V	
Package	VBGA	VBGA	TBGA	LBGA	
Pin Count	132-ball	132-ball	132-ball	132-ball	
MT/s	166 MT/s	166 MT/s	166 MT/s	166 MT/s	
I/O	Common	Common	Common	Common	
Op Temp	0C to +70C	0C to +70C	0C to +70C	0C to +70C	

### MT29F32G08ABEDBJ4-12

(Current)

Status	Contact Factory	
Density	32Gb	
RoHS	Yes	
Width	<b>x8</b>	9
Voltage	3.3V	
Package	VBGA	F
Pin Count	132-ball	
MT/s	166 MT/s	
I/O	Common	

# Where to Buy

Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
Contact Factory	N/A	NW642	N/A	N/A	No		N/A

- OR - Check with Distributors

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Op Temp 0C to +70C Your Region: Americas **VIEW** WDVN

**See All Distributors** 

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