



Products >

Solutions ∨

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Q Search

Home > Memory and Storage Products > MT29F64G08AFAAAWP-ITZ

## MT29F64G08AFAAAWP-ITZ

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**SPECS** 

SIM MODELS & **SOFTWARE** 

**ROHS CERTIFICATES**  **DOCUMENTATION** & SUPPORT

PARTS WITH THE SAME DATA SHEET (12)

WHERE TO BUY

# Data Sheets (1)

**Data Sheet** 

Datasheet: 32/64/128/256Gb Async/Sync NAND (M73A)

NAND Flash **SLC NAND** 

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File Type: PDF

**Updated:** 08/2016

# Specs

Orderable Parts for: MT29F64G08AFAAAWP-ITZ

**SEE ALL SLC NAND PARTS** 

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F64G08AFAAAWP- ITZ:A	Production	N/A	N/A	N/A	N/A	No		N/A

## **Detailed Specifications**

Density	64Gb	Status	Production	RoHS	Yes	Width	x8
Voltage	3.3V	Package	TSOP	Pin Count	48-pin	MT/s	
I/O	Common	Op Temp	-40C to +85C				

## Sim Models

IBIS



#### IBIS: NAND 32/64/128/256Gb SLC (Rev A) M73A (ZIP)

NAND Flash SLC NAND

Rev. 2.2

File Type: ZIP

**Updated:** 11/18/2016

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## HSpice: NAND 32/64/128/256Gb SLC (Rev A) M73A (ZIP)

NAND Flash SLC NAND

Rev. 2.0

File Type: ZIP

**Updated:** 09/20/2010

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## Software

**Software Support Pack** 

## NAND FLASH lockunlock bug fix (ZIP)

MLC NAND NAND Flash SLC NAND

Reset NAND before write protect check.

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**File Type:** ZIP **Updated:** 03/31/2016

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**Software Support Pack** 

#### YAFFS mark bad block issue (ZIP)

MLC NAND NAND Flash SLC NAND

If the bitflips num over mtd->bitflip\_threshold the mtd\_read\_oob will return -EUCLEAN and tags->ecc\_result > YAFFS\_ECC\_RESULT\_NO\_ERROR. Then we will call yaffs\_handle\_chunk\_error.

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**File Type:** ZIP **Updated:** 03/31/2016

**Software Support Pack** 



#### **NANDcode Flash Translation Layer (ZIP)**

NAND Flash SLC NAND

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

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File Type: ZIP

**Updated:** 08/08/2014

**Software Driver** 

# MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)

NAND Flash SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver here.

Downloaded from **Arrow.com**.

File Type: ZIP Updated: 04/05/2013

**Software Driver** 



MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)

NAND Flash Serial NAND SLC NAND

M68A, M69A, M60A

File Type: ZIP

**Updated:** 12/28/2011

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## **RoHS** Certificates

### **RoHS Certificates**

### **RoHS Certificate of Compliance (PDF)**

MT29F64G08AFAAAWP-ITZ

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

**Updated:** 11/2016

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#### **RoHS Certificates**

## **China RoHS Certificate (PDF)**

MT29F64G08AFAAAWP-ITZ

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

**Updated:** 11/2016

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# Documentation & Support

See All SLC NAND Documentation

Technical Notes

**SEARCH (17) SLC NAND TECHNICAL NOTES** 

**Technical Notes** 



TN-13-49: SLC NAND Device Refresh (PDF)

NAND Flash SLC NAND mitigate these adverse effects.

**Updated:** 10/31/2016 File Type: PDF

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**Technical Notes** 



#### TN-29-58: ONFI NV-DDR2 Design Guide (PDF)

NAND Flash SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF

**Updated:** 09/29/2016

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#### **Technical Notes**

### TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)

NAND Flash **SLC NAND** 

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF

**Updated:** 08/16/2016

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**Technical Notes** 



### TN-29-51: Migrating from 50-Series to 60-Series SLC NAND (PDF)

**Embedded Memory Solutions** NAND Flash SLC NAND

(TN-29-51) This technical note supplements the product change notification (PCN) covering the transition from Micron® 50-series (50nm) to 60-series (34nm) single-level cell (SLC) NAND Flash devices.

File Type: PDF

**Updated:** 08/16/2016

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#### **Technical Notes**

## TN-26-03: How to Read, Program, and Manage Small Page NAND OTP Memory Area (PDF)

NAND Flash **SLC NAND** 

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF

**Updated:** 08/16/2016

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**SEARCH (17) SLC NAND TECHNICAL NOTES** 

MT29F64G08AFAAAWP-ITZ ( Current )		MT29F32G08ABAAAWP-ITZ	MT29F32G08ABAAAWP-Z	MT29F32G08ABCABH1-10IT Z		
Status	Production	Production	Production	Production		
Density	64Gb	32Gb	32Gb	32Gb		
RoHS	Yes	Yes	Yes	Yes		
Width	х8	x8	x8	х8		
Voltage	3.3V	3.3V	3.3V	3.3V		
Package	TSOP	TSOP TSOP		VBGA		
Pin Count	48-pin	48-pin	48-pin	100-ball		
	MT/s			200 MT/s		
I/O	Common	Common	Common	Common		
Op Temp	-40C to +85C	-40C to +85C	0C to +70C	-40C to +85C		

# Where to Buy

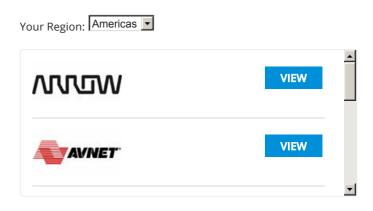
## Orderable Parts

		Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F64G08 ITZ:A	AFAAAWP-	Production	N/A	N/A	N/A	N/A	No		N/A

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