



# PMN30UN

30 V, N-channel Trench MOSFET

29 January 2016

Product data sheet

## 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- Enhanced power dissipation capability of 1240 mW

## 3. Applications

- LED driver
- Power management
- Low-side loadswitch
- Switching circuits

## 4. Quick reference data

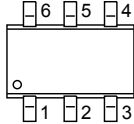
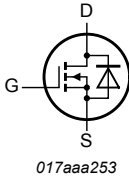
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{DS}$	drain-source voltage	$T_j = 25\text{ °C}$	-	-	30	V
$V_{GS}$	gate-source voltage		-12	-	12	V
$I_D$	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}; t \leq 5\text{ s}$	[1]	-	5.7	A
<b>Static characteristics</b>						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 4.5\text{ A}; T_j = 25\text{ °C}$	-	33	40	m $\Omega$

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain	 <p><b>TSOP6 (SOT457)</b></p>	 <p>017aaa253</p>
2	D	drain		
3	G	gate		
4	S	source		
5	D	drain		
6	D	drain		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMN30UN	TSOP6	plastic surface-mounted package (TSOP6); 6 leads	SOT457

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PMN30UN	H2

## 8. Limiting values

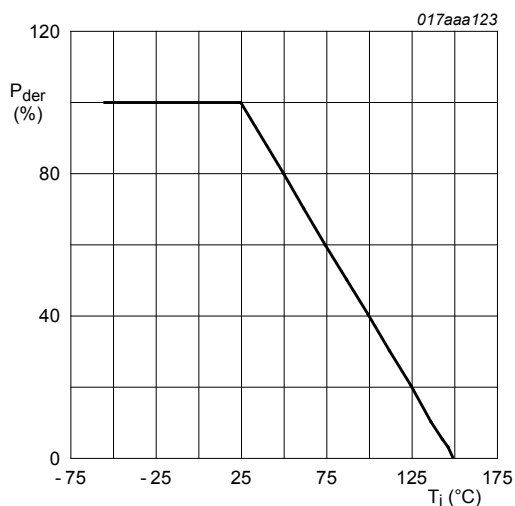
**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
V <sub>GS</sub>	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	5.7	A
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	4.5	A
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	2.9	A
I <sub>DM</sub>	peak drain current	T <sub>amb</sub> = 25 °C; single pulse; t <sub>p</sub> ≤ 10 μs		-	18	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	530	mW
			[1]	-	1.24	W
		T <sub>sp</sub> = 25 °C		-	4.46	W
T <sub>j</sub>	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
<b>Source-drain diode</b>						
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	1.2	A

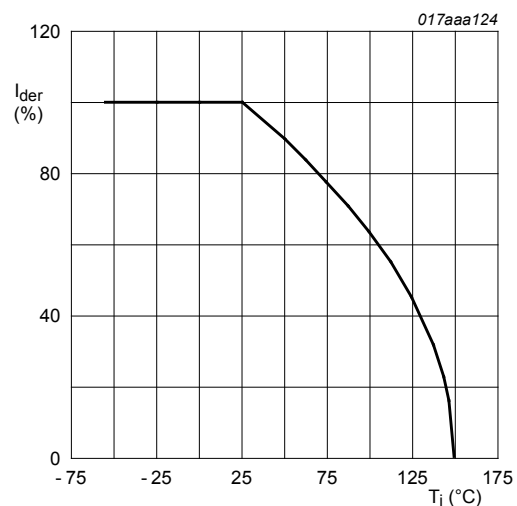
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



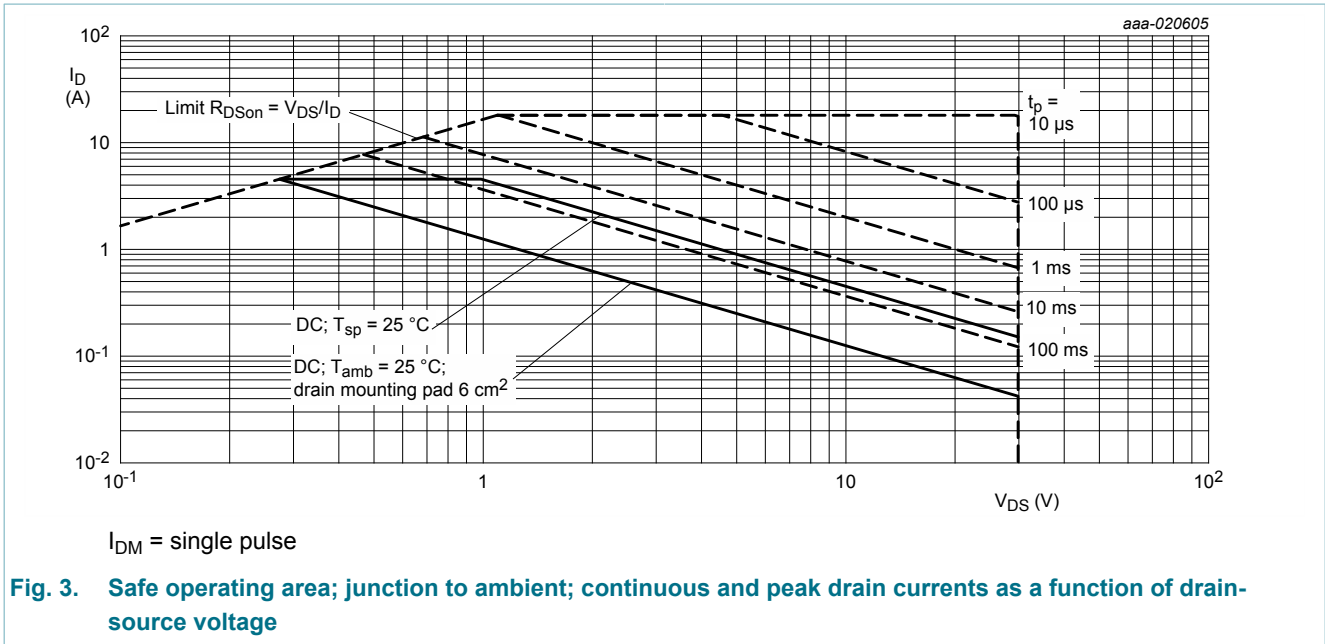
**Fig. 1. Normalized total power dissipation as a function of junction temperature**

$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ\text{C})}} \times 100 \%$$



**Fig. 2. Normalized continuous drain current as a function of junction temperature**

$$I_{der} = \frac{I_D}{I_{D(25^\circ\text{C})}} \times 100 \%$$



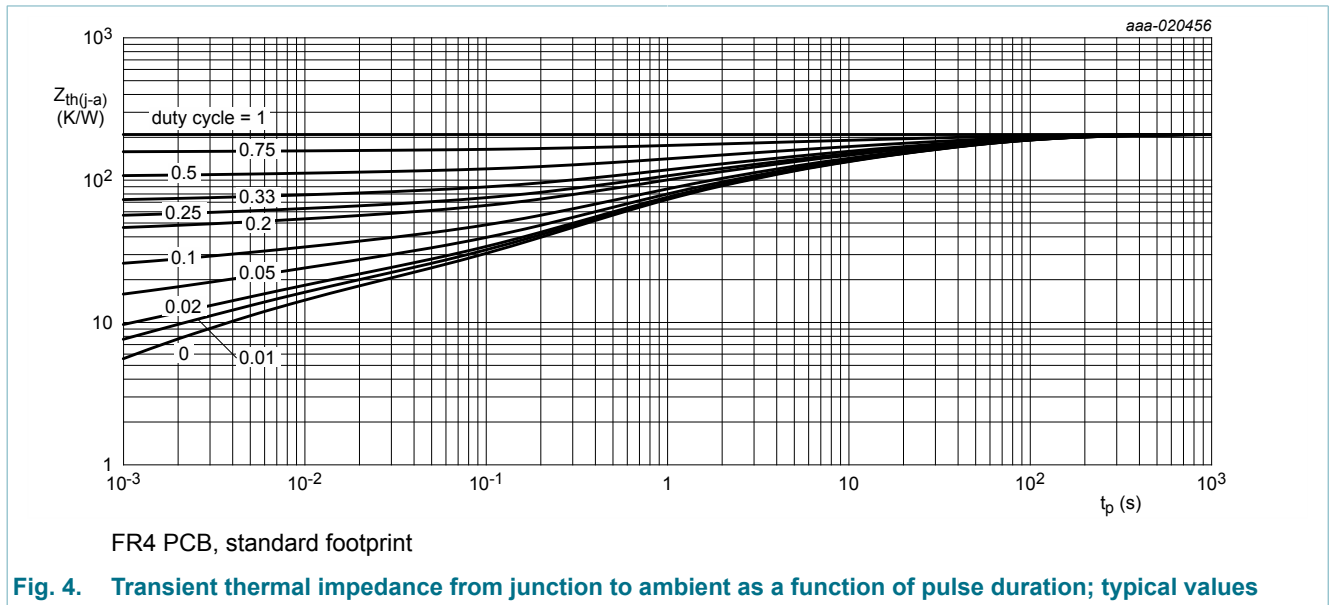
## 9. Thermal characteristics

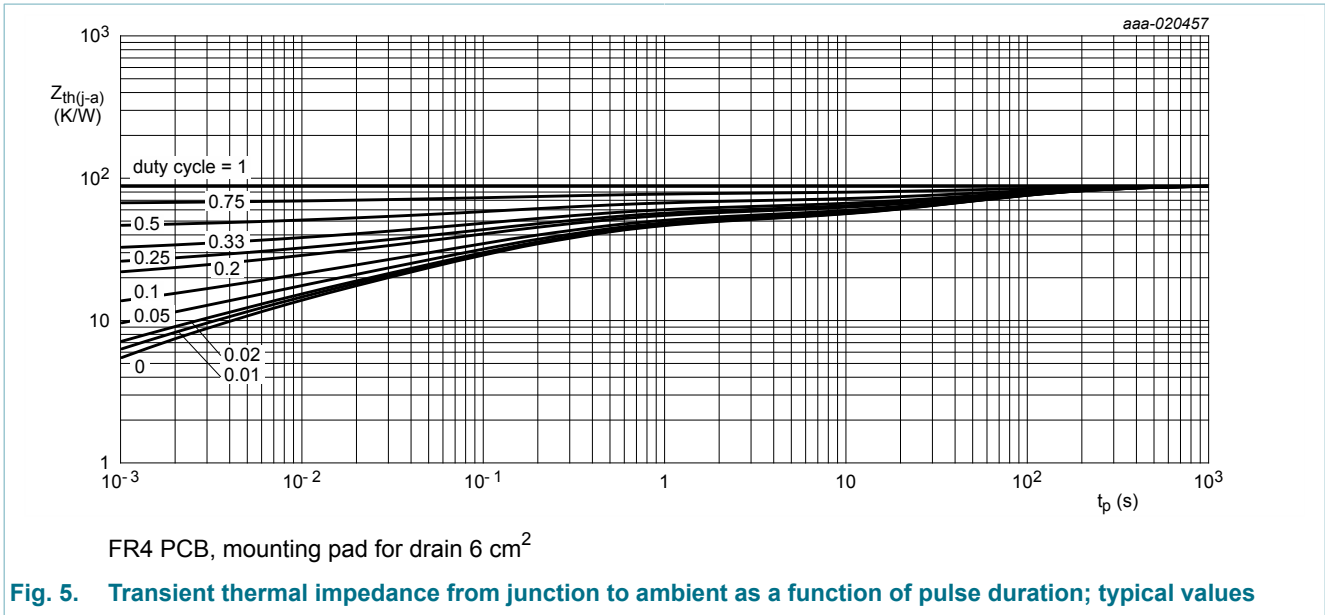
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	205	235	K/W
			[2]	-	88	101	K/W
		in free air; $t \leq 5$ s	[2]	-	55	63	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	24	28	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

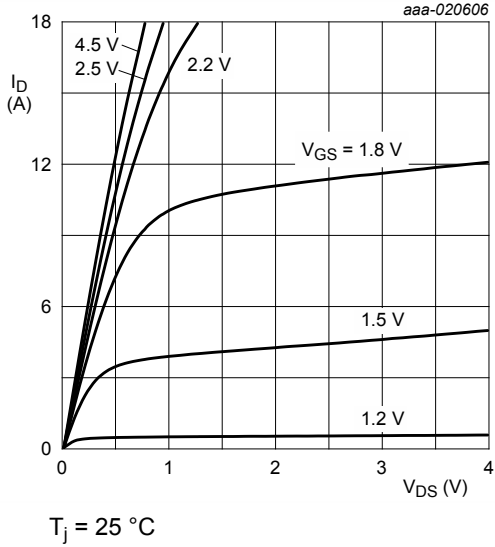




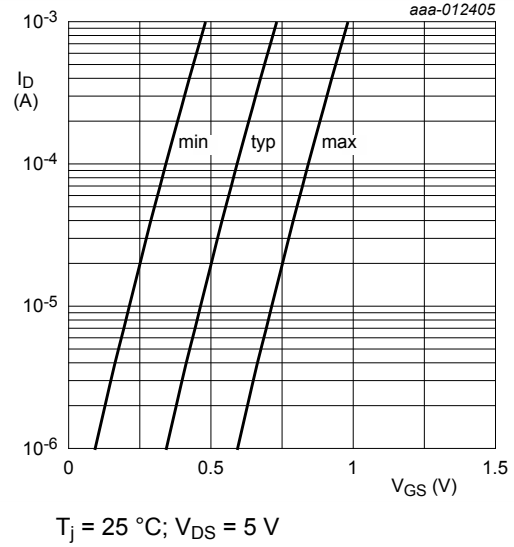
## 10. Characteristics

Table 7. Characteristics

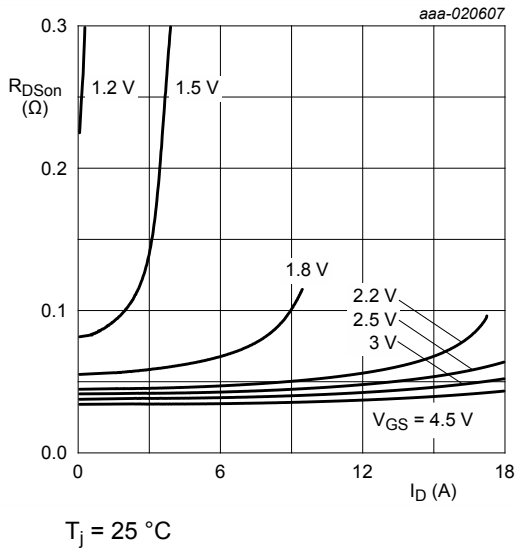
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu\text{A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	30	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu\text{A}; V_{DS} = V_{GS}; T_j = 25 \text{ }^\circ\text{C}$	0.4	0.65	0.9	V
$I_{DSS}$	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	gate leakage current	$V_{GS} = 12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	-	100	nA
		$V_{GS} = -12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	-	-100	nA
$R_{DSon}$	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 4.5 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	33	40	m $\Omega$
		$V_{GS} = 4.5 \text{ V}; I_D = 4.5 \text{ A}; T_j = 150 \text{ }^\circ\text{C}$	-	50	61	m $\Omega$
		$V_{GS} = 2.5 \text{ V}; I_D = 4 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	42	52	m $\Omega$
		$V_{GS} = 1.8 \text{ V}; I_D = 1 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	54	75	m $\Omega$
		$V_{GS} = 1.5 \text{ V}; I_D = 0.1 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	85	200	m $\Omega$
$g_{fs}$	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 2 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$	-	12.6	-	S
$R_G$	gate resistance	$f = 1 \text{ MHz}; T_j = 25 \text{ }^\circ\text{C}$	-	8.7	-	$\Omega$
<b>Dynamic characteristics</b>						
$Q_{G(tot)}$	total gate charge	$V_{DS} = 15 \text{ V}; I_D = 3.7 \text{ A}; V_{GS} = 4.5 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	7	12	nC
$Q_{GS}$	gate-source charge		-	0.9	-	nC
$Q_{GD}$	gate-drain charge		-	1.7	-	nC
$C_{iss}$	input capacitance	$V_{DS} = 15 \text{ V}; f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	635	-	pF
$C_{oss}$	output capacitance		-	40	-	pF
$C_{riss}$	reverse transfer capacitance		-	35	-	pF
$t_{d(on)}$	turn-on delay time		$V_{DS} = 15 \text{ V}; I_D = 3.7 \text{ A}; V_{GS} = 4.5 \text{ V}; R_{G(ext)} = 6 \text{ } \Omega; T_j = 25 \text{ }^\circ\text{C}$	-	9	-
$t_r$	rise time	-		23	-	ns
$t_{d(off)}$	turn-off delay time	-		34	-	ns
$t_f$	fall time	-		12	-	ns
<b>Source-drain diode</b>						
$V_{SD}$	source-drain voltage	$I_S = 1.2 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$	-	0.7	1.2	V



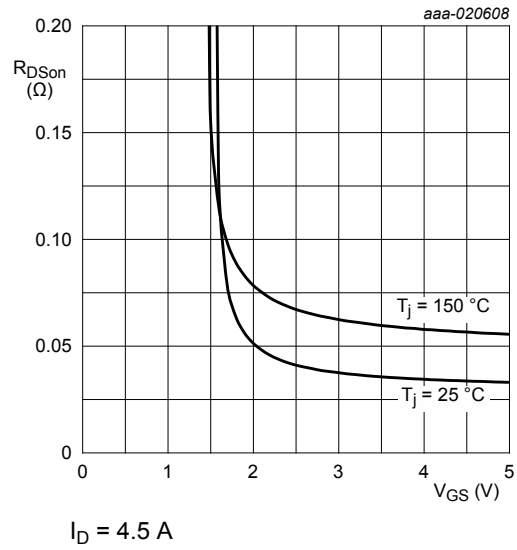
**Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values**



**Fig. 7. Sub-threshold drain current as a function of gate-source voltage**

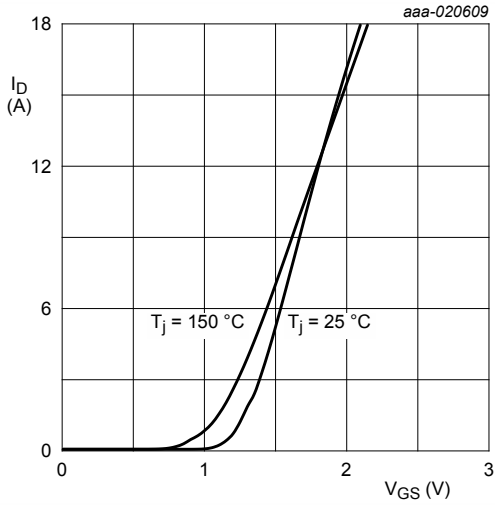


**Fig. 8. Drain-source on-state resistance as a function of drain current; typical values**



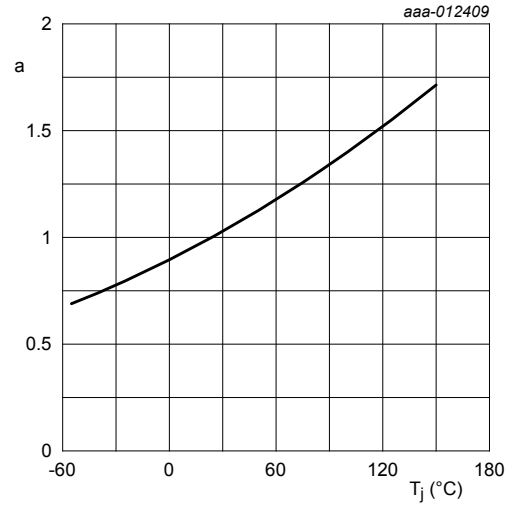
**Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values**





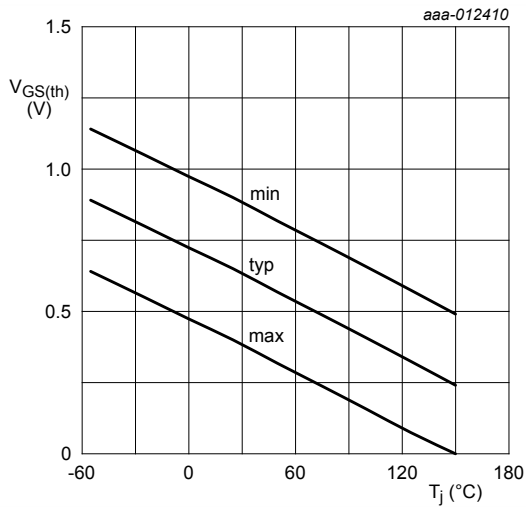
$$V_{DS} > I_D \times R_{DSon}$$

**Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values**



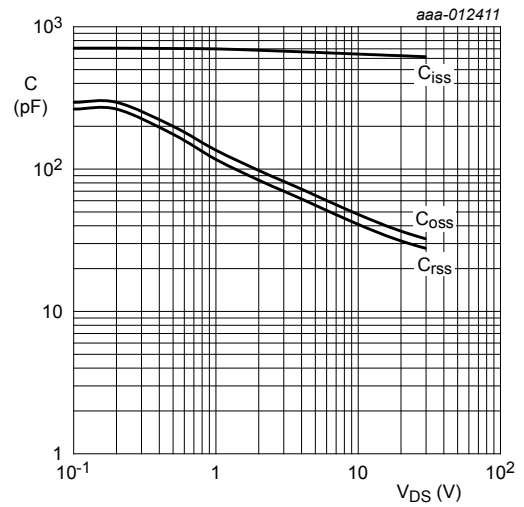
**Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values**

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$



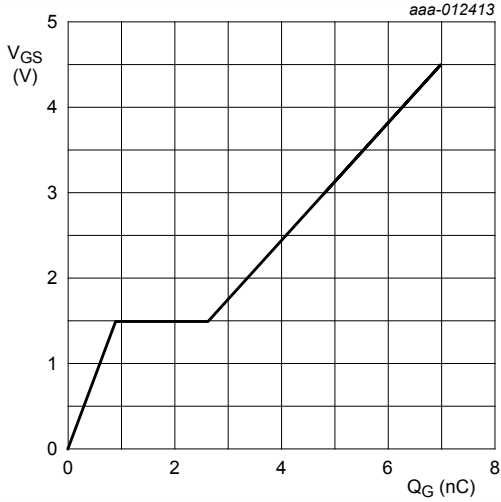
$$I_D = 250 \mu A; V_{DS} = V_{GS}$$

**Fig. 12. Gate-source threshold voltage as a function of junction temperature**



$$f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}$$

**Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values**



$I_D = 3.7 \text{ A}; V_{DS} = 15 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}$

Fig. 14. Gate-source voltage as a function of gate charge; typical values

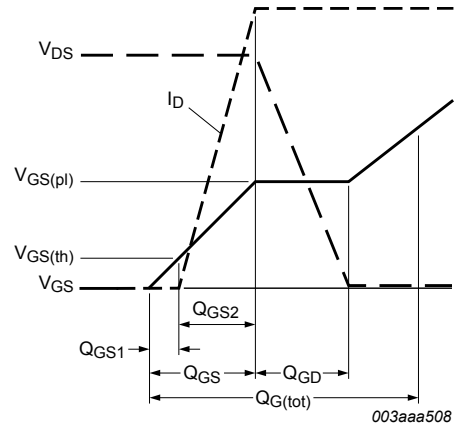
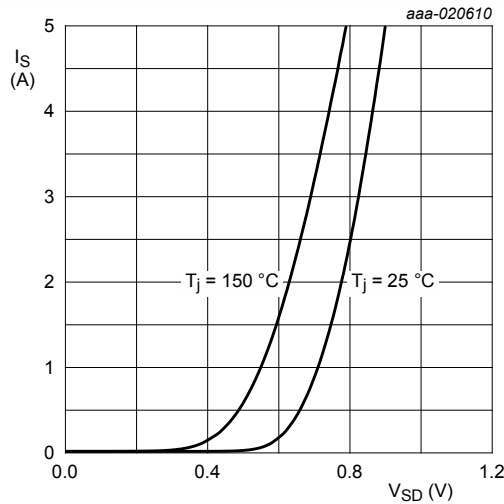


Fig. 15. MOSFET transistor: Gate charge waveform definitions



$V_{GS} = 0 \text{ V}$

Fig. 16. Source current as a function of source-drain voltage; typical values

## 11. Test information

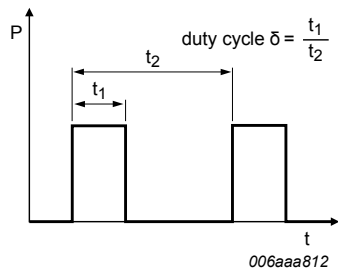


Fig. 17. Duty cycle definition

## 12. Package outline

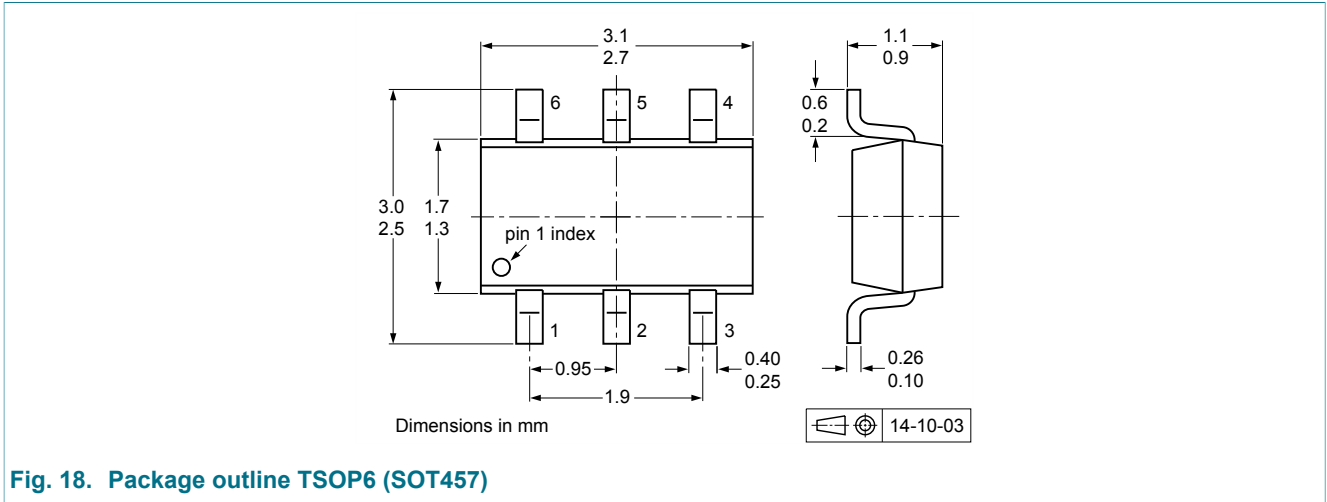


Fig. 18. Package outline TSOP6 (SOT457)

## 13. Soldering

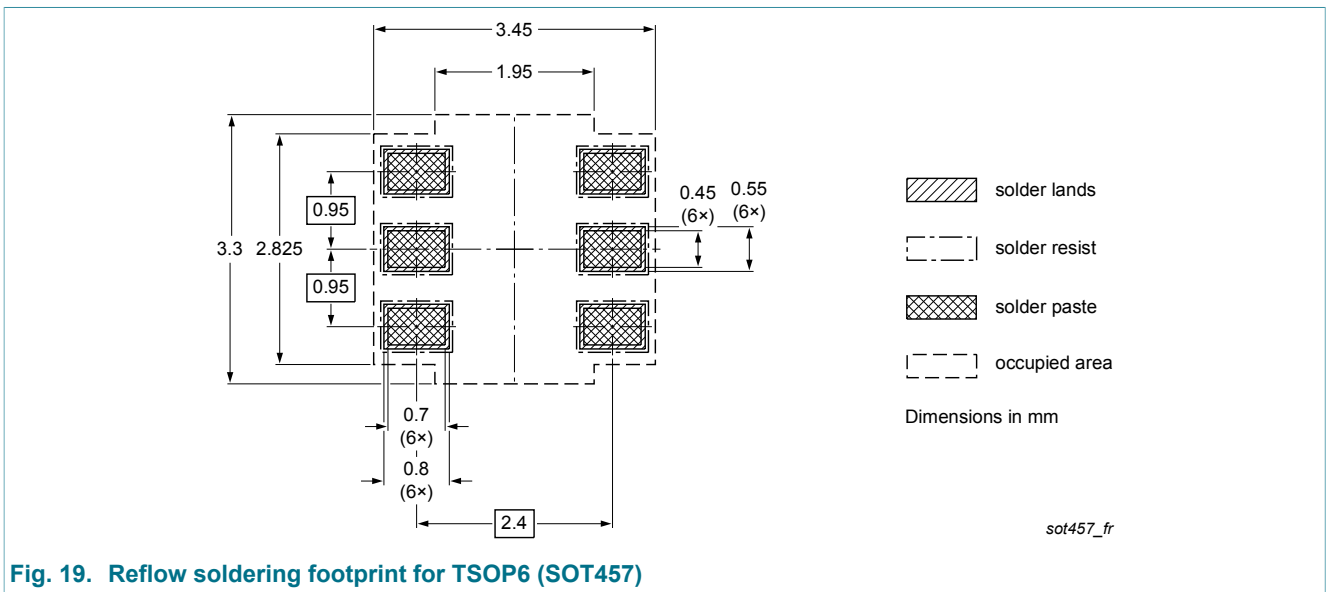


Fig. 19. Reflow soldering footprint for TSOP6 (SOT457)

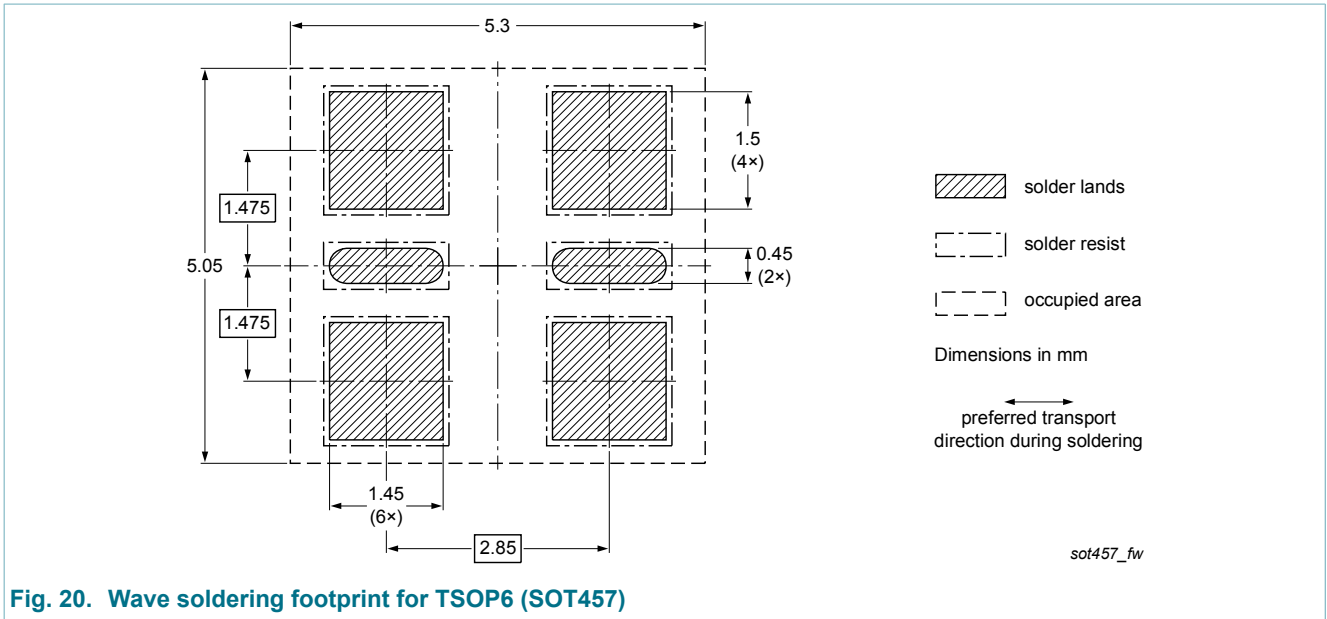


Fig. 20. Wave soldering footprint for TSOP6 (SOT457)

## 14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMN30UN v.1	20160129	Product data sheet	-	-

## 15. Legal information

### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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