4-Bit Binary Counter

Description

The MC10H016 is a high-speed synchronous, presettable, cascadable 4-bit binary counter. It is useful for a large number of conversion, counting and digital integration applications.

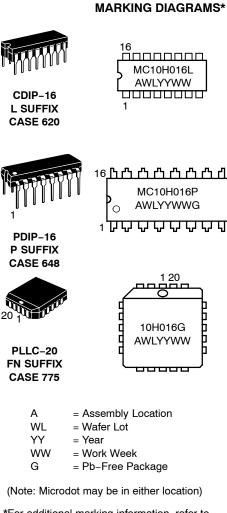
Features

- Counting Frequency, 200 MHz Minimum
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10KTM Compatible
- Positive Edge Triggered
- Pb-Free Packages are Available*



ON Semiconductor®

http://onsemi.com



16

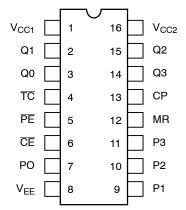
*For additional marking information, refer to Application Note AND8002/D.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

© Semiconductor Components Industries, LLC, 2006 February, 2006 – Rev. 8



Pin assignment is for Dual-in-Line Package

Figure 1. Pin Assignment

Table 2. MAXIMUM RATINGS

Table 1. TRUTH TABLE

CE	PE	MR	СР	Function
L H L X X	L H H X X		Z Z Z ZZ X	Load Parallel (P_n to Q_n) Load Parallel (P_n to Q_n) Count Hold Masters Respond; Slaves Hold Reset ($Q_n = LOW$, $\overline{T}_C = HIGH$)

Z = Clock Pulse (Low to High); ZZ = Clock Pulse (High to Low) Features include assertion inputs and outputs on each of the four master/slave counting flip-flops. Terminal count is generated internally in a manner that allows synchronous loading at nearly the speed of the basic counter.

Symbol	Characteristic	Rating	Unit	
V _{EE}	Power Supply (V _{CC} = 0)	-8.0 to 0	Vdc	
VI	Input Voltage (V _{CC} = 0)	0 to V _{EE}	Vdc	
l _{out}	Output Current	Continuous Surge	50 100	mA
T _A	Operating Temperature Range		0 to +75	°C
T _{stg}	Storage Temperature Range	Plastic Ceramic	−55 to +150 −55 to +165	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

		0 °		25 °		75 °		
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
Ι _Ε	Power Supply Current	-	126	-	115	-	126	mA
l _{inH}	Input Current High All Except MR Pin 12 MR		450 1190		265 700		265 700	μΑ
l _{inL}	Input Current Low	0.5	-	0.5	-	0.3	-	μΑ
V _{OH}	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V _{OL}	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V _{IH}	High Input Voltage	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
V _{IL}	Low Input Voltage	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

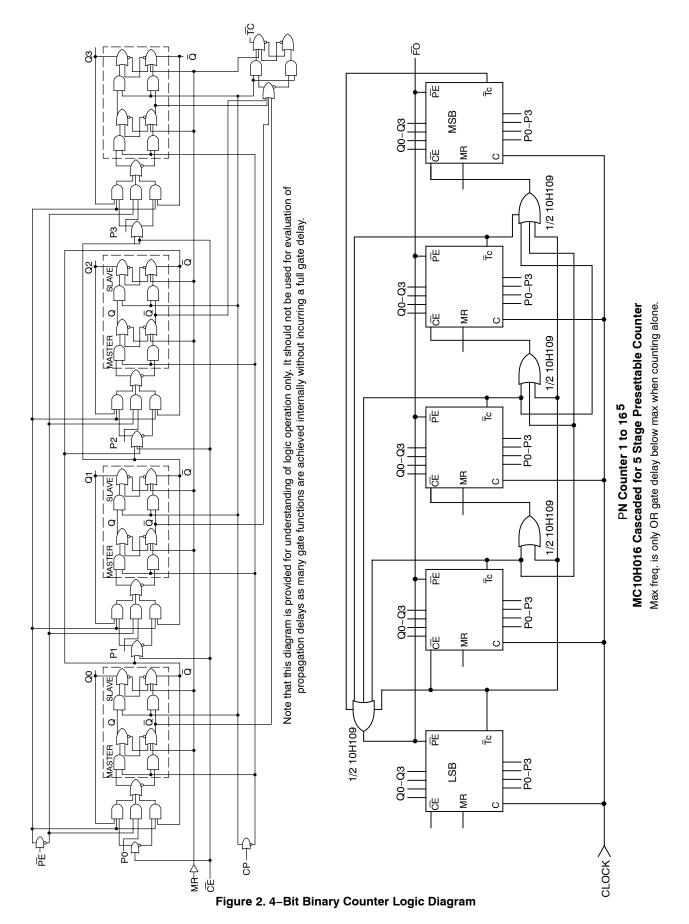
Table 3. ELECTRICAL CHARACTERISTICS (V_{EE} = $-5.2 \text{ V} \pm 5\%$) (Note 1)

1. Each MECL 10H[™] series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50 Ω resistor to −2.0 V.

Table 4. AC CHARACTERISTICS

		0 °		25 °		75 °		
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
t _{pd}	Propagation Delay Clock to Q Clock to TC MR to Q	1.0 0.7 0.7	2.4 2.4 2.4	1.0 0.7 0.7	2.5 2.5 2.5	1.0 0.7 0.7	2.7 2.6 2.6	ns
t _{set}	Set–up Time P _n to Clock CE or PE to Clock	2.0 2.5		2.0 2.5		2.0 2.5		ns
t _{hold}	Hold Time Clock to P _n Clock to CE or PE	1.0 0.5	- -	1.0 0.5		1.0 0.5		ns
f _{count}	Counting Frequency	200	-	200	-	200	-	MHz
t _r	Rise Time	0.5	2.0	0.5	2.1	0.5	2.2	ns
t _f	Fall Time	0.5	2.0	0.5	2.1	0.5	2.2	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

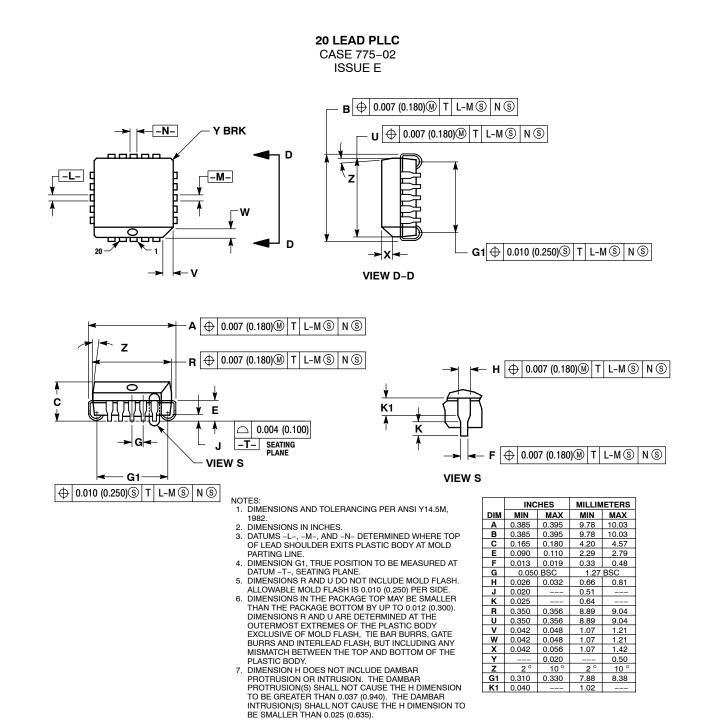


ORDERING INFORMATION

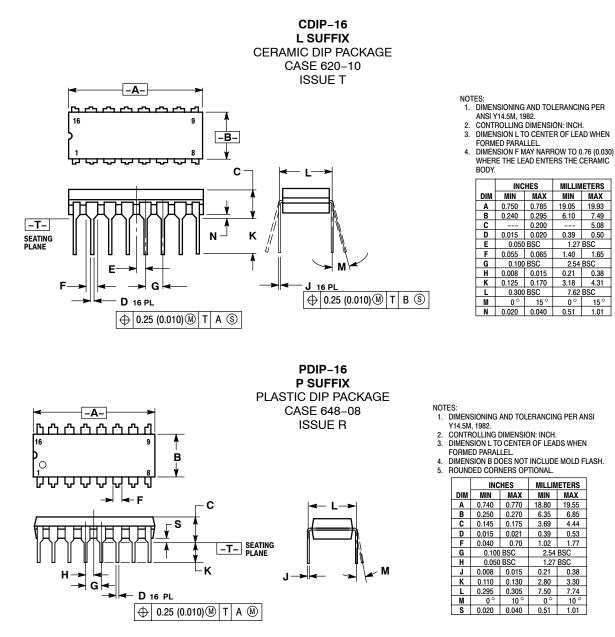
Device	Package	Shipping [†]		
MC10H016FN	PLLC-20	46 Unit / Rail		
MC10H016FNG	PLLC-20 (Pb-Free)	46 Unit / Rail		
MC10H016FNR2	PLLC-20	500 / Tape & Reel		
MC10H016FNR2G	PLLC-20 (Pb-Free)	500 / Tape & Reel		
MC10H016L	CDIP-16	25 Unit / Rail		
MC10H016P	PDIP-16	25 Unit / Rail		
MC10H016PG	PDIP-16 (Pb-Free)	25 Unit / Rail		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS



PACKAGE DIMENSIONS



MECL 10H and MECL 10K are trademarks of Motorola, Inc.

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative

7.49

5.08

0.50

15°

1.01