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MT29F16G08ABABAWP-IT

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Data Sheets (1)

Data Sheet 🔒

Data sheet: 16Gb, 32Gb, 64Gb, and 128Gb Async/Sync NAND (NDA)

NAND Flash
SLC NAND

Rev. E

File Type: PDF   **Updated:** 03/2010

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Specs

Orderable Parts for: MT29F16G08ABABAWP-IT

SEE ALL SLC NAND PARTS

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F16G08ABABAWP-IT:B	**Contact Factory**	N/A	N/A	N/A	N/A	No		VIEW

Detailed Specifications

Density	16Gb	**Status**	Production	**RoHS**	Yes	**Width**	x8
Voltage	3.3V	**Package**	TSOP	**Pin Count**	48-pin	**MT/s**	
I/O	Common	**Op Temp**	-40C to +85C				

Sim Models & Software

Sim Models

Verilog 🔒

Verilog: NAND 16/32/64/128Gb, SLC (rev B) M62B (ZIP)

16/32/64/128Gb SLC NAND Verilog model Rev 7.26

File Type: ZIP   **Updated:** 08/02/2010

NAND Flash
SLC NAND

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HSpice 🔒

HSpice: NAND 16/32/64/128Gb SLC (Rev B) M62B (ZIP)

Rev. 2.1

File Type: ZIP   **Updated:** 10/30/2009

NAND Flash
SLC NAND

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IBIS 🔒

IBIS: NAND 16/32/64/128Gb SLC (Rev B) M62B (ZIP)

Rev. 2.1

File Type: ZIP   **Updated:** 10/30/2009

NAND Flash
SLC NAND

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Software

Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

Reset NAND before write protect check.

File Type: ZIP   **Updated:** 03/31/2016

MLC NAND
NAND Flash
SLC NAND

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Software Support Pack

YAFFS mark bad block issue (ZIP)

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP   **Updated:** 03/31/2016

MLC NAND
NAND Flash
SLC NAND

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Software Support Pack 🔒

NANDcode Flash Translation Layer (ZIP)

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 08/08/2014

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Software Driver

MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)

NAND Flash
SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver here.

File Type: ZIP   **Updated:** 04/05/2013

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Software Driver 🔒

MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)

NAND Flash
Serial NAND
SLC NAND

M68A, M69A, M60A

File Type: ZIP   **Updated:** 12/28/2011

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F16G08ABABAWP-IT

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)   **Updated:** 11/2016

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F16G08ABABAWP-IT

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)   **Updated:** 11/2016

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Documentation & Support

See All SLC NAND Documentation

Technical Notes

Technical Notes 🔒

TN-13-49: SLC NAND Device Refresh (PDF)

NAND Flash
SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF   **Updated:** 10/31/2016

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Technical Notes 🔒

TN-29-58: ONFI NV-DDR2 Design Guide (PDF)

NAND Flash
SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF   **Updated:** 09/29/2016

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Technical Notes 🔒

TN-29-51: Migrating from 50-Series to 60-Series SLC NAND (PDF)

Embedded Memory Solutions
NAND Flash
SLC NAND

(TN-29-51) This technical note supplements the product change notification (PCN) covering the transition from Micron® 50-series (50nm) to 60-series (34nm) single-level cell (SLC) NAND Flash devices.

File Type: PDF   **Updated:** 08/16/2016

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Technical Notes

TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)

NAND Flash
SLC NAND

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF   **Updated:** 08/16/2016

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Technical Notes

TN-26-03: How to Read, Program, and Manage Small Page NAND OTP Memory Area (PDF)

NAND Flash
SLC NAND

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF   **Updated:** 08/16/2016

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Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F16G08ABABAWP-IT:B	Contact Factory	N/A	N/A	N/A	N/A	No		VIEW

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CONTACT A REP

- OR -

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Your Region: Americas ▾

ARROW — VIEW

AVNET — VIEW

See All Distributors

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Networking Innovations
Supercomputing Memory
Ultrathin Solutions

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Managed NAND
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