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MT29F1T08CUECBH8-12

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DATA SHEETS (1) | SPECS | SIM MODELS & SOFTWARE | ROHS CERTIFICATES | DOCUMENTATION & SUPPORT | PARTS WITH THE SAME DATA SHEET (5) | WHERE TO BUY

Data Sheets (1)

Data Sheet 🔒

Datasheet: NAND 128/256/512Gb/1Tb/2Tb, MLC, Async/Sync, L95B

Datasheet: NAND 128/256/512Gb/1Tb/2Tb, MLC, Async/Sync, L95B

File Type: PDF   **Updated:** 09/2016

Client Storage
MLC NAND
NAND Flash
ONFI
Personal Systems

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Specs

Orderable Parts for: MT29F1T08CUECBH8-12

SEE ALL MLC NAND PARTS

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F1T08CUECBH8-12:C	**Production**	N/A	NW681	N/A	N/A	No		N/A

Detailed Specifications

Density	1Tb	**Status**	Production	**NAND Type**	2D	**RoHS**	Yes
Width	x8	**Voltage**	3.3V	**Package**	LBGA	**Pin Count**	152-ball
Op. Temp.	0C to +70C	**MT/s**	166 MT/s	**I/O**	Common	**Product Name**	

Sim Models & Software

Sim Models

Verilog 🔒

Verilog: MLC 128Gb to 2Tb Async/Sync NAND L95B (ZIP)

MLC NAND
NAND Flash

128GB/2TB Verilogg model 7.30

File Type: ZIP   **Updated:** 12/15/2016   **DOWNLOAD**

IBIS 🔒

IBIS: 128Gb 256Gb 512Gb 1Tb 2Tb Async Sync NAND (ZIP)

MLC NAND
NAND Flash

Rev 2.3

File Type: ZIP   **Updated:** 03/03/2016   **DOWNLOAD**

HSpice 🔒

HSpice: 128Gb 256Gb 512Gb 1Tb Async Sync NAND (ZIP)

MLC NAND
NAND Flash

Rev 2.3

File Type: ZIP   **Updated:** 03/03/2016   **DOWNLOAD**

Software

Software Support Pack

fixup-ubi-cannot-recover-master node issue (ZIP)

MLC NAND
NAND Flash

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be dameged in one single master block.

File Type: ZIP   **Updated:** 03/31/2016   **DOWNLOAD**

Software Support Pack

mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)

MLC NAND
NAND Flash

MLC NANDs have more bit flips that SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.

File Type: ZIP   **Updated:** 03/31/2016   **DOWNLOAD**

Software Support Pack

YAFFS mark bad block issue (ZIP)

MLC NAND
NAND Flash

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

UBI power loss issue for paired page Ver2.0 (ZIP)

MLC NAND
NAND Flash

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND
NAND Flash
SLC NAND

Reset NAND before write protect check.

File Type: ZIP   **Updated:** 03/31/2016

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F1T08CUECBH8-12

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)   **Updated:** 01/2017

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F1T08CUECBH8-12

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)   **Updated:** 01/2017

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Documentation & Support

See All MLC NAND Documentation

Technical Notes

Technical Notes

TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

File Type: PDF   **Updated:** 01/25/2016

3D NAND
DDR3 SDRAM
DDR4 SDRAM
DRAM
See More Tags

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Technical Notes

TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

File Type: PDF   **Updated:** 02/14/2013

DDR SDRAM
DDR2 SDRAM
DDR3 SDRAM
DDR3L-RS
See More Tags

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Technical Notes

TN-29-61: Wear Leveling in NAND Flash Memory (PDF)

(TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory.

File Type: PDF   **Updated:** 10/15/2010

MLC NAND
NAND Flash
SLC NAND

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Technical Notes

TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory (PDF)

(TN-29-65) Download the low-level driver described in this document here.

File Type: PDF   **Updated:** 10/15/2010

MLC NAND
NAND Flash
SLC NAND

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Parts with the same Data Sheet (5)

	MT29F1T08CUECBH8-12 (Current)	MT29F128G08CBECBH6-12	MT29F128G08CBECBL95B3 WC1	MT29F256G08CEECBH6-12
Status	Production	Production	Production	Production
Density	1Tb	**128Gb**	**128Gb**	**256Gb**
NAND Type	2D	2D	2D	2D
RoHS	Yes	Yes	Yes	Yes
Width	x8	x8	x8	x8
Voltage	3.3V	3.3V	3.3V	3.3V
Package	LBGA	**VBGA**	**n/a**	**VBGA**
Pin Count	152-ball	152-ball	**n/a**	152-ball
Op. Temp.	0C to +70C	0C to +70C	0C to +70C	0C to +70C
MT/s	166 MT/s	166 MT/s		166 MT/s
I/O	Common	Common	Common	Common
Product Name				

Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F1T08CUECBH8-12:C	Production	N/A	NW681	N/A	N/A	No		N/A

Contact Your Sales Rep

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AVNET   **VIEW**

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Enterprise SSD Storage

Mobile Memory Solutions

Networking Innovations

Supercomputing Memory

Ultrathin Solutions

Memory and Storage Products

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NAND Flash

Managed NAND

NOR Flash

Hybrid Memory Cube

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