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Home > Memory and Storage Products > MLC NAND > MT29F128G08CFAAAWP-IT ADD EMAIL 8 <sup>+</sup> in Y MT29F128G08CFAAAWP-IT	f
DATA SHEETS SPECS SIM MODELS & ROHS DOCUMENTATION PARTS WITH THE SAME WHE (1) DATA SHEET (12) TO B	
Data Sheets (1)	
Data Sheet	
L74A NAND datasheet	China
L74A NAND datasheet NAN	NAND D Flash
	Taiwan USA
File Type: PDFUpdated: 08/2016	OAD

S	oecs
-	

Orderable Parts for: MT29F128G08CFAAAWP-IT							SEE ALL	MLC NAND PARTS
	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F128G08CFAAAWP- ITZ:A	Contact Factory	N/A	N/A	N/A	N/A	No		N/A

## Detailed Specifications

Density	128Gb	Status	Contact Factory	NAND Type	2D	RoHS	Yes
Width	x8	Voltage	3.3V	Package	TSOP	Pin Count	48-pin
Op. Temp.	-40C to +85C	MT/s		I/O	Common	Product Name	

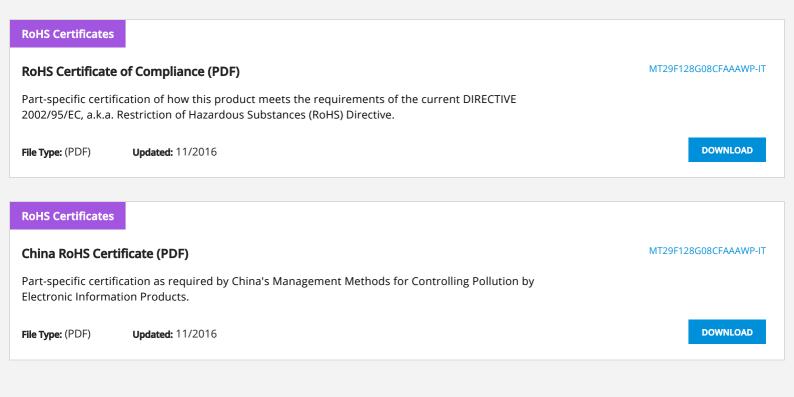
# Sim Models & Software

Sim Models

IBIS 🔒	
IBIS: NAND 64/128/256/512Gb MLC (Rev A) L74A (ZIP)	MLC NAND NAND Flash
Rev. 2.2	
File Type: ZIP   Updated: 11/18/2016	DOWNLOAD
Verilog	
Verilog: NAND 64/128/256/512Gb MLC L74A (ZIP)	MLC NAND NAND Flash
64/128/256/512Gb Verilog model Rev 7.30	
File Type: ZIP   Updated: 07/01/2013	DOWNLOAD
HSpice	
HSpice: NAND 64/128/256/512Gb MLC (Rev A) L74A (ZIP)	MLC NAND NAND Flash
Rev. 2.1	
File Type: ZIP   Updated: 02/10/2011	DOWNLOAD
Coftunero	
Software	
Software Support Pack	
mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)	MLC NAND NAND Flash
MLC NANDs have more bit flips that SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.	
File Type: ZIP   Updated: 03/31/2016	DOWNLOAD
Software Support Pack	
NAND FLASH lockunlock bug fix (ZIP)	MLC NAND NAND Flash
Reset NAND before write protect check.	SLC NAND
File Type: ZIP   Updated: 03/31/2016	DOWNLOAD
Software Support Pack	

fixup-ubi-cannot-recover-master node issue (ZIP)	MLC NAND NAND Flash
For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be dameged in one single master block.	
File Type: ZIP   Updated: 03/31/2016	DOWNLOAD
Software Support Pack	
UBI power loss issue for paired page Ver2.0 (ZIP)	MLC NAND NAND Flash
These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.	
File Type: ZIP   Updated: 03/31/2016	DOWNLOAD
Software Support Pack	
YAFFS mark bad block issue (ZIP)	MLC NAND NAND Flash
If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags- >ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.	SLC NAND

## **RoHS** Certificates



# Documentation & Support

See All MLC NAND Documentation

## Technical Notes

**SEARCH (4) MLC NAND TECHNICAL NOTES** 

### **Technical Notes 3D NAND** TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF) **DDR3 SDRAM** DDR4 SDRAM DRAM (TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also See More Tags provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools. DOWNLOAD Updated: 01/25/2016 File Type: PDF **Technical Notes** DDR SDRAM TN-00-01: Moisture Sensitivity of Plastic Packages (PDF) DDR2 SDRAM DDR3 SDRAM DDR3L-RS (TN-00-01) This technical note describes shipping procedures for preventing memory devices from See More Tags absorbing moisture and recommendations for baking devices exposed to excessive moisture. DOWNLOAD File Type: PDF Updated: 02/14/2013 **Technical Notes** MLC NAND TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory NAND Flash (PDF) **SLC NAND** (TN-29-65) Download the low-level driver described in this document here. DOWNLOAD File Type: PDF Updated: 10/15/2010 **Technical Notes** MLC NAND TN-29-61: Wear Leveling in NAND Flash Memory (PDF) NAND Flash **SLC NAND** (TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory. DOWNLOAD File Type: PDF Updated: 10/15/2010 **SEARCH (4) MLC NAND TECHNICAL NOTES**

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## Parts with the same Data Sheet (12)

MT29F128G08CFAAAWP-IT ( Current )		MT29F512G08CUCABH3-10IT Z	MT29F512G08CUCABH3-10Z	MT29F256G08CMCABH2-12I TZ		
Status	Contact Factory	Contact Factory	Contact Factory	Contact Factory		
Density	128Gb	512Gb	512Gb	256Gb		
NAND Type	2D	2D	2D	2D		
RoHS	Yes	Yes	Yes	Yes		
Width	x8	x8	x8	x8		
Voltage	3.3V	3.3V	3.3V	3.3V		
Package	TSOP	LBGA	LBGA	TBGA		
Pin Count	48-pin	100-ball	100-ball	100-ball		
Op. Temp.	-40C to +85C	-40C to +85C	0C to +70C	-40C to +85C		
MT/s		200 MT/s	200 MT/s	166 MT/s		
I/O	Common	Common	Common	Common		
F	Product Name					

# Where to Buy

## Orderable Parts

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	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F128G08CFAAAWP- ITZ:A	Contact Factory	N/A	N/A	N/A	N/A	No		N/A

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