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# MTFC8GACAEDQ-K1

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## Specs

Orderable Parts for: MTFC8GACAEDQ-K1

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MTFC8GACAEDQ-K1 AIT	Production	N/A	D9TLY	N/A	N/A	No		N/A

## RoHS Certificates

### RoHS Certificates

#### RoHS Certificate of Compliance (PDF)

[MTFC8GACAEDQ K1](#)

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 02/2017

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### RoHS Certificates

#### China RoHS Certificate (PDF)

[MTFC8GACAEDQ K1](#)

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 02/2017

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## Technical Notes

SEARCH (54) TECHNICAL NOTES

### Technical Notes

#### [TN-00-08: Thermal Applications \(PDF\)](#)

(TN-00-08) Describes some considerations in thermal applications for Micron memory devices

File Type: PDF

Updated: 01/24/2017

[DRAM](#)  
[Managed NAND](#)  
[Multichip Packages](#)  
[NAND Flash](#)  
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### Technical Notes



#### [TN-13-49: SLC NAND Device Refresh \(PDF\)](#)

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF

Updated: 10/31/2016

[NAND Flash](#)  
[SLC NAND](#)

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### Technical Notes

#### [TN-29-14: NAND Flash Performance Increase Using PROGRAM PAGE CACHE MODE Command \(PDF\)](#)

(TN-29-14) This technical note highlights the significant performance gains realized when using the PROGRAM PAGE CACHE MODE command in Micron's NAND Flash devices.

File Type: PDF

Updated: 09/29/2016

[NAND Flash](#)

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### Technical Notes

#### [TN-29-25: Improving NAND Flash Performance Using Two-Plane Command Enabled Micron Devices \(PDF\)](#)

(TN-29-25) This technical note describes the performance benefits of Micron two-plane commands and provides implementation guidelines for making the best use of two-plane capabilities.

File Type: PDF

Updated: 09/29/2016

[NAND Flash](#)

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### Technical Notes

#### [TN-29-42: Wear-Leveling Techniques in NAND Flash Devices \(PDF\)](#)

[NAND Flash](#)

(TN-29-42) This technical note highlights the importance of wear leveling, explains two wear-leveling techniques, and discusses implementing wear leveling.

File Type: PDF

Updated: 09/29/2016

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SEARCH (54) TECHNICAL NOTES

## Customer Service Note

### Customer Service Note

#### Customer Service Note

##### Shipping Quantities (PDF)

(CSN-04) This customer service note describes the standard shipping quantity, box quantity, and types such as tape and reel for Micron's products.

DRAM  
DRAM Modules  
Hybrid Memory Cube  
Multichip Packages  
[See More Tags](#)

File Type: PDF

Updated: 07/18/2016

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#### Customer Service Note

##### Wafer Packaging and Packaging Materials (PDF)

(CSN-20) Provides complete shipping and recycling information about each of the materials used for shipping Micron's products.

DRAM  
NAND Flash

File Type: PDF

Updated: 06/21/2016

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SEARCH (2) CUSTOMER SERVICE NOTE



## Where to Buy

### Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MTFC8GACAEDQ-K1 AIT	Production	N/A	D9TLY	N/A	N/A	No		N/A

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