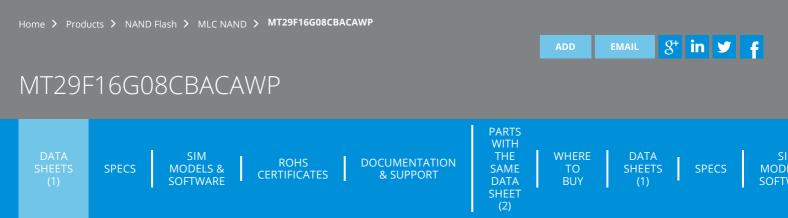
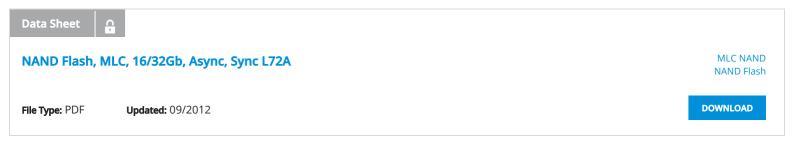
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Data Sheets (1)



Specs

Orderable Parts for: MT29F16G08CBACAWP

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F16G08CBACAWP:C	Production	N/A	N/A	N/A	N/A	No		N/A

SEE ALL MLC NAND PARTS

Detailed Specifications

Density	16Gb	Status	Production	NAND Type	2D	Width	x8
Voltage	3.3V	Package	TSOP	Pin Count	48-pin	Op. Temp.	0C to +70C
MT/s		Product Name					

Sim Models & Software

Sim Models



Verilog: NAND 16/32Gb MLC L72A (ZIP)

MLC NAND NAND Flash

16/32Gb Verilog model Rev 7.30

File Type: ZIP

Updated: 08/30/2012

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HSpice: NAND 16/32Gb MLC (Rev C) L72A (ZIP)

MLC NAND NAND Flash

Rev. 2.0

File Type: ZIP

Updated: 02/18/2011

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IBIS: NAND 16/32Gb MLC (Rev C) L72A (ZIP)

MLC NAND NAND Flash

Rev. 2.0

File Type: ZIP

Updated: 02/18/2011

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Software

Software Support Pack

mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)

MLC NAND NAND Flash

MLC NANDs have more bit flips that SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND NAND Flash SLC NAND

Reset NAND before write protect check.

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File Type: ZIP

Updated: 03/31/2016

Software Support Pack

fixup-ubi-cannot-recover-master node issue (ZIP)

MLC NAND NAND Flash

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be dameged in one single master block.

Downloaded from Arrow.com.

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Software Support Pack

UBI power loss issue for paired page Ver2.0 (ZIP)

MLC NAND NAND Flash

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

YAFFS mark bad block issue (ZIP)

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP

Updated: 03/31/2016

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MLC NAND

NAND Flash SLC NAND

RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2011/65/EU and 2015/863/EU, a.k.a. Restriction of Hazardous Substances (RoHS) Directive (Recast) without exemptions.

File Type: (PDF)

Updated: 08/2018

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RoHS Certificates

China RoHS Certificate (PDF)

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 08/2018

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Documentation & Support

See All MLC NAND Documentation

Technical Notes

TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

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DDR SDRAM

DDR2 SDRAM DDR3 SDRAM DDR3L-RS

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3D NAND

DDR3 SDRAM DDR4 SDRAM DRAM

See More Tags

File Type: PDF

Updated: 01/25/2016

Technical Notes

TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

File Type: PDF

Updated: 02/14/2013

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Customer Service Notes

SEE ALL CUSTOMER SERVICE NOTES

Customer Service Note

Product Marks, Product Labels, and Packaging Labels (PDF)

(CSN-11) The first section of this customer service note describes the product marks and labels we place on our devices. The second section describes the labels used on and in our packaging.

File Type: PDF

Updated: 08/24/2018

Client Cloud Cloud See More Tags

3D XPoint

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Customer Service Note

Micron BGA Manufacturer's User Guide (PDF)

(CSN-33) Provides information to enable customers to easily integrate both leading-edge and legacy Micron's ball grid array (BGA) packages into their manufacturing processes. It is intended as a set of high-level guidelines and a reference manual describing typical package-related and...

File Type: PDF

Updated: 12/10/2014

DDR2 SDRAM DDR4 SDRAM Embedded USB eMMC See More Tags

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Parts with the same Data Sheet (2)

	5G08CBACAWP Current)	MT29F16G08CBACAL72A3W C1	MT29F32G08CFACAWP	
Status	Production	Production	Obsolete	
Density	16Gb	16Gb	32Gb	
NAND Type	2D	2D	2D	
Width	x8	x8	x8	
Voltage	3.3V	3.3V	3.3V	
Package	TSOP	Wafer	TSOP	
Pin Count	48-pin	n/a	48-pin	
Op. Temp.	0C to +70C	0C to +70C	0C to +70C	
	MT/s			
Pro	oduct Name			

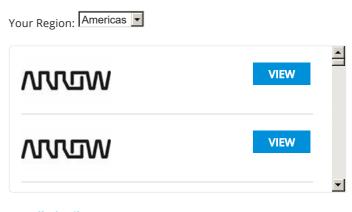
MT29F16G08CBACAWP

(Current)

Status	Production			
Density	16Gb			
NAND Type	2D			
Width	x8			
Voltage	3.3V			
Package	TSOP			
Pin Count	48-pin			
Op. Temp.	0C to +70C			
MT/s				
Product Name				

Where to Buy

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