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# MT29F32G08CBAAAWP

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## Data Sheets (1)

Data Sheet



### L63A datasheet 32Gb, 64Gb, 128Gb: NAND Flash (NDA)

L63A datasheetC

File Type: PDF

Updated: 04/2009

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[China](#)  
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## Specs

Orderable Parts for: [MT29F32G08CBAAAWP](#)

[SEE ALL MLC NAND PARTS](#)

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F32G08CBAAAWP:A	Obsolete	N/A	N/A	N/A	N/A	No		N/A

## Detailed Specifications

Density	32Gb	Status	Obsolete	NAND Type	2D	RoHS	Yes
Width	x8	Voltage	3.3V	Package	TSOP	Pin Count	48-pin
Op. Temp.	0C to +70C	MT/s		I/O	Common	Product Name	

# Sim Models & Software

## Sim Models

Verilog



### Verilog: NAND 32/64/128Gb, MLC (rev A) L63A (ZIP)

MLC NAND  
NAND Flash

32/64/128Gb Verilog Model Rev 7.26

File Type: ZIP

Updated: 08/02/2010

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HSpice



### HSpice: NAND 32/64/128Gb MLC (Rev A) L63A (ZIP)

MLC NAND  
NAND Flash

Rev. 2.2

File Type: ZIP

Updated: 11/03/2009

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IBIS



### IBIS: NAND 32/64/128Gb MLC (Rev A) L63A (ZIP)

MLC NAND  
NAND Flash

Rev. 2.2

File Type: ZIP

Updated: 11/03/2009

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## Software

### Software Support Pack

#### [mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND \(ZIP\)](#)

MLC NAND  
NAND Flash

MLC NANDs have more bit flips than SLC. When looking for a bad block marker we have a lot of false positives if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips per byte.

File Type: ZIP

Updated: 03/31/2016

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### Software Support Pack

#### [NAND FLASH lockunlock bug fix \(ZIP\)](#)

MLC NAND  
NAND Flash  
SLC NAND

Reset NAND before write protect check.

File Type: ZIP

Updated: 03/31/2016

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### Software Support Pack

#### [fixup-ubi-cannot-recover-master node issue \(ZIP\)](#)

MLC NAND  
NAND Flash

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node

File Type: ZIP

Updated: 03/31/2016

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### Software Support Pack

#### UBI power loss issue for paired page Ver2.0 (ZIP)

MLC NAND  
NAND Flash

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP

Updated: 03/31/2016

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### Software Support Pack

#### YAFFS mark bad block issue (ZIP)

MLC NAND  
NAND Flash  
SLC NAND

If the bitflips num over mtd->bitflip\_threshold the mtd\_read\_oob will return -EUCLEAN and tags->ecc\_result > YAFFS\_ECC\_RESULT\_NO\_ERROR. Then we will call yaffs\_handle\_chunk\_error.

File Type: ZIP

Updated: 03/31/2016

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## RoHS Certificates

### RoHS Certificates

#### RoHS Certificate of Compliance (PDF)

MT29F32G08CBAAWP

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 11/2016

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### RoHS Certificates

#### China RoHS Certificate (PDF)

MT29F32G08CBAAWP

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 11/2016

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## Documentation & Support

[See All MLC NAND Documentation](#)

## Technical Notes

## Technical Notes

**TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)**

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

File Type: PDF

Updated: 01/25/2016

3D NAND  
DDR3 SDRAM  
DDR4 SDRAM  
DRAM  
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## Technical Notes

**TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)**

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

File Type: PDF

Updated: 02/14/2013

DDR SDRAM  
DDR2 SDRAM  
DDR3 SDRAM  
DDR3L-RS  
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## Technical Notes

**TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory (PDF)**

(TN-29-65) Download the low-level driver described in this document [here](#).

File Type: PDF

Updated: 10/15/2010

MLC NAND  
NAND Flash  
SLC NAND

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## Technical Notes

**TN-29-61: Wear Leveling in NAND Flash Memory (PDF)**

(TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory.

File Type: PDF

Updated: 10/15/2010

MLC NAND  
NAND Flash  
SLC NAND

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Parts with the same Data Sheet (10)

( Current )

Status	Obsolete	Obsolete	Obsolete	Obsolete
Density	<b>32Gb</b>	<b>128Gb</b>	<b>128Gb</b>	<b>128Gb</b>
NAND Type	<b>2D</b>	2D	2D	2D
RoHS	<b>Yes</b>	Yes	Yes	Yes
Width	<b>x8</b>	x8	x8	x8
Voltage	<b>3.3V</b>	3.3V	3.3V	3.3V
Package	<b>TSOP</b>	TSOP	TSOP	<b>VLGA</b>
Pin Count	<b>48-pin</b>	48-pin	48-pin	<b>52-pad</b>
Op. Temp.	<b>0C to +70C</b>	0C to +70C	<b>-40C to +85C</b>	0C to +70C
MT/s				
I/O	<b>Common</b>	Common	Common	<b>Separate</b>
Product Name				

## Where to Buy

### Orderable Parts


	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F32G08CBAAWP:A	Obsolete	N/A	N/A	N/A	N/A	No		N/A

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  - NOR Flash
  - Hybrid Memory Cube
  - Multichip Packages
  - Solid State Drives
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  - Authorized Sales
  - Site Map
  - Surplus Equipment
  - Terms of Use
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