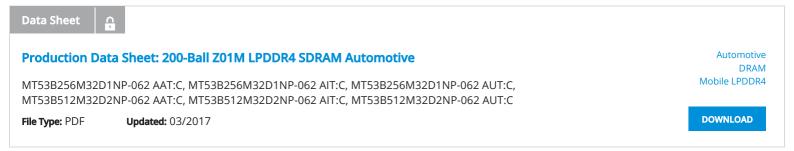
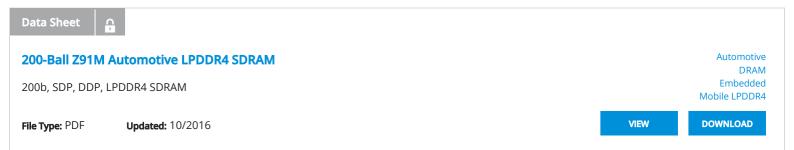


Data Sheets (2)





Specs

Orderable Parts for: MT53B256M32D1DS-062 AUT

Orderable Parts for: M	Г53B256N	132D1E					SEE	SEE ALL LPDRAM PARTS	
	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part	
MT53B256M32D1DS-062 AUT:C	Sampling	N/A	D9VXZ	N/A	N/A	No		N/A	

Detailed Specifications

Downloaded from Arrow.com.

Technology	LPDDR4	Density	8Gb	Part Status	Sampling	RoHS	Yes
Depth	256Mb	Width	x32	Voltage	1.1V	Package	WFBGA
Pin Count	200-ball	Clock Rate	1600 MHz	Data Rate	DDR4-3200	Cycle Time	
Op. Temp.	-40C to +125C	Brand	Micron				

Sim Models & Software

Sim Models



RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

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RoHS Certificates

China RoHS Certificate (PDF)

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

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Documentation & Support

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Technical Notes

SEARCH (9) LPDRAM TECHNICAL NOTES

Technical Notes

Technical Note: Low-Power Function of Mobile RAM - Deep Power Down (DPD) (PDF)

DRAM LPDRAM

(E0598E21) This technical note describes Deep Power Down (DPD), one of the low-power functions that have been adapted to Mobile DRAM.

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Technical Notes

Technical Note: Low-Power Function of Mobile RAM - Partial Array Self Refresh (PASR) (PDF)

DRAM LPDRAM

(E0597E10) This technical note describes Partial Array Self Refresh (PASR), one of the low-power functions that have been adapted to Mobile DRAM.

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Technical Note: Low-Power Function of Mobile RAM - Auto Temperature Compensated Self Refresh (ATCSR) (PDF)

DRAM LPDRAM

(E0599E20) This technical note describes Auto Temperature Compensated Self Refresh (ATCSR), one of the low-power functions that have been adapted to Mobile DRAM.

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TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

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TN-46-23: Backward Compatibility for Faster LPDDR SDRAM (PDF)

DRAM LPDRAM Mobile

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File Type: PDF

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SEARCH (9) LPDRAM TECHNICAL NOTES

Parts with the same Data Sheet (5)

MT53B256M32D1DS-062 AU T (Current)		MT53B256M32D1DS-062 AA T	MT53B256M32D1DS-062 AIT	MT53B256M32D1NP-062 AU T	
Part Status	Sampling	Sampling	Sampling	Production	
Technology	LPDDR4	LPDDR4	LPDDR4	LPDDR4	
Density	8Gb	8Gb	8Gb	8Gb	
RoHS	Yes	Yes	Yes	Yes	
Depth	256Mb	256Mb	256Mb	256Mb	
Width	/idth x32	x32	x32	x32	
Voltage	1.1V	1.1V	1.1V	1.1V	
Package	WFBGA	WFBGA	WFBGA	WFBGA	
Pin Count	200-ball	200-ball	200-ball	200-ball	
Clock Rate	1600 MHz	1600 MHz	1600 MHz	1600 MHz	
Data Rate	DDR4-3200	DDR4-3200	DDR4-3200	DDR4-3200	
	Cycle Time				
Op. Temp.	-40C to +125C	-40C to +105C	-40C to +95C	-30C to +85C	
Brand Micron		Micron	Micron	Micron	

MT53B256M32D1DS-062 AU

1

(Current)
Downloaded from Arrow.com.

Where to Buy **Sampling** Technology LPDDR4 Density 8Gb **FBGA SPD** Chipset Start Alternative PLP Media RoHS Yes Code Data Validation Date Part Depth 256Mb Sampling N/A D9VXZ N/A N/A No N/A Width x32 Voltage **1.1V** Package **WFBGA** Check with Distributors Pin Count 200-ball Your Region: Americas Clock Rate 1600 MHz **VIEW** WDW Data Rate **DDR4-3200** Cycle Time **VIEW** NUDNOp. Temp. -40C to +125C

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