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LC72725KVSGEVB: Signal Processing for Broadcast Evaluation Board

Evaluation/Development Tool Description

This device implements the signal processing required by the European Broadcasting Union RDS (Radio Data System) standard and by the US NRSC (National Radio System Committee) RBDS (Radio Broadcast Data System) standard. It includes a band-pass filter, demodulator and data buffer on chip. RDS data can be read out from this on-chip data buffer by external clock input in slave operation mode.



Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
LC72725KVSGEVB	Active	Pb-free	Signal Processing for Broadcast Evaluation Board	LC72725KVS -TLM-H	Buy Now Contact Local Sales Office Inventory

Technical Documents						
Туре	Document Title	Document ID/Size	Rev			
Eval Board: BOM	LC72725KVSGEVB Bill of Materials ROHS Compliant	LC72725KVSGEVB_BOM.ROHS.PDF - 76 KB	0			
Eval Board: Gerber	LC72725KVSGEVB Gerber Layout Files (Zip Format)	LC72725KVSGEVB_GERBER.ZIP - 117.0 KB	0			
Eval Board: Schematic	LC72725KVSGEVB Schematic	LC72725KVSGEVB_SCHEMATIC.PDF - 129 KB	0			
Eval Board: Test Procedure	LC72725KVSGEVB Test Procedure	LC72725KVSGEVB_TEST_PROCEDURE.PDF - 483 KB	0			

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