

# NB3L02

## 2.8 V, High Precision 1:2 Clock Fanout Buffer

### Description

The NB3L02 is a low-skew, low jitter 1:2 clock fanout buffer, ideal for use in portable end-equipment, such as mobile phones or tablet applications. The MCLK\_IN pin has an integrated AC coupling capacitor and will directly accept a square or sine wave clock input, such as a temperature compensated crystal oscillator (TCXO). The minimum acceptable input amplitude of the sine wave is 800 mV peak-to-peak. The NB3L02 is offered in a 0.4 mm pitch 6-ball, wafer-level chip-scale package (WLCSP) (0.77 mm x 1.17 mm).

### Features

- 800 mV Single Ended Outputs
- Low Phase Noise: -144 dbc/Hz @ 10 kHz Offset
- Ultra Small Package: 0.4 mm Pitch WLCSP6 (0.77 mm x 1.17 mm)
- Exceeds JEDEC ESD Standards: 4000 V HBM, 200 V MM
- Industrial Temperature Range: -40°C to +85°C
- These are Pb-Free Devices

### PIN DESCRIPTIONS

Ball No.	Name	I/O	Description
A1	V <sub>DD</sub>	I	Power Supply Voltage
A2	CLK_OUT1	O	Clock Output 1
B1	MCLK_IN	I	Master Clock Input
B2	GND	-	Ground
C1	GND	-	Ground
C2	CLK_OUT2	O	Clock Output 2



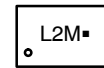
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WLCSP6  
FC SUFFIX  
CASE 567HJ

### MARKING DIAGRAM



- L2 = Specific Device Code
- M = Date Code
- = Pb-Free Package

### PINOUT DIAGRAM

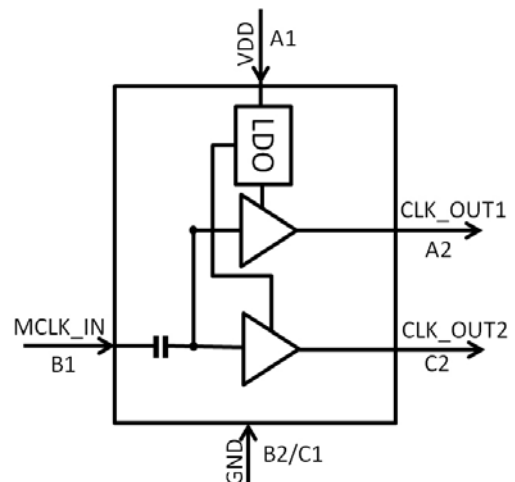
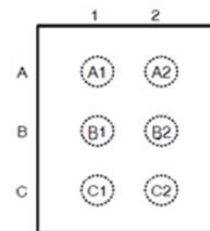


Figure 1. Simplified Block Diagram

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

## NB3L02

**Table 1. MAXIMUM RATINGS**

Symbol	Parameter	Condition	Min	Max	Unit
	Voltage Range (Note 1)	MCLK_IN,CLK_OUT1, CLK_OUT2	-0.3	V <sub>DD</sub> + 0.3	V
I <sub>O</sub>	Continuous Output Current	CLK_OUT1/2		±20	mA
T <sub>J</sub>	Operating Junction Temperature Range		-40	150	°C
T <sub>stg</sub>	Storage Temperature Range		-55	150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. All voltage values are with respect to network ground terminal.

**Table 2. ATTRIBUTES**

Characteristic		Value
ESD Protection	Human Body Model	>4 kV
	Machine Model	>200 V
Moisture Sensitivity	WLCSP6	Level 1
Maximum Soldering Temperature for Lead-free Devices Using a Lead-free Solder Paste		260
Flammability Rating Oxygen Index: 28 to 34		UL 94 V-0 @ 0.125 in
Transistor Count		149
Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test II		

**Table 3. ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = -40°C to +85°C)

Symbol	Characteristic	Min	Typ	Max	Unit
V <sub>DD</sub>	Supply Voltage	2.3	2.8	3.465	V
V <sub>IN</sub>	Input Voltage p-p	800		V <sub>DD</sub>	mV
V <sub>OUT</sub>	Output Voltage p-p	0.6	0.8	1.0	V
I <sub>DDdynamic</sub>	Dynamic Current at 26 MHz		3.5	5	mA
F <sub>IN</sub>	MCLK_IN Frequency Range with 800 mV input p-p	10	26	52	MHz
t <sub>PD</sub>	MCLK_IN to CLK_OUT_n Propagation Delay, input = 1 Vp-p @ 26 MHz	2.0	4.0	6.5	ns
DC	CLK_OUT_n Duty Cycle	45	50	55	%
-	Phase Noise, F <sub>IN</sub> = 26 MHz, input t <sub>r</sub> /t <sub>f</sub> < 1 ns		1 kHz Offset 10 kHz Offset 100 kHz Offset	-134 -144 -148	dbc/Hz dbc/Hz dbc/Hz
t <sub>r</sub> /t <sub>f</sub>	Output Rise Time 20%-80% with 10 pF Load, V <sub>IN</sub> = 800 mVp-p, 26 MHz, input slew rate < 1 ns/V	0.5	0.8	1.2	ns
t <sub>sk</sub>	Channel to Channel Skew		10	30	ps
V <sub>oh</sub>	High Level Output (V <sub>oh</sub> -V <sub>ol</sub> not to exceed V <sub>OUT</sub> )	0.6	0.8	1.0	V
V <sub>ol</sub>	Low Level Output (V <sub>oh</sub> -V <sub>ol</sub> not to exceed V <sub>OUT</sub> )		0		V

# NB3L02

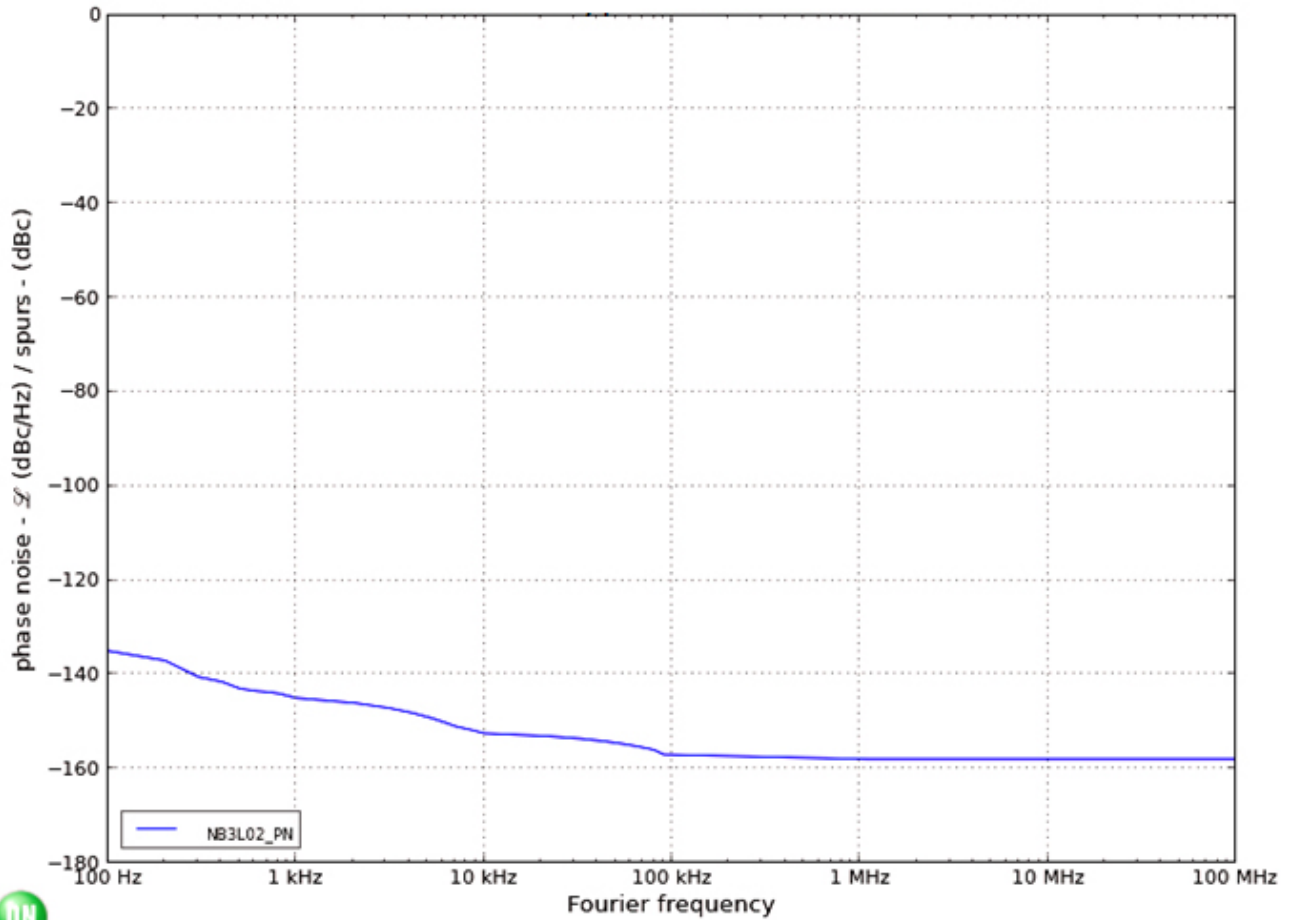


Figure 2. Typical Phase Noise

## ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NB3L02FCT2G	WLCSP6 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

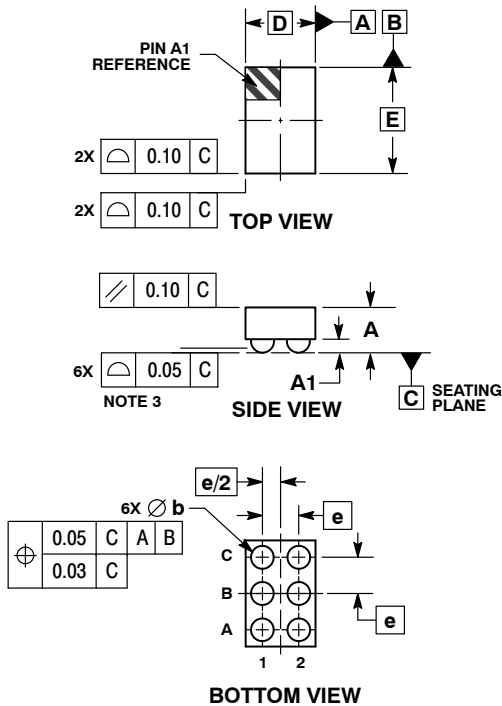
ON Semiconductor®



SCALE 4:1

WLCSP6, 1.17x0.77  
CASE 567HJ  
ISSUE O

DATE 28 MAY 2013

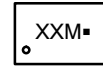


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

DIM	MILLIMETERS	
	MIN	MAX
A	---	0.50
A1	0.13	0.17
b	0.21	0.25
D	0.77 BSC	
E	1.17 BSC	
e	0.40 BSC	

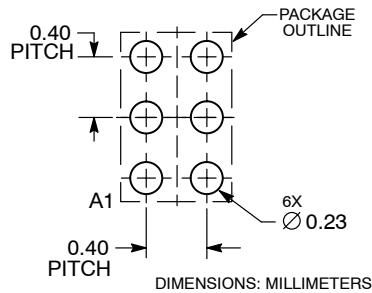
**GENERIC MARKING DIAGRAM\***



- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

**RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>WLCSP6, 1.17X0.77</b>	<b>PAGE 1 OF 1</b>

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