# LA4450

#### **Monolithic Linear IC**

# 2-Channel, 26V, Power Amplifier for Bus and Track in Car Stereo

#### Overview

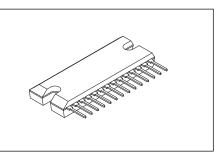
The LA4450 is a single package 2-channel power Amplifier that supports an operating voltage of 26V. It is particularly well suited for use as the bus and track power IC in car stereo applications. Additionally, since the LA4450 can drive  $4\Omega$  loads, it can be used effectively in high-power high-end products. Furthermore, since it supports a high operating voltage and has low distortion, it is also optimal for use in TV and home audio products.

#### Features

- Two channels in a single package
- $P_O = 12W \times 2 (V_{CC} = 26.4V, R_L = 8\Omega, THD = 10\%)$
- $P_O = 20W \times 2 (V_{CC} = 26.4V, R_L = 4\Omega, THD = 10\%)$
- Can drive 4Ω speakers
- Built-in standby switch
- Minimizes impulse noises

#### **Functions**

- Standby switch (active on high (+5V) input)
- On-chip impulse noise protection circuit
- On-chip thermal protection circuit
- On-chip overvoltage and surge protection circuits



SIP14 36.8x13.8 / SIP14H

# Specifications

#### Maximum Ratings at $Ta = 25^{\circ}C$

| Parameter                   | Symbol              | Conditions          | Ratings     | Unit |
|-----------------------------|---------------------|---------------------|-------------|------|
| Maximum supply voltage      | V <sub>CC</sub> max | Rg = 0 (no signal)  | 37          | V    |
| Maximum output current      | I <sub>O</sub> peak |                     | 4           | А    |
| Allowable power dissipation | Pd max              | Infinite heat sink* | 25          | W    |
| Operating temperature       | Topr                |                     | -35 to +80  | °C   |
| Storage temperature         | Tstg                |                     | -40 to +150 | °C   |

Note : \* Set  $V_{CC}$  and  $R_L$  within ranges that do not cause Pd max to exceed 25W.

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### **Operating Conditions** at Ta = 25°C

| Parameter                      | Symbol             | Conditions | Ratings  | Unit |
|--------------------------------|--------------------|------------|----------|------|
| Recommended supply voltage     | V <sub>CC</sub>    |            | 26.4     | V    |
| Recommended load resistance    | RL                 |            | 8        | Ω    |
| Operating supply voltage range | V <sub>CC</sub> op |            | 10 to 30 | V    |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 8 of this data sheet.



| Parameter                 | Querra ha al    |   | Ratings |      |                 | 11.5 |
|---------------------------|-----------------|---|---------|------|-----------------|------|
|                           | Symbol          | Conditions                                      | min     | typ  | max             | Unit |
| Standby current           | Ist             | Standby switch off                              |         | 1    | 30              | μA   |
| Quiescent current         | Icco            | Rg = 0  | 50      | 80   | 140             | mA   |
| Output power              | P <sub>O1</sub> | THD = 10%                                       | 10      | 12   |                 | W    |
|                           | P <sub>O2</sub> | THD = 10%, $R_L = 4\Omega$                      |         | 20   |                 | W    |
| Voltage gain              | VG              | V <sub>O</sub> = 0dBm                           | 49      | 51   | 53              | dB   |
| Total harmonic distortion | THD             | P <sub>O</sub> = 1W                             |         | 0.07 | 0.4             | %    |
| Output noise voltage      | V <sub>NO</sub> | Rg = 0, BPF-BW = 20Hz to 20kHz                  |         | 0.4  | 1.0             | mV   |
| Ripple exclusion ratio    | SVRR            | $Rg = 0$ , $f_R = 100Hz$ , $V_R = 0dBm$         | 45      | 55   |                 | dB   |
| Channel separation        | CHsep           | $V_{O} = 0 dBm, Rg = 10 k\Omega$                | 45      | 55   |                 | dB   |
| Standby control voltage   | Vst             | With a $10k\Omega$ resistor connected at pin 12 | 2.5     |      | V <sub>CC</sub> | V    |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

 $\theta_f - S_f$ 

2

Al heat sink, t = 1.5mm

Heat sink attached with

a torque of 39N · cm.

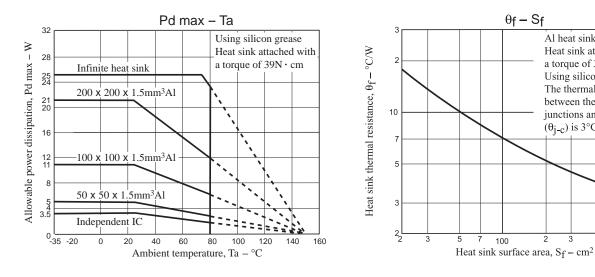
Using silicon grease The thermal resistance

between the transistor

junctions and the case  $(\theta_{j-c})$  is 3°C/W.

5

7 1000



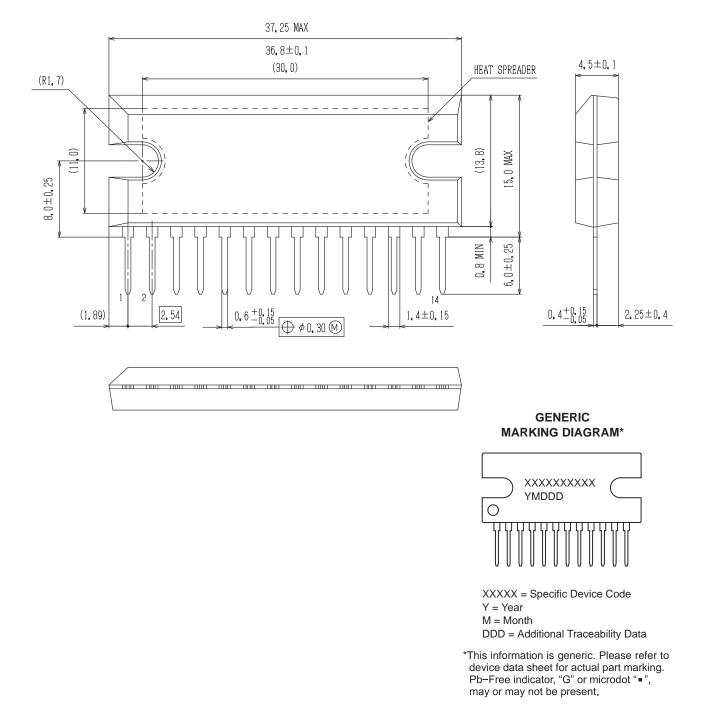


# Package Dimensions

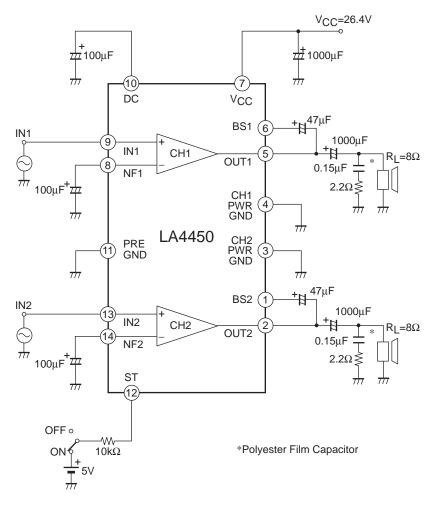
unit : mm

#### SIP14 36.8x13.8 / SIP14H CASE 127AQ

ISSUE A

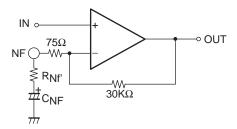


## **Test Circuit**



#### 1. Features and Usage Notes

- Pin 12 is the standby pin. The IC operates when a voltage of 2V or higher is applied through the external resistor R1. Note that the maximum influx current to pin 12 is 500µA.
- Changing the voltage gain



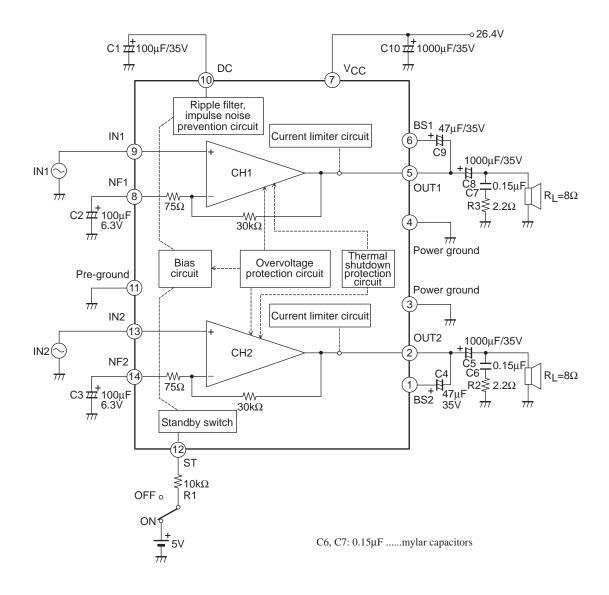
The voltage gain VG can be lowered by connecting an external resistor in series between the NF pin (pins 8 and 14) and  $C_{\rm NF}$ .

$$VG = 20\log \frac{30k\Omega}{75 + R_{Nf}},$$

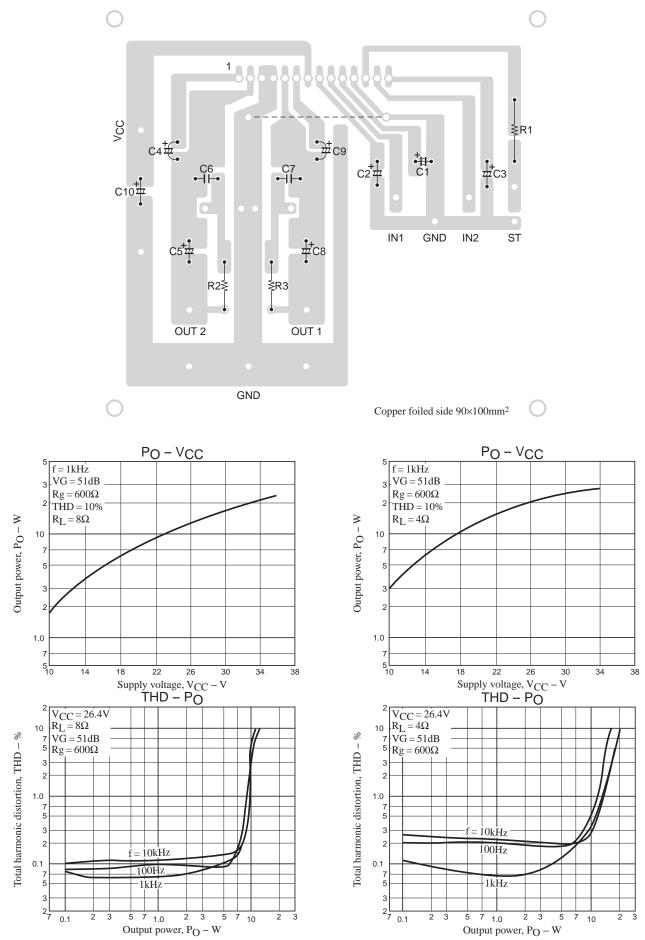
However, since the IC may oscillate if VG is 30dB or lower, use a VG of 36dB or higher.

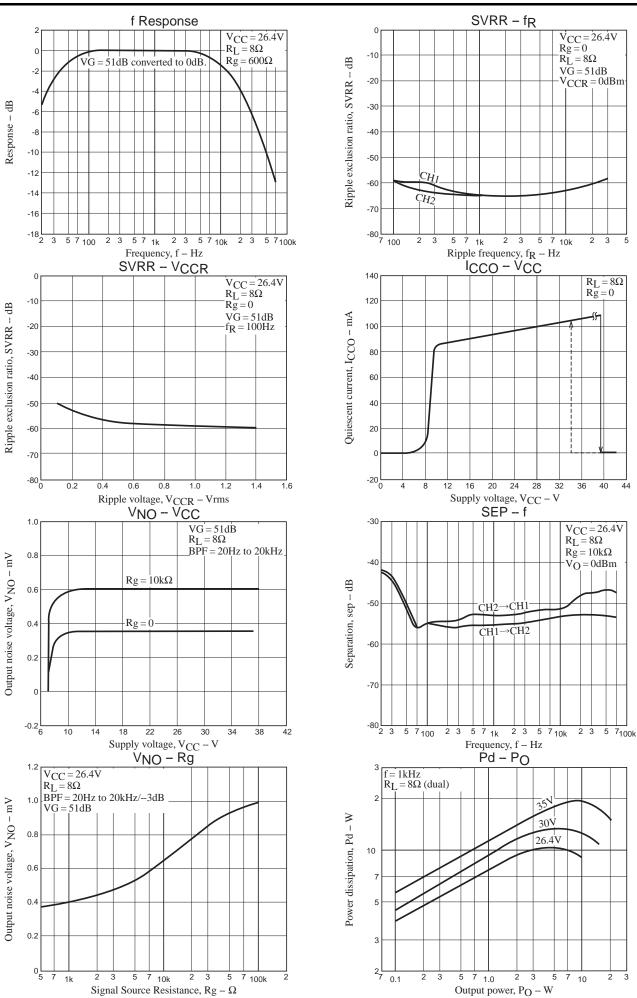
- The LA4450 includes a thermal protection circuit to prevent damage to or destruction of the IC due to abnormal overheating. As a result, the output may be attenuated or cut off if the application heat sinking is inadequate.
- The LA4450 includes an overvoltage protection circuit to protect the IC against power supply surges and abnormal voltages. This circuit has hysteresis characteristics : it operates at between 39 and 40V, and recovers at around 34V.
- Although the LA4450 includes a current limiter circuit to prevent damage due to abnormal currents, care must still be exercised to prevent load shorts and other excessive current conditions.

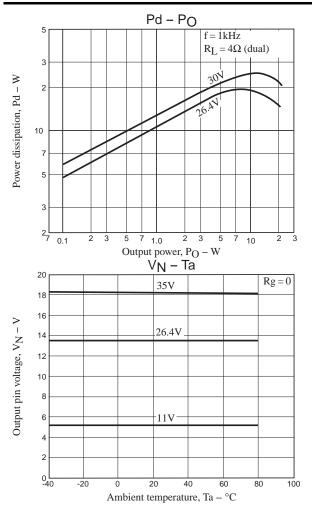
# **Application Circuit Example**

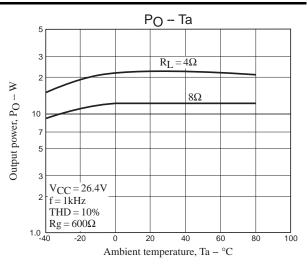


**Printed Circuit Board Pattern Example** 









### **ORDERING INFORMATION**

| Device    | Package                               | Shipping (Qty / Packing) |
|-----------|---------------------------------------|--------------------------|
| LA4450-E  | SIP14 36.8x13.8 / SIP14H<br>(Pb-Free) | 15 / Fan-Fold            |
| LA4450F-E | SIP14 36.8x13.8 / SIP14H<br>(Pb-Free) | 14 / Fan-Fold            |
| LA4450L-E | SIP14 36.8x13.8 / SIP14H<br>(Pb-Free) | 15 / Fan-Fold            |

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa