1. General description

NPN switching transistor in an ultra small DFN1010D-3 (SOT1215) leadless Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

2. Features and benefits

- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- · Suitable for Automatic Optical Inspection (AOI) of solder joint
- Power dissipation comparable to SOT23

3. Applications

· General-purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	200	mA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 10 mA	100	180	300	



40 V, 200 mA NPN switching transistor

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	Е	emitter		
3	С	collector		B — (**)
4	С	collector	2	Ë sym123
			Transparent top view	
			DFN1010D-3 (SOT1215)	

6. Ordering information

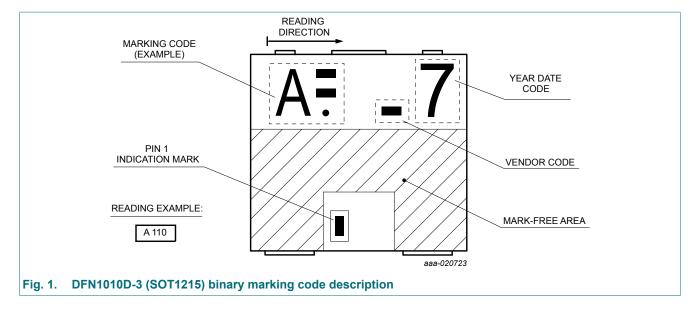
Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMBT3904QA		plastic, leadless thermal enhanced ultra thin small outline package; 3 terminals; 0.75 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1215		

7. Marking

Table 4. Marking codes

Type number	Marking code
PMBT3904QA	X 110



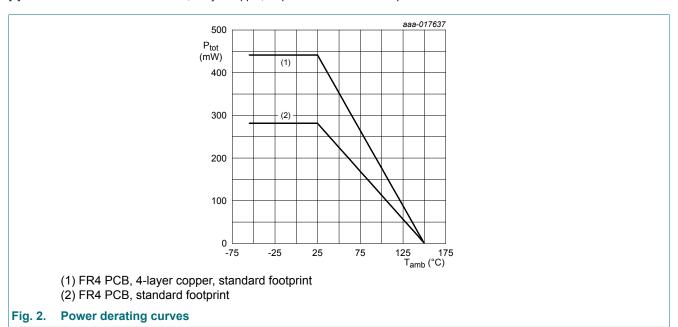
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
Ic	collector current			-	200	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	280	mW
			[3] [2]	-	440	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.



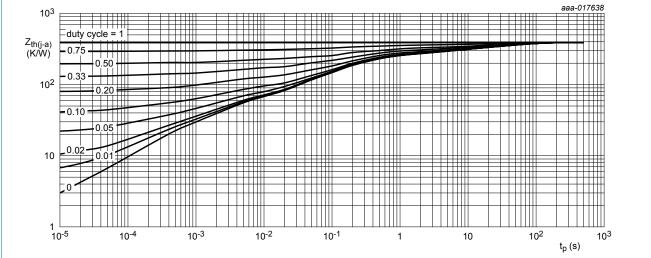
40 V, 200 mA NPN switching transistor

9. Thermal characteristics

Table 6. Thermal characteristics

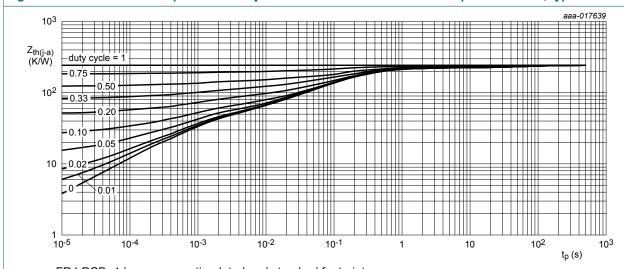
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	447	K/W
			[3] [2]	-	-	285	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.



FR4 PCB, single-sided copper, tin-plated and standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, tin-plated and standard footprint.

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

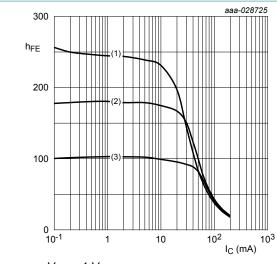
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10. Characteristics

Table 7. Characteristics

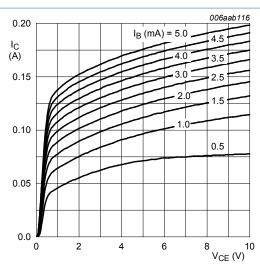
 T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A	60	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 1 mA; I _B = 0 A	40	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I _C = 0 A; I _E = 100 μA	6	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A	-	-	50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A	-	-	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 100 μA	60	180	-	
		V _{CE} = 1 V; I _C = 1 mA	80	180	-	
		V _{CE} = 1 V; I _C = 10 mA	100	180	300	
		V _{CE} = 1 V; I _C = 50 mA	60	105	-	
		V_{CE} = 1 V; I_{C} = 100 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02	30	50	-	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 1 mA	-	75	200	mV
	saturation voltage	I _C = 50 mA; I _B = 5 mA	-	120	300	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA; I _B = 1 mA	650	750	850	mV
	voltage	I _C = 50 mA; I _B = 5 mA	-	850	950	mV
t _d	delay time	I _C = 10 mA; I _{Bon} = 1 mA; I _{Boff} = -1 mA	-	-	35	ns
t _r	rise time		-	-	35	ns
t _{on}	turn-on time		-	-	70	ns
t _s	storage time		-	-	200	ns
t _f	fall time		-	-	50	ns
t _{off}	turn-off time		-	-	250	ns
C _c	collector capacitance	V _{CB} = 5 V; I _E = 0 A; i _e = 0 A; f = 1 MHz	-	-	4	pF
C _e	emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz	-	-	8	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 10 mA; f = 100 MHz	300	-	-	MHz



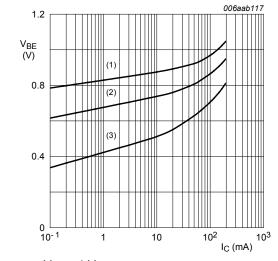
V_{CE} = 1 V (1) T_{amb} = 100 °C (2) T_{amb} = 25 °C (3) T_{amb} = -55 °C

Fig. 5. DC current gain as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



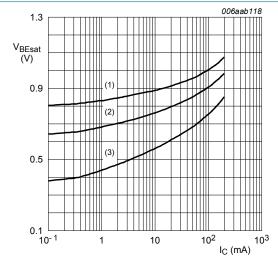
 $V_{CE} = 1 V$

(1) $T_{amb} = -55 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 $I_{\rm C}/I_{\rm B} = 10$

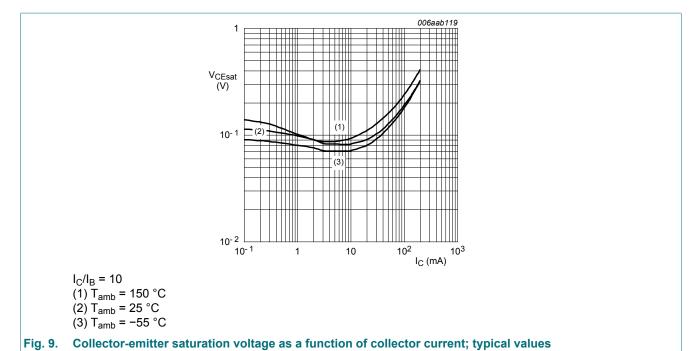
(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 150 \, ^{\circ}C$

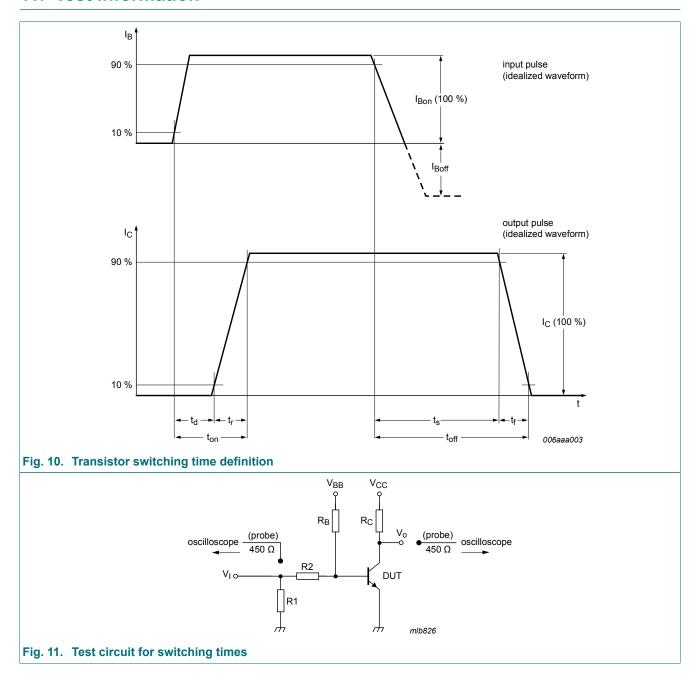
Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

40 V, 200 mA NPN switching transistor



40 V, 200 mA NPN switching transistor

11. Test information

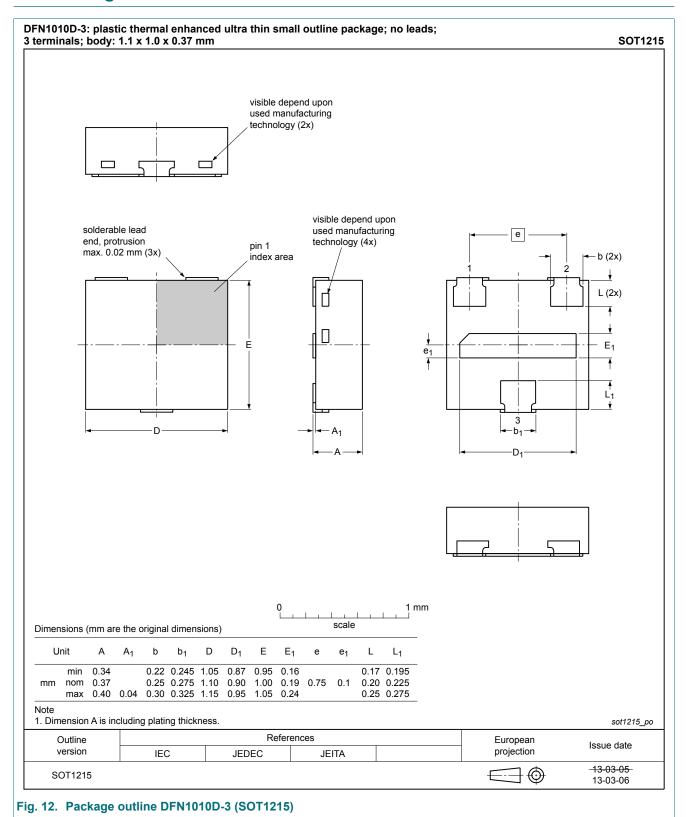


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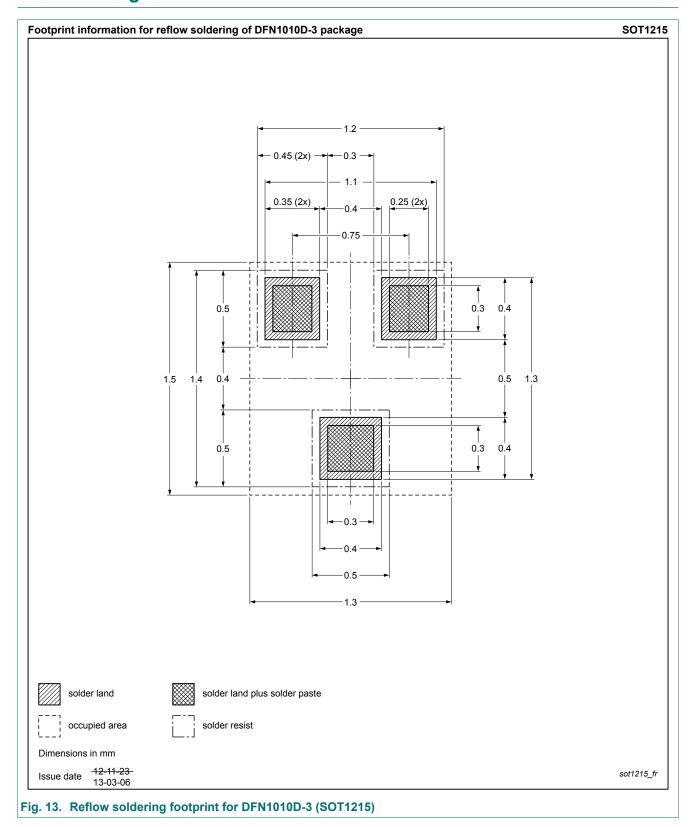
40 V, 200 mA NPN switching transistor

12. Package outline



40 V, 200 mA NPN switching transistor

13. Soldering



40 V, 200 mA NPN switching transistor

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT3904QA v.1	20180829	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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40 V, 200 mA NPN switching transistor

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