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February 2008

FSA5157 0.4 Ω Low-Voltage SPDT Analog Switch

Features

- Typical 0.4Ω On Resistance (R_{ON}) for +2.7V Supply
- FSA5157 Features Less than 12µA I_{CCT} Current When S Input is Lower than V_{CC}
- 0.25Ω Maximum R_{ON} Flatness for +2.7V Supply
- 1.0 x 1.45mm 6-Lead MicroPak™ Package
- Broad V_{CC} Operating Range: 1.65V to 4.3V
- Low THD (0.02% Typical for 32Ω Load)
- High-Current Handling Capability (350mA Continuous Current Under 3.3V Supply)
- Control Logic is 1.8V CMOS Logic Compatible

Applications

- Cellular Phone
- PDA
- Portable Media Player

General Description

The FSA5157 is a low on resistance, low-power, Single Pole Double Throw (SPDT) analog switch. This product has been designed for switching audio signals in applications such as cell phones and portable media players. The ultra-low 0.4Ω impedance, sub- μ A current consumption, and 1.65V to 4.3V operating voltage range makes this product ideal for battery-powered applications. The FSA5157 also features bi-directional operation and break-before-make functionality. This device is fully specified for operation at 1.8V, 2.5V, and 3.3V.

A growing number of applications require the voltage applied to the select input to be lower than the V_{CC} applied. Under this condition, most switches would typically consume over $100\mu A$ of current. This would be an unacceptable level for battery-powered applications. The FSA5157 is designed to minimize current consumption under this condition. The I_{CCT} is specified for <12 μA under a worst-case condition of $V_{CC}=4.3V$ and $V_{IN}=1.8V$.

Ordering Information

Order Number	Top Mark	Package Description	Packing Method
FSA5157P6X	C57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	3000 Units on Tape and Reel
FSA5157L6X	FT	6-Lead MicroPak, 1.0mm Wide	5000 Units on Tape and Reel



All packages are lead free per JEDEC: J-STD-020B standard.

Application Diagram

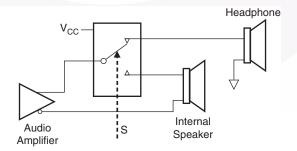


Figure 1. Application Diagram

 $\label{eq:microPak} \mbox{MicroPak}^{\tiny{TM}} \mbox{ is a trademark of Fairchild Semiconductor Corporation}.$

Pin Assignments

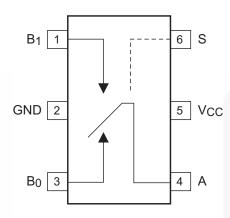


Figure 2. 6-Lead SC70

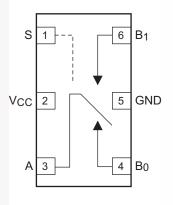


Figure 3. 6-Lead MicroPak™

Truth Tables

Control Input(s)	Function
LOW Logic Level	B ₀ Connected to A
HIGH Logic Level	B ₁ Connected to A

Pin Descriptions

Pin Names	Function
A, B ₀ , B ₁	Data Ports
S	Control Input

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max	Units
V _{CC}	Supply Voltage	-0.5	5.5	V
V _S	Switch Voltage ⁽¹⁾	-0.5	V _{CC} + 0.3V	V
V _{IN}	Input Voltage ⁽¹⁾	-0.5	V _{CC}	V
I _{IK}	Input Diode Current		-50	mA
I _{SW}	Switch Current		350	mA
I _{SWPEAK}	Peak Switch Current (Pulsed at 1ms duration, <10% Duty Cycle)		500	mA
P _D	SC70 6L Package		180	mW
' D	MicroPak 6L Package		180	mW
T _{STG}	Storage Temperature Range	-65	+150	°C
T_J	Maximum Junction Temperature		+150	°C
T_L	Lead Temperature Soldering, 10 seconds		+260	°C
ESD	Human Body Model		8000	V

Note:

 The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Rating
V _{CC}	Supply Voltage	1.65	4.30	V
V _{IN}	Control Input Voltage ⁽²⁾	0	V _{CC}	V
V _{IN}	Switch Input Voltage	0	V _{CC}	V
T _A	Operating Temperature	-40	+85	°C

Note:

2. Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V _{CC} (V)	T _A =+25°C			T _A = -40°C to +85°C		Units
				Min.	Тур.	Max.	Min.	Max.	
			3.6 to 4.3				1.4		
			2.7 to 3.6				1.3		
V_{IH}	Input Voltage High		2.3 to 2.7				1.1		V
			1.65 to 1.95				0.9		
			3.6 to 4.3					0.7	
			2.7 to 3.6					0.5	
V_{IL}	Input Voltage Low		2.3 to 2.7					0.4	V
			1.65 to 1.95					0.4	
I _{IN}	Control Input Leakage	$V_{IN} = 0V \text{ to } V_{CC}$	1.65 to 4.3				-0.5	0.5	μΑ
I _{NO(OFF)} , I _{NC(OFF)}	Off-Leakage Current of Port B ₀ and B ₁	$A = 0.3V, V_{CC} - 0.3V,$ B_0 or $B_1 = 0.3V,$ $V_{CC} - 0.3V$ or Floating	1.95 to 4.3	-10		10	-50	50	nA
I _{A(ON)}	On Leakage Current of Port A	$A = 0.3V, V_{CC} - 0.3V,$ B_0 or $B_1 = 0.3V,$ $V_{CC} - 0.3V$ or Floating	1.95 to 4.3	-20		20	-100	100	nA
	Switch On Resistance ⁽³⁾	$I_{OUT} = 100$ mA, B_0 or $B_1 = 0$ V, 0.7V, 3.6V, 4.3V	4.3		0.36			0.60	Ω
R _{ON}		I _{OUT} = 100mA, B ₀ or B ₁ = 0V, 0.7V, 2.0V, 2.7V	2.7		0.4			0.7	
TON		I _{OUT} = 100mA, B ₀ or B ₁ = 0V, 0.7V, 2.0V, 2.3V	2.3		0.55			0.80	
		$I_{OUT} = 100 \text{mA}, B_0 \text{ or} $ $B_1 = 0.7 \text{V}$	1.65		1.5	2.5		3.0	
			4.3		0.04			0.75	
ΔR_{ON}	On Resistance Matching	I _{OUT} = 100mA,	2.7		0.06			0.13	Ω
ON	Between Channels ⁽⁴⁾	$B_0 \text{ or } B_1 = 0.7V$	2.3		0.12			0.20	22
			1.65		1.0	4			
			4.3					0.25	_
R _{FLAT(ON)}	On Resitance	$I_{OUT} = 100 \text{mA},$ $R_{T} = 0 \text{ M} \text{ to } \text{M}_{T} = 0 \text{ M} \text{ to } \text{M}_{T} = 0 \text{ M}_{T} =$	2.7					0.25	Ω
()	Flatness ⁽⁵⁾	B_0 or $B_1 = 0V$ to V_{CC}	2.3 1.65		0.3			0.3	4
I _{CC}	Quiescent Supply Current	$V_{IN} = 0V \text{ or } V_{CC}, I_{OUT} = 0$	4.3	-100	30	100	-500	500	nA
	Increase in I _{CC} per	V _{IN} = 1.8V	4.3		7.0	12.0		15.0	μА
I _{CCT}	Control Input	V _{IN} = 2.6V	4.3		3.0	6.0		7.0	3/

- 3. On resistance is determined by the voltage drop between A and B pins at the indicated current through the switch.
- 4. $\Delta R_{ON} = R_{ONmax} R_{ONmin}$ measured at identical V_{CC} , temperature, and voltage. 5. Flatness is defined as the difference between the maximum and minimum value of on resistance over the specified range of conditions.

AC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

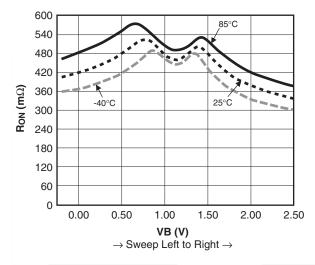
Symbol	Parameter	Conditions	V _{CC} (V)	T,	_A = +25>	(C		40×C to 5×C	Units	Figure					
				Min.	Тур.	Max.	Min.	Max.							
			3.6 to 4.3			55		60							
	Turn On Time	$B_0 \text{ or } B_1 = 1.5 \text{V},$	2.7 to 3.6			60		65	-	F: 7					
t _{ON}	Turn-On Time	$R_L = 50\Omega$, $C_L = 35pF$	2.3 to 2.7			65		70	ns	Figure 7					
			1.65 to 1.95		70			90	1						
			3.6 to 4.3			30		35							
	Turn Off Time	B_0 or $B_1 = 1.5V$,	2.7 to 3.6			35		40]	Figure 7					
t _{OFF}	Turn-Off Time	$R_L = 50\Omega$, $C_L = 35pF$	2.3 to 2.7			40		45	ns	Figure 7					
			1.65 to 1.95		40			55							
	/*		3.6 to 4.3				5								
	Break-Before-	B_0 or $B_1 = 1.5V$,	2.7 to 3.6				5			Figure 8					
t _{BBM}	Make Time	$R_L = 50\Omega$, $C_L = 35pF$	2.3 to 2.7				5		ns						
			1.65 to 1.95				5								
			3.6 to 4.3		6					Figure 11					
0	Charge Injection	ge Injection $C_L = 1.0 \text{ nF}, V_{GEN} = 0V,$ $R_{GEN} = 0\Omega$	2.7 to 3.6		6				pC						
Q			2.3 to 2.7		6										
	7		1.65 to 1.95												
			3.6 to 4.3		-75										
OIDD	Off loolation	$f = 100kHz, R_L = 50\Omega,$	2.7 to 3.6		-75				٦,	Figure 9					
OIRR	Off-Isolation	$C_L = 5pF (Stray)$	2.3 to 2.7		-75				- dB						
			1.65 to 1.95		-75										
			3.6 to 4.3		-75										
Xtalk	Crosstalk	$f = 100kHz, R_L = 50\Omega,$	2.7 to 3.6		-75				dB	Figure 10					
Aldik	Crossiaik	$C_L = 5pF (Stray)$	2.3 to 2.7		-75				ub ub	Figure 10					
			1.65 to 1.95		-70										
BW	-3db Bandwidth	$R_L = 50\Omega$	1.65 to 4.3		80				MHz	Figure 13					
	$\begin{array}{c} R_L = 32\Omega, \ V_{IN} = 2V_{PP}, \\ f = 20Hz \ to \ 20kHz \\ \hline R_L = 32\Omega, \ V_{IN} = 1.5V_{PP}, \\ f = 20Hz \ to \ 20kHz \\ \end{array}$	3.6 to 4.3													
			2.7 to 3.6		0.02										
THD		Distortion R _L = 3		2.3 to 2.7		0.036				%	Figure 14				
		$R_L = 32\Omega, V_{IN} = 1.2V_{PP},$ f = 20Hz to 20kHz	1.65 to 1.95		0.01										

Capacitance

Symbol Parameter		Parameter Conditions V _{CC} (T _A =+25°C			T _A =40°C to +85°C		Units	Figure
				Min.	Тур.	Max.	Min.	Max.		
C _{IN}	Control Pin Input Capacitance	f = 1MHz	0.0		1.5				pF	Figure 12
C _{OFF}	B Port OFF Capacitance	f = 1MHz	4.5		21.0				pF	Figure 12
C _{ON}	A Port ON Capacitance	f = 1MHz	4.5		90.0				pF	Figure 12

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Typical Characteristics



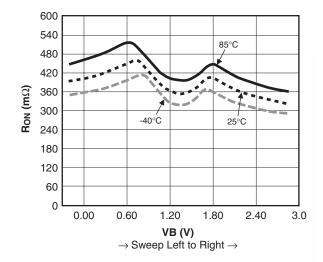


Figure 4. R_{ON} Switch On Resistance, I_{ON} = 100mA, V_{CC} = 2.3V B0

Figure 5. R_{ON} Switch On Resistance, I_{ON} = 100mA, V_{CC} = 2.7V B0

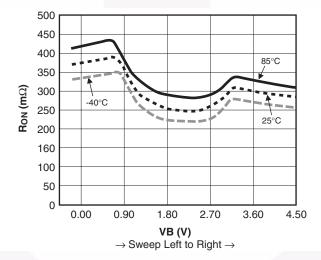
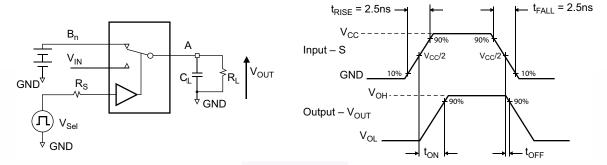


Figure 6. R_{ON} Switch On Resistance, I_{ON} = 100mA, V_{CC} = 4.3V B0

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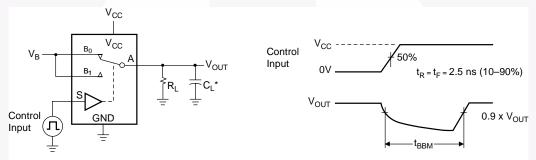
AC Loading and Waveforms



Notes:

- 6. R_L , R_S , and C_L are functions of the application environment (see AC Electrical table for specific values). 7. C_L includes test fixture and stray capacitance.

Figure 7. Turn-Off Timing



*C_L includes fixture and stray capacitance

Figure 8. Break-Before-Make Timing

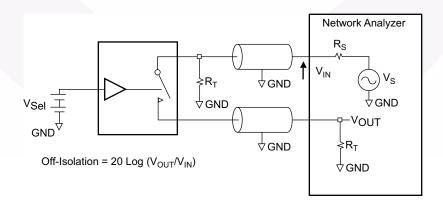


Figure 9. Off Isolation

AC Loading and Waveforms (continued)

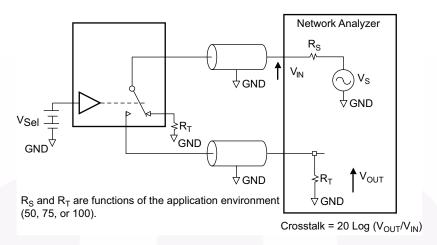


Figure 10. Non-Adjacent Channel-to-Channel Crosstalk

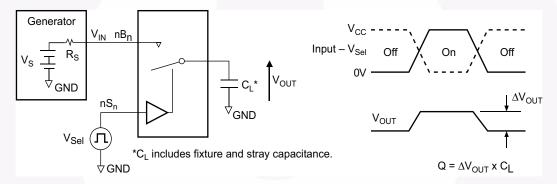


Figure 11. Charge Injection Test

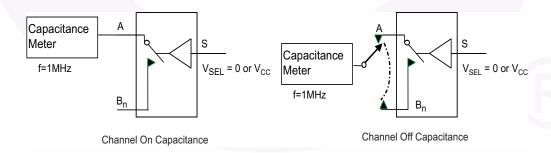


Figure 12. On/Off Capacitance Measurement Setup

AC Loading and Waveforms (continued)

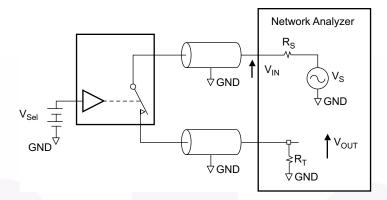


Figure 13. Bandwidth

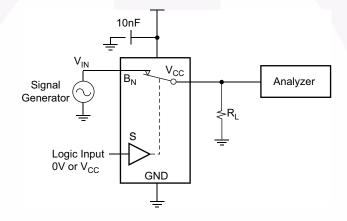


Figure 14. Harmonic Distortion

Physical Dimensions

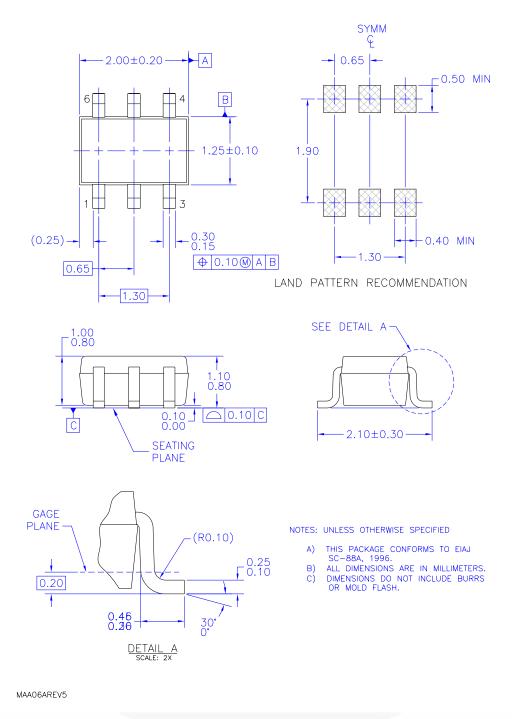
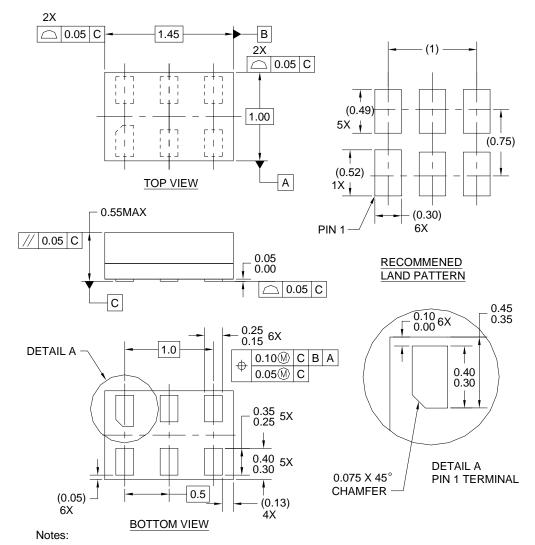


Figure 15. 6-Lead SC70, EIAJ SC88, 1.25mm Wide

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Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

Figure 16. 6-Lead MicroPak™, 1.0mm Wide

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