



NCV74250V1GEVB: 3.3 V / 150 mA LDO Evaluation Board

Evaluation/Development Tool Description

The NCV74250V1GEVB is a Reference Design for LIN transceiver with integrated low drop voltage regulator. The reference design can be interfaced via IO pins to an external microcontroller or simple switch board. The boards are intended to give designers easy, quick and convenient means for evaluation of NCV7425 devices. The design incorporates the lowest bill of material and small board size.



Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
NCV74250V1GEVB	Active	Pb-free	3.3 V / 150 mA LDO Evaluation Board	NCV7425DW0G , NCV7425DW0R2G	Buy Now » Contact Local Sales Office » Inventory

Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: Manual	NCV7425 LIN Transceiver with Voltage Regulator and Reset Pin Evaluation Board	EVBUM2045/D - 500.0 KB	0
Eval Board: BOM	NCV74250V1GEVB Bill of Materials ROHS Compliant	NCV74250V1GEVB_BOM_ROHS.PDF - 96.0 KB	0
Eval Board: Gerber	NCV74250V1GEVB Gerber Layout Files (Zip Format)	NCV74250V1GEVB_GERBER.ZIP - 122.0 KB	0
Eval Board: Schematic	NCV74250V1GEVB Schematic	NCV74250V1GEVB_SCHEMATIC.PDF - 194.0 KB	0
Eval Board: Test Procedure	NCV74250V1GEVB Test Procedure	NCV74250V1GEVB_TEST_PROCEDURE.PDF - 375.0 KB	0

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