XC7WH14

Triple inverting Schmitt trigger

Rev. 5 — 7 February 2013

Product data sheet

1. General description

The XC7WH14 is a high-speed Si-gate CMOS device. This device provides three inverting buffers with Schmitt trigger action. This device is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

2. Features and benefits

- Symmetrical output impedance
- High noise immunity
- ESD protection:
 - ◆ HBM JESD22-A114F exceeds 2000 V
 - ♦ MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101D exceeds 1000 V
- Low power dissipation
- Balanced propagation delays
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Applications

- Wave and pulse shaper for highly noisy environment
- Astable multivibrator
- Monostable multivibrator

4. Ordering information

Table 1. Ordering information

Type number	Package	Package								
	Temperature range	Name	Description	Version						
XC7WH14DP	–40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm	SOT505-2						
XC7WH14DC	–40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1						
XC7WH14GT	–40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 \times 1.95 \times 0.5 mm	SOT833-1						
XC7WH14GD	–40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body $3\times2\times0.5~\text{mm}$	SOT996-2						



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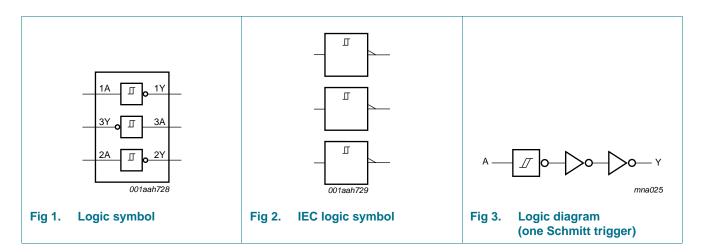
5. Marking

Table 2. Marking codes

Type number	Marking code ^[1]
XC7WH14DP	f14
XC7WH14DC	f14
XC7WH14GT	f14
XC7WH14GD	f14

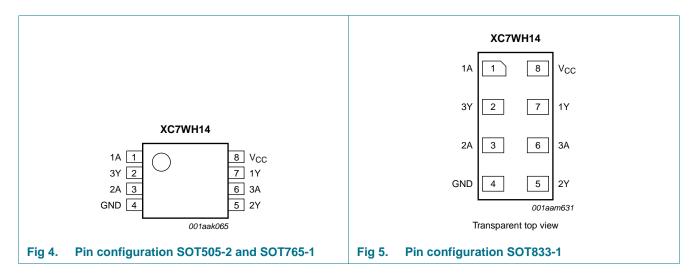
^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram



7. Pinning information

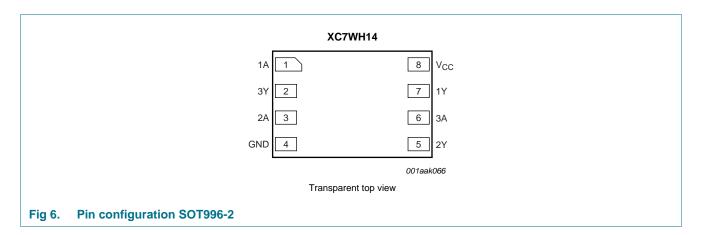
7.1 Pinning



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7.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
1A, 2A, 3A	1, 3, 6	data input
GND	4	ground (0 V)
1Y, 2Y, 3Y	7, 5, 2	data output
V _{CC}	8	supply voltage

8. Functional description

Table 4. Function table [1]

Input nA	Output nY
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level

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9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7.0	V
V_{I}	input voltage		-0.5	+7.0	V
I_{IK}	input clamping current	$V_1 < -0.5 V$	-20	-	mA
I _{OK}	output clamping current	V_O < -0.5 V or V_O > V_{CC} + 0.5 V	<u>[1]</u> _	±20	mA
Io	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$	-	±25	mA
I _{CC}	supply current		-	75	mA
I_{GND}	ground current		-75	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$	[2] _	250	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CC}	supply voltage		2.0	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V_{CC}	V
T_{amb}	ambient temperature		-40	+25	+125	°C

^[2] For TSSOP8 package: above 55 °C the value of P_{tot} derates linearly at 2.5 mW/K.
For VSSOP8 package: above 110 °C the value of P_{tot} derates linearly at 8 mW/K.
For XSON8 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

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11. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C 1	to +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	-
V _{OH} HIGH-level		$V_I = V_{T+}$ or V_{T-}							•	
	output voltage	$I_O = -50 \mu A; V_{CC} = 2.0 V$	1.9	2.0	-	1.9	-	1.9	-	V
		$I_O = -50 \mu A; V_{CC} = 3.0 \text{ V}$	2.9	3.0	-	2.9	-	2.9	-	V
		$I_O = -50 \mu A; V_{CC} = 4.5 V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.40	-	V
		$I_{O} = -8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.8	-	3.70	-	V
V _{OL} LOW-level	$V_I = V_{T+}$ or V_{T-}									
	output voltage	$I_O = 50 \mu A; V_{CC} = 2.0 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 3.0 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 4.5 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		$I_O = 8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
I _I	input leakage current	$V_I = 5.5 \text{ V or GND};$ $V_{CC} = 0 \text{ V to } 5.5 \text{ V}$	-	-	0.1	-	1.0	-	2.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

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11.1 Transfer characteristics

Table 8. Transfer characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V). See Figure 9 and Figure 10.

Symbol	Parameter	Conditions		25 °C		–40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V_{T+}	positive-going	$V_{CC} = 3.0 \text{ V}$	-	-	2.2	-	2.2	-	2.2	V
	threshold voltage	V _{CC} = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
	voltage	V _{CC} = 5.5 V	-	-	3.85	-	3.85	-	3.85	V
V_{T-}	negative-going	$V_{CC} = 3.0 \text{ V}$	0.9	-	-	0.9	-	0.9	-	V
	threshold voltage	V _{CC} = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
	voitage	V _{CC} = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
V_{H}	hysteresis voltage	V_{CC} = 3.0 V	0.3	-	1.2	0.3	1.2	0.25	1.2	V
		V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.35	1.4	V
		$V_{CC} = 5.5 \text{ V}$	0.5	-	1.6	0.5	1.6	0.45	1.6	V

12. Dynamic characteristics

 Table 9.
 Dynamic characteristics

GND = 0 V; for test circuit see Figure 8.

Symbol	Parameter	Conditions			25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
				Min	Тур	Max	Min	Max	Min	Max	
t_{pd}	propagation	nA to nY; see Figure 7	[1]								
	delay	$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	[2]								
		C _L = 15 pF		-	4.2	12.8	1.0	15.0	1.0	16.5	ns
		$C_L = 50 pF$		-	6.0	16.3	1.0	18.5	1.0	20.5	ns
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	[3]								
		C _L = 15 pF		-	3.2	8.6	1.0	10.0	1.0	11.0	ns
		$C_L = 50 pF$		-	4.6	10.6	1.0	12.0	1.0	13.5	ns
C_{PD}	power dissipation capacitance	per buffer; $C_L = 50 \text{ pF}$; $f_i = 1 \text{ MHz}$; $V_I = \text{GND to } V_{CC}$	[4]	-	10	-	-	-	-	-	pF

- [1] t_{pd} is the same as t_{PLH} and t_{PHL} .
- [2] Typical values are measured at V_{CC} = 3.3 V.
- [3] Typical values are measured at $V_{CC} = 5.0 \text{ V}$.
- [4] C_{PD} is used to determine the dynamic power dissipation P_D (μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

 $\Sigma (C_L \times V_{CC}{}^2 \times f_o) = sum$ of the outputs.

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13. Waveforms

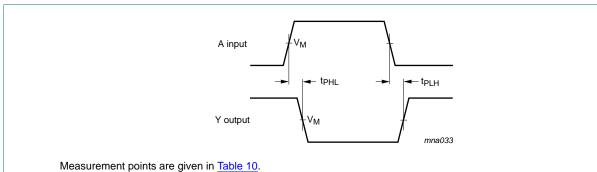
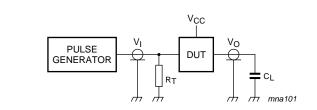


Fig 7. The input (nA) to output (nY) propagation delays

Table 10. Measurement points

Type number	Input	Input C			
	V _I	V _M	V _M		
XC7WH14	GND to V _{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$		



Test data is given in Table 11.

Definitions for test circuit:

 $\ensuremath{C_L}$ = Load capacitance including jig and probe capacitance.

 $R_{T} = \mbox{Termination}$ resistance should be equal to output impedance Z_{0} of the pulse generator.

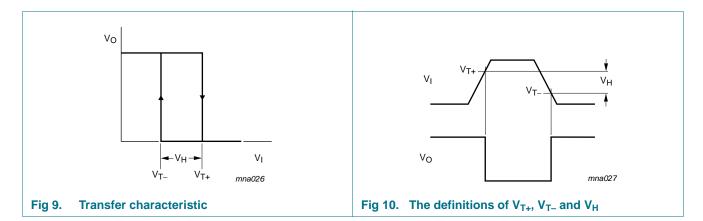
Fig 8. Test circuit for measuring switching times

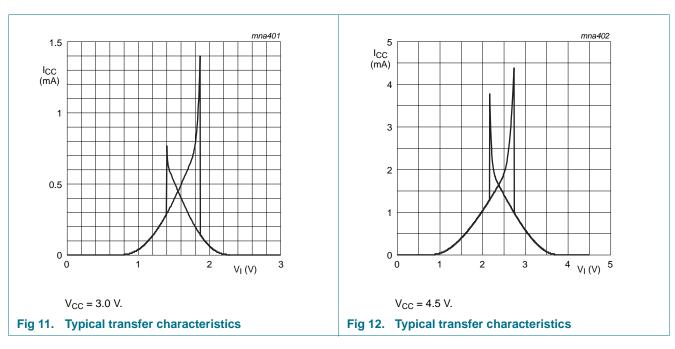
Table 11. Test data

Туре	Input L		Load	Test
	VI	t _r , t _f	CL	
XC7WH14	V _{CC}	≤ 3.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}

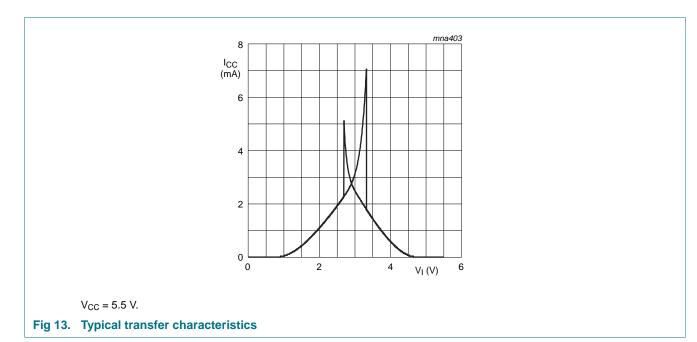
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13.1 Transfer characteristic waveforms





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14. Application information

The slow input rise and fall times cause additional power dissipation, which can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC}$ where:

 P_{add} = additional power dissipation (μW);

f_i = input frequency (MHz);

 t_r = input rise time (ns); 10 % to 90 %;

 t_f = input fall time (ns); 90 % to 10 %;

 $\Delta I_{CC(AV)}$ = average additional supply current (μA).

 $\Delta I_{CC(AV)}$ differs with positive or negative input transitions, as shown in Figure 14.

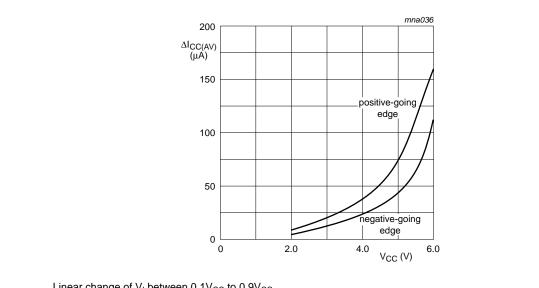
For XC7WH14 used in relaxation oscillator circuit, see Figure 15.

Note to the application information:

1. All values given are typical unless otherwise specified.

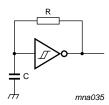
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Linear change of V_{I} between $0.1V_{CC}$ to $0.9V_{CC}$

Fig 14. Average additional I_{CC}

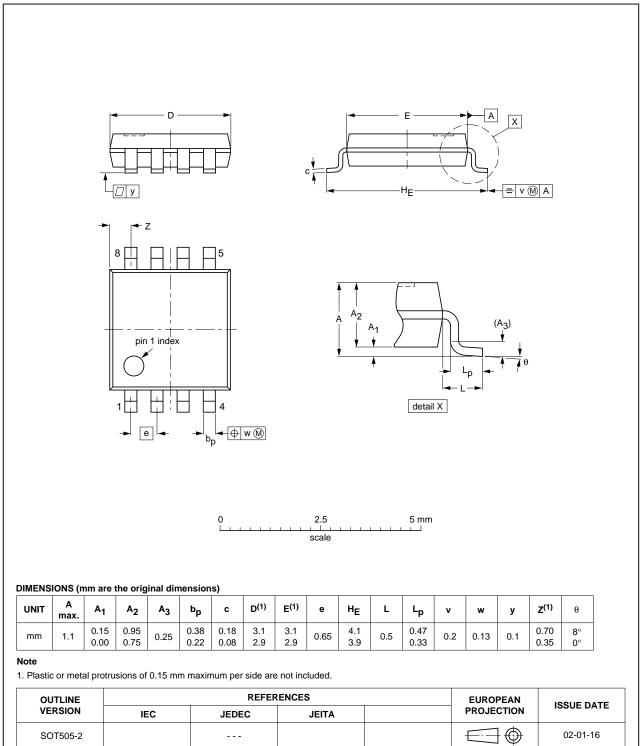


$$f = \frac{1}{T} \approx \frac{1}{0.55 \times RC}$$

Fig 15. Relaxation oscillator using the XC7WH14

15. Package outline

TSSOP8: plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm



OUTLINE		REFER	ENCES	EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT505-2					02-01-16	

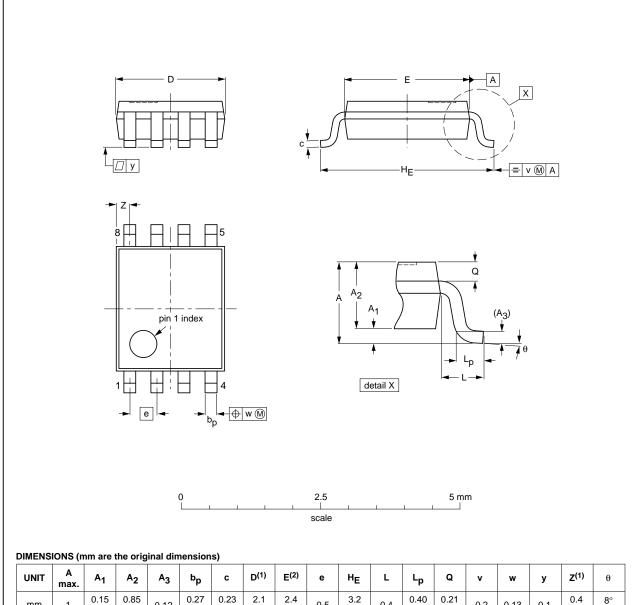
Fig 16. Package outline SOT505-2 (TSSOP8)

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VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽²⁾	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1	0.15 0.00	0.85 0.60	0.12	0.27 0.17	0.23 0.08	2.1 1.9	2.4 2.2	0.5	3.2 3.0	0.4	0.40 0.15	0.21 0.19	0.2	0.13	0.1	0.4 0.1	8° 0°

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included. 2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	1330E DATE	
SOT765-1		MO-187				02-06-07	

Fig 17. Package outline SOT765-1 (VSSOP8)

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XC7WH14

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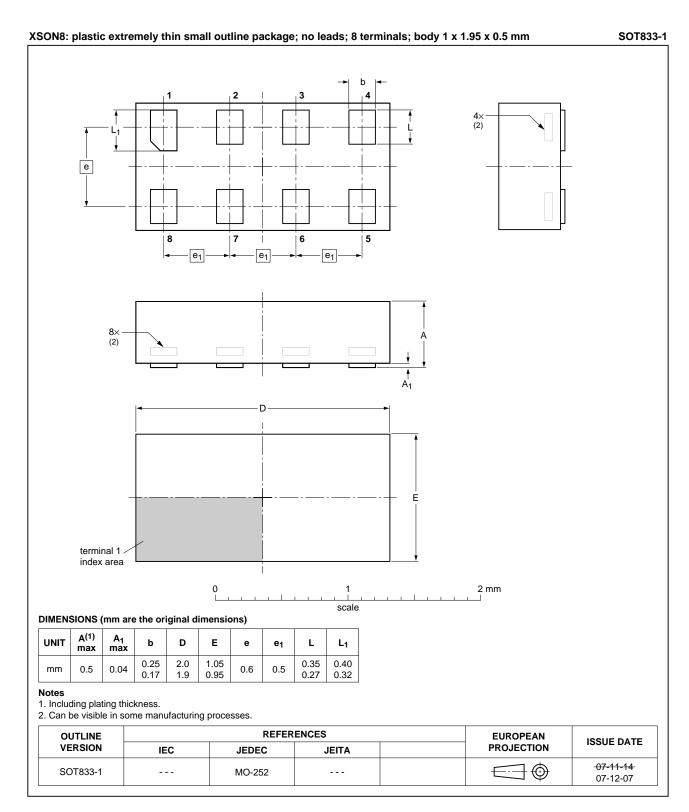


Fig 18. Package outline SOT833-1 (XSON8)

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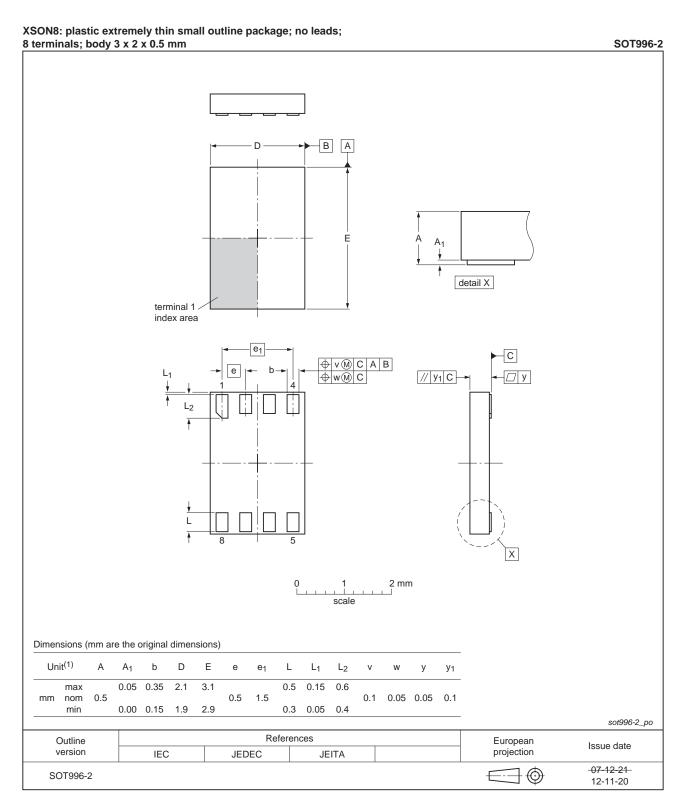


Fig 19. Package outline SOT996-2 (XSON8)

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16. Abbreviations

Table 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

17. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
XC7WH14 v.5	20130207	Product data sheet	-	XC7WH14 v.4
Modifications:	 For type nu 	mber XC7WH14GD XSON	8U has changed to XSC)N8.
XC7WH14 v.4	20111103	Product data sheet	-	XC7WH14 v.3
XC7WH14 v.3	20101118	Product data sheet	-	XC7WH14 v.2
XC7WH14 v.2	20101021	Product data sheet	-	XC7WH14 v.1
XC7WH14 v.1	20090907	Product data sheet	-	-

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18.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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