**Product data sheet** 

## 1. Product profile

## 1.1. General description

PNP power transistors in a medium power SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

**Table 1. Product overview** 

Type number	Package	NPN complement	
	Nexperia	JEDEC	
BCX53T	SOT89	SC-62	BCX56T
BCX53-10T			BCX56-10T
BCX53-16T			BCX56-16T

### 1.2. Features and benefits

- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- · Three current gain selections
- High power dissipation capability
- AEC-Q101 qualified

## 1.3. Applications

- · Linear voltage regulators
- MOSFET drivers
- · High-side switches
- · Power management
- Amplifiers

### 1.4. Quick reference data

#### Table 2. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-80	V
I <sub>C</sub>	collector current		-	-	-1	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	-2	Α



## 80 V, 1 A PNP power bipolar transistors

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h <sub>FE</sub>	DC current gain						
	BCX53T	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	[1]	63	-	250	
	BCX53-10T		[1]	63	-	160	
	BCX53-16T		[1]	100	-	250	

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

## 2. Pinning information

#### Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		C
2	С	collector		R_
3	В	base		
			$\overline{3}$ $\overline{2}$ $\overline{1}$	E
				006aaa231

# 3. Ordering information

### **Table 4. Ordering information**

Type number	Package	Package				
	Name	Description	Version			
BCX53T	SC-62	plastic, surface-mounted package; 3 leads; 1.5 mm pitch;	SOT89			
BCX53-10T		4.5 mm x 2.5 mm x 1.5 mm body				
BCX53-16T						

# 4. Marking

#### Table 5. Marking

Type number	Marking code
BCX53T	A4
BCX53-10T	A2
BCX53-16T	A3

**Product data sheet** 

# 5. Limiting values

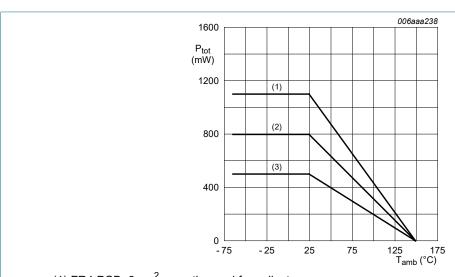
#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-100	V
V <sub>CEO</sub>	collector-emitter voltage	open base	open base		-80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current		-		-1	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-2	Α
I <sub>B</sub>	base current			-	-200	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-300	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	500	mW
			[2]	-	800	mW
			[3]	-	1100	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB; 6 cm<sup>2</sup> mounting pad for collector
- (2) FR4 PCB; 1 cm<sup>2</sup> mounting pad for collector
- (3) FR4 PCB; standard footprint

Fig. 1. Power derating curves

**Product data sheet** 

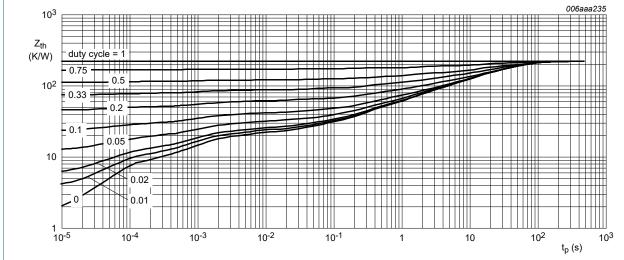
## 6. Thermal characteristics

#### **Table 7. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

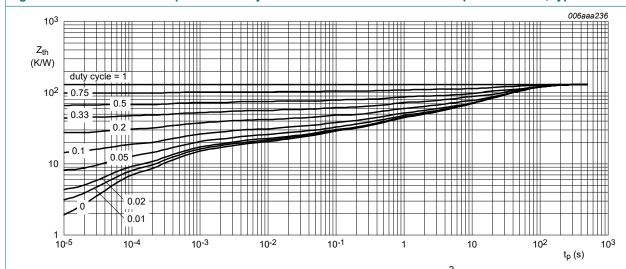
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]	-	-	157	K/W
			[3]	-	-	114	K/W

- [1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



FR4 PCB; single-sided copper; tin-plated and standard footprint

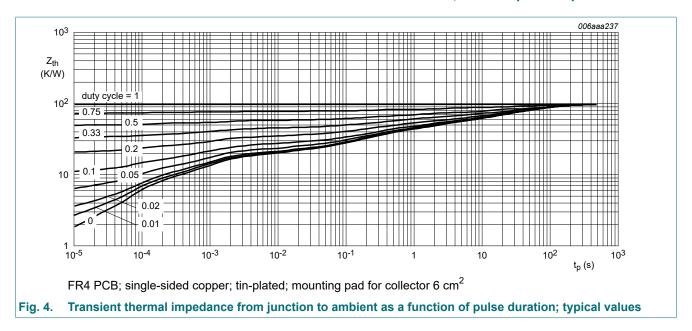
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 80 V, 1 A PNP power bipolar transistors



## 7. Characteristics

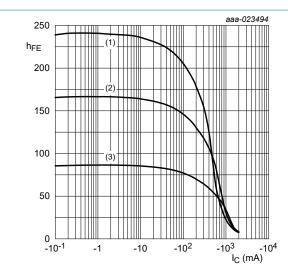
#### **Table 8. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_C = -100 \ \mu A; I_E = 0 \ A$		-100	-		V	
$V_{(BR)CEO}$	collector-emitter breakdown voltage	I <sub>C</sub> = -2 mA; I <sub>E</sub> = 0 A		-80	-		V	
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = -100 μA; I <sub>C</sub> = 0 A		-5	-		V	
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A		-	-	-100	nA	
	cut-off current	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ °C}$		-	-	-10	μΑ	
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A		-	-	-100	nA	
h <sub>FE</sub>	DC current gain							
	BCX53T, -10T, -16T	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -5 mA		63	-	-		
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA	[1]	40	-	-		
	BCX53T	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	[1]	63	-	250		
	BCX53-10T	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	[1]	63	-	160		
	BCX53-16T	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	[1]	100	-	250		
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA	[1]	-	-	-500	mV	
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA	[1]	-	-	-1	V	
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -50 mA; f = 100 MHz		-	140	-	MHz	
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	7	-	рF	

[1] pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

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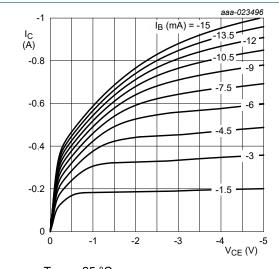


$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

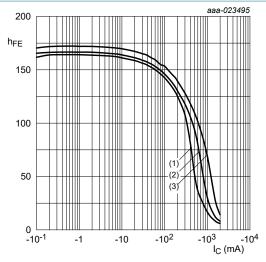
(3) 
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values



 $T_{amb} = 25 \, ^{\circ}C$ 

Fig. 7. Collector current as a function of collectoremitter voltage; typical values



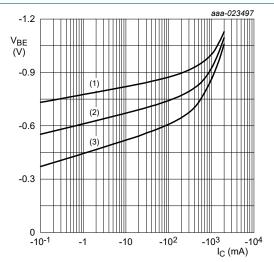
$$T_{amb} = 25 \, ^{\circ}C$$

$$(1) V_{CE} = -1 V$$

(2) 
$$V_{CE} = -2 V$$

(3) 
$$V_{CE} = -5 V$$

Fig. 6. DC current gain as a function of collector current; typical values



$$V_{CE} = -2 V$$

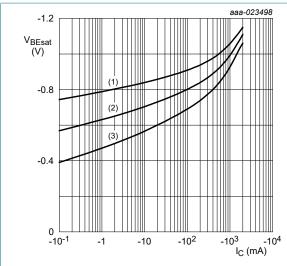
(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 8. Base-emitter voltage as a function of collector current; typical values

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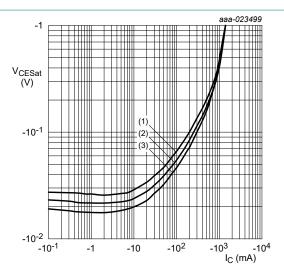
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb}$$
 = 100 °C

Base-emitter saturation voltage as a function of Fig. 10. Collector-emitter saturation voltage as a Fig. 9. collector current; typical values



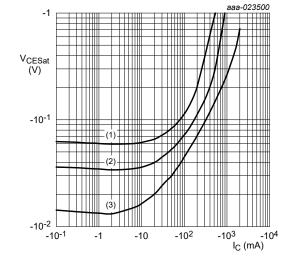
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

function of collector current; typical values



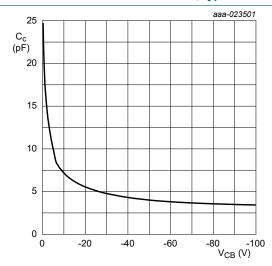
 $T_{amb}$  = 25 °C

(1) 
$$I_{\rm C}/I_{\rm B} = 50$$

(2) 
$$I_C/I_B = 20$$

(3)  $I_C/I_B = 5$ 

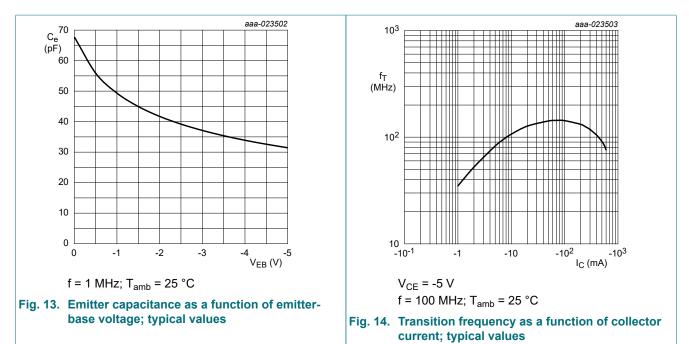
Fig. 11. Collector-emitter saturation voltage as a function of collector current; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ °C}$ 

Fig. 12. Collector capacitance as a function of collectorbase voltage; typical values

#### 80 V, 1 A PNP power bipolar transistors

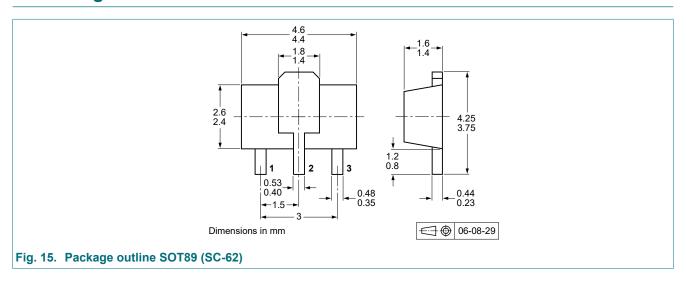


## 8. Test information

## 8.1. Quality information

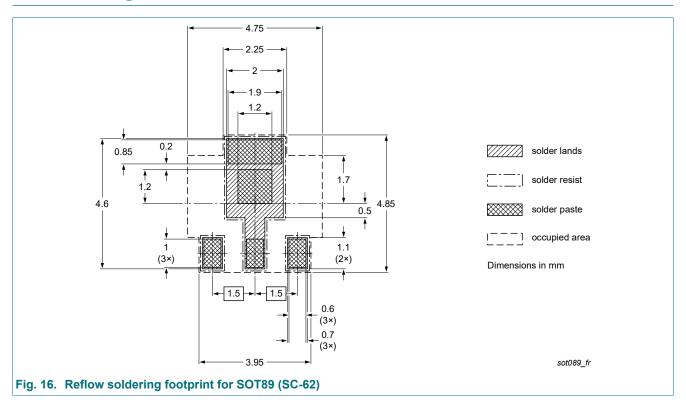
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

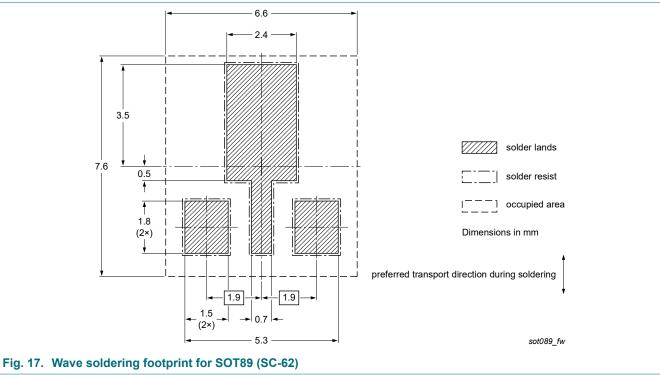
# 9. Package outline



80 V, 1 A PNP power bipolar transistors

# 10. Soldering





**Product data sheet** 

80 V, 1 A PNP power bipolar transistors

# 11. Revision history

### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCX53T_SER v.1	20190822	Product data sheet	-	-

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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