

Product data sheet

# **Product profile**

## 1.1 General description

Two planar Schottky barrier double diodes with common cathodes and an integrated guard ring for stress protection encapsulated in a SOT666 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

#### 1.2 Features and benefits

- Low forward voltage
- Low capacitance
- AEC-Q101 qualified
- Ultra small and flat lead SMD plastic package
- Excellent coplanarity and improved thermal behavior

### 1.3 Applications

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Reverse polarity protection

#### 1.4 Quick reference data

Table 1. Quick reference data

| Symbol                         | Parameter       | Conditions             | Min | Тур | Max | Unit |
|--------------------------------|-----------------|------------------------|-----|-----|-----|------|
| Per diode                      |                 |                        |     |     |     |      |
| I <sub>F</sub>                 | forward current |                        | -   | -   | 200 | mA   |
| $V_R$                          | reverse voltage |                        | -   | -   | 30  | V    |
| V <sub>F</sub> forward voltage | forward voltage |                        | [1] |     |     |      |
|                                |                 | $I_F = 0.1 \text{ mA}$ | -   | -   | 240 | mV   |
|                                |                 | $I_F = 1 \text{ mA}$   | -   | -   | 320 | mV   |
|                                |                 | $I_F = 10 \text{ mA}$  | -   | _   | 400 | mV   |
|                                |                 | $I_F = 30 \text{ mA}$  | -   | _   | 500 | mV   |
|                                |                 | $I_F = 100 \text{ mA}$ | -   | -   | 800 | mV   |

<sup>[1]</sup> Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 



### Two Schottky barrier double diodes

# 2. Pinning information

Table 2. Pinning

| IUDIC Z. | 1 11111119                  |                    |                |
|----------|-----------------------------|--------------------|----------------|
| Pin      | Description                 | Simplified outline | Graphic symbol |
| 1        | anode (diode 1)             |                    |                |
| 2        | anode (diode 2)             | 6 5 4              | 6 5 4          |
| 3        | common cathode (diode 3, 4) |                    |                |
| 4        | anode (diode 3)             |                    |                |
| 5        | anode (diode 4)             | 1 2 3              |                |
| 6        | common cathode (diode 1, 2) | 1 2 3              | 1 2 3          |
|          |                             |                    | sym057         |

# 3. Ordering information

Table 3. Ordering information

| Type number | Package |  |         |  |
|-------------|---------|--|---------|--|
|             | Name    | Description                              | Version |  |
| BAT54CV     | -       | plastic surface-mounted package; 6 leads | SOT666  |  |

# 4. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| BAT54CV     | C5           |

# 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                           | Conditions                                 | Min   | Max  | Unit |
|------------------|-------------------------------------|--|-------|------|------|
| Per diode        |                                     |  |       |      |      |
| $V_R$            | reverse voltage                     |  | -     | 30   | V    |
| I <sub>F</sub>   | forward current                     |  | -     | 200  | mA   |
| I <sub>FRM</sub> | repetitive peak forward current     | $t_p \leq 10 \text{ ms; } \delta \leq 0.5$ | -     | 0.85 | Α    |
| I <sub>FSM</sub> | non-repetitive peak forward current | square wave;<br>t <sub>p</sub> = 8.3 ms    | [1] - | 2    | Α    |

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#### Two Schottky barrier double diodes

 Table 5.
 Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter               | Conditions                  | Min   | Max  | Unit |
|------------------|-------------------------|-----------------------------|-------|------|------|
| Per device       | e, one diode loaded     |                             |       |      |      |
| P <sub>tot</sub> | total power dissipation | $T_{amb} \le 25  ^{\circ}C$ | [2]   |      |      |
|                  |                         |                             | [3] _ | 350  | mW   |
|                  |                         |                             | [4] _ | 420  | mW   |
| Tj               | junction temperature    |                             | -     | 125  | °C   |
| T <sub>amb</sub> | ambient temperature     |                             | -65   | +125 | °C   |
| T <sub>stg</sub> | storage temperature     |                             | -65   | +150 | °C   |
|                  |                         |                             |       |      |      |

<sup>[1]</sup>  $T_i = 25$  °C prior to surge.

### 6. Thermal characteristics

Table 6. Thermal characteristics

| Symbol         | Parameter  | Conditions  | Min          | Тур | Max | Unit |
|----------------|--|-------------|--------------|-----|-----|------|
| Per device     | e, one diode loaded                              |             |              |     |     |      |
| $R_{th(j-a)}$  | thermal resistance from junction to ambient      | in free air | [1][2]       |     |     |      |
|                |  |             | [3] _        | -   | 360 | K/W  |
|                |  |             | [4] _        | -   | 300 | K/W  |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point |             | <u>[5]</u> _ | -   | 175 | K/W  |

<sup>[1]</sup> For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

<sup>[3]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

<sup>[4]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

<sup>[3]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[4]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[5]</sup> Soldering point of cathode tab.

### Two Schottky barrier double diodes

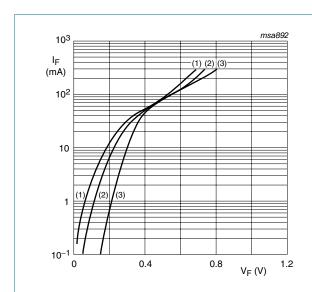
# 7. Characteristics

Table 7. Characteristics

 $T_{amb} = 25$  °C unless otherwise specified.

| · amb = 0      | e dilicos etilei wise s | 1                       |            |     |     |      |
|----------------|-------------------------|-------------------------|------------|-----|-----|------|
| Symbol         | Parameter               | Conditions              | Min        | Тур | Max | Unit |
| Per diode      | 9                       |                         |            |     |     |      |
| V <sub>F</sub> | forward voltage         |                         | <u>[1]</u> |     |     |      |
|                |                         | I <sub>F</sub> = 0.1 mA | -          | -   | 240 | mV   |
|                |                         | I <sub>F</sub> = 1 mA   | -          | -   | 320 | mV   |
|                |                         | I <sub>F</sub> = 10 mA  | -          | -   | 400 | mV   |
|                |                         | I <sub>F</sub> = 30 mA  | -          | -   | 500 | mV   |
|                |                         | I <sub>F</sub> = 100 mA | -          | -   | 800 | mV   |
| I <sub>R</sub> | reverse current         | V <sub>R</sub> = 25 V   | -          | -   | 2   | μΑ   |
| $C_d$          | diode capacitance       | $V_R = 1 V; f = 1 MHz$  | -          | -   | 10  | pF   |
|                |                         |                         |            |     |     |      |

[1] Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

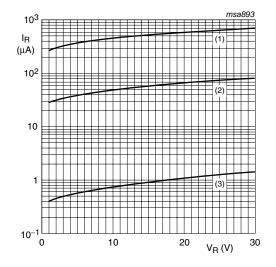




<sup>(2)</sup> T<sub>amb</sub> = 85 °C

(3)  $T_{amb} = 25 \, ^{\circ}C$ 

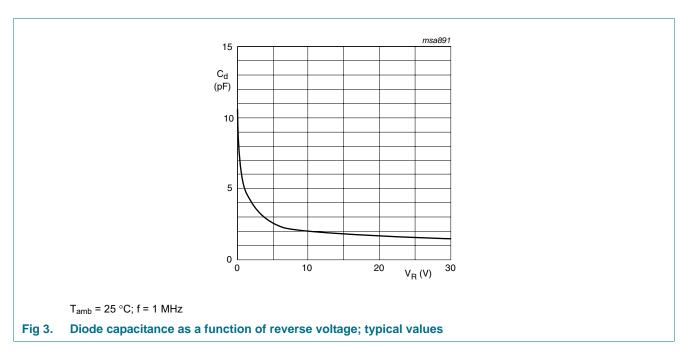
Fig 1. Forward current as a function of forward voltage; typical values



- (1) T<sub>amb</sub> = 125 °C
- (2) T<sub>amb</sub> = 85 °C
- (3) T<sub>amb</sub> = 25 °C

Fig 2. Reverse current as a function of reverse voltage; typical values

#### Two Schottky barrier double diodes

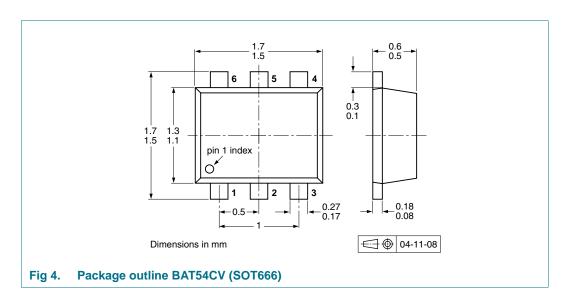


### 8. Test information

## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 9. Package outline



BAT54CV

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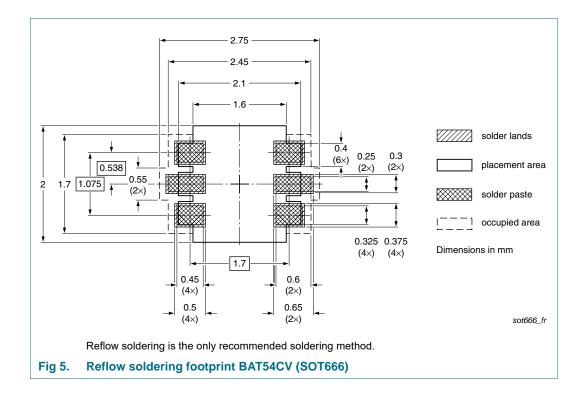
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Two Schottky barrier double diodes

# 10. Packing information

Please refer to packing information on www.nexperia.com.

# 11. Soldering



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# 12. Revision history

### Table 9. Revision history

| Document ID    | Release date   | Data sheet status                  | Change notice | Supersedes |
|----------------|--|------------------------------------|---------------|------------|
| BAT54CV v.3    | 20101115   | Product data sheet                 | -             | BAT54CV_2  |
| Modifications: | Section 1.2 "  | Features and benefits": an         | nended.       |            |
|                | <ul> <li>Table 1 "Quid</li> </ul>  | ck reference data": update         | d.            |            |
|                | <ul> <li><u>Table 5 "Limiting values"</u>: P<sub>tot</sub> amended.</li> </ul>   |                                    |               |            |
|                | <ul> <li><u>Table 6 "Thermal characteristics"</u>: R<sub>th(j-a)</sub> amended, R<sub>th(j-sp)</sub> added.</li> </ul> |                                    |               |            |
|                | Figure 4: superseded by minimized outline drawing.   |                                    |               |            |
|                | <ul> <li>Section 8 "Te</li> </ul>  | est information": added.           |               |            |
|                | Section 11 "S  | Soldering": added.                 |               |            |
|                | <ul> <li>Section 13 "L</li> </ul>  | <u>egal information"</u> : updated | d.            |            |
| BAT54CV_2      | 20100115   | Objective data sheet               | -             | BAT54CV_1  |
| BAT54CV_1      | 20040922   | Objective data sheet               | -             | -          |
|                |  |                                    |               |            |

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# 13. Legal information

#### 13.1 Data sheet status

| Document status[1][2]          | Product status[3] | Definition  |
|--------------------------------|-------------------|---|
| Objective [short] data sheet   | Development       | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification     | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production        | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design
- [2] The term 'short data sheet' is explained in section "Definitions"
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### Two Schottky barrier double diodes

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

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### Two Schottky barrier double diodes

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