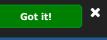
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## NCP51460SN33GEVB: 20 mA Precise LDO Evaluation Board

20 mA Precise LDO Evaluation Board

The NCP51460 is a high performance, low power precision voltage reference. It can supply output current up to 20 mA at a 3.3 V fixed output voltage. Excellent line and load regulation characteristics, very good PSRR, low Output Noise and low Quiescent Current makes the NCP51460 ideal for precision regulator applications. The device is designed to be stable without an output capacitor which makes the device ideal for space constrained applications or where fast rise times are required. An optional output capacitor (which is present on the evaluation board) may be used to improve the load transient response. Protective features include Short Circuit and Reverse Input Voltage Protection. In the case of reverse input voltage on the output pin, the reference will not conduct any current protecting the device from damage. The NCP51460 is packaged in a 3-lead surface mount SOT-23 package.

Status Compliance Short Description

NCP54160SN33GEVB Bill of Materials ROHS

NCP51460SN33GEVB Gerber Layout Files (Zip

NCP51460SN33GEVBEVB Schematic

NCP51460SN33GEVB Test Procedure

Pb-free

Document Title

Compliant

Format)

**Evaluation/Development Tool Information** 

Active

Product

Туре

Eval Board: BOM

Eval Board: Gerber

Eval Board: Test Procedure

Eval Board: Schematic

NCP51460SN33GEVB

**Technical Documents** 



Action >> Contact

Local Sales Office

Rev

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Parts Used

NCP54160SN33GEVB\_BOM\_ROHS.PDF - 98.0 KB

NCP51460SN33GEVBEVB\_SCHEMATIC.PDF - 124.0

NCP51460SN33GEVB\_GERBER.ZIP - 10.0 KB

NCP51460SN33GEVB\_TEST\_PROCEDURE.PDF -

NCP51460SN33T1G

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