Product data sheet

1. General description

Planar Schottky barrier diode with an integrated guard ring for stress protection, encapsulated in a very small and flat lead SOD323F (SC-90) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- Reverse voltage V_R ≤ 100 V
- Very small and flat lead SMD plastic package
- Low capacitance
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- High-speed switching
- Line termination
- · Voltage clamping
- · Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_R	reverse voltage		-	-	100	V
V _F		I_F = 250 mA; t_p ≤ 300 μs; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	710	850	mV
I _R		V_R = 75 V; $t_p \le 300 \ \mu s$; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	1	4	μΑ



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	1 2	К .[К.] -А
2	Α	anode	SC-90 (SOD323F)	aaa-003679

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BAT46WJ-Q		plastic, surface-mounted package; 2 leads; 1.7 mm x 1.25 mm x 0.7 mm body	SOD323F		

7. Marking

Table 4. Marking codes

Type number	Marking code
BAT46WJ-Q	JK

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage			-	100	V
l _F	forward current			-	250	mA
I _{FSM}	non-repetitive peak forward current	t_p < 10 ms; square wave; $T_{j(init)}$ = 25 °C		-	2.5	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	400	mW
			[3] [2]	-	715	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

^[2] Reflow soldering is the only recommended soldering method.

^[3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for cathode 1 cm².

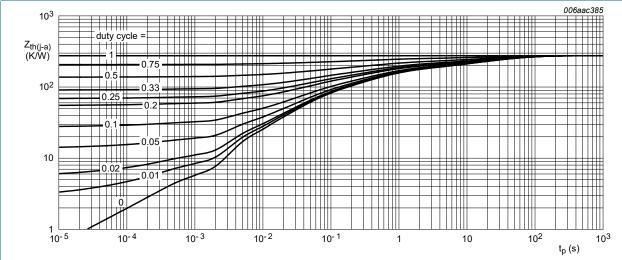
Schottky barrier diode

9. Thermal characteristics

Table 6. Thermal characteristics

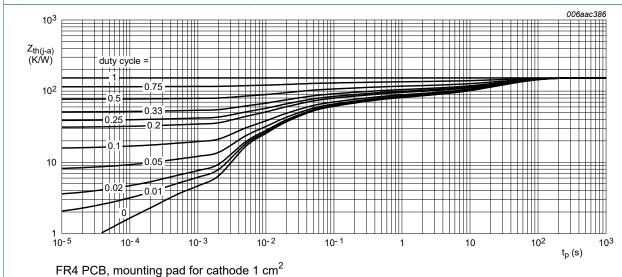
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	310	K/W
jur	junction to ambient		[3] [2]	-	-	175	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	35	K/W

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Reflow soldering is the only recommended soldering method.
- [2] [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- Soldering point of cathode tab.



FR4 PCB, standard footprint

Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 1.



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 2.

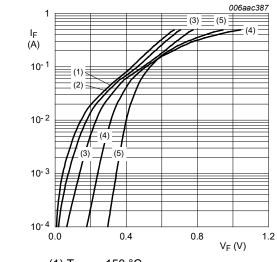
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I_F = 0.1 mA; $t_p \le 300$ μs; $δ \le 0.02$; pulsed; T_{amb} = 25 °C	-	175	200	mV
		I_F = 10 mA; $t_p \le 300$ μs; $δ \le 0.02$; pulsed; T_{amb} = 25 °C	-	315	350	mV
		I_F = 10 mA; t_p ≤ 300 μs; δ ≤ 0.02; pulsed; T_j = -40 °C	-	-	470	mV
		I_F = 50 mA; t_p ≤ 300 μs; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	415	475	mV
		I_F = 50 mA; $t_p \le 300$ μs; $δ \le 0.02$; pulsed; T_j = -40 °C	-	-	560	mV
		I_F = 250 mA; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; pulsed; T_{amb} = 25 °C	-	710	850	mV
R	reverse current	V_R = 1.5 V; $t_p \le 300 \ \mu s$; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	0.2	0.5	μΑ
		V_R = 1.5 V; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; pulsed; $T_j = 60 \text{ °C}$	-	-	12	μΑ
		V_R = 10 V; $t_p \le 300 \mu s$; $\delta \le 0.02$; pulsed; T_{amb} = 25 °C	-	0.3	0.8	μΑ
		V_R = 10 V; $t_p \le 300 \mu s$; δ ≤ 0.02; pulsed; T_j = 60 °C	-	-	20	μΑ
		$V_R = 50 \text{ V}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ pulsed; $T_{amb} = 25 ^{\circ}\text{C}$	-	0.7	2	μΑ
		V_R = 50 V; $t_p \le 300 \mu s$; δ ≤ 0.02; pulsed; T_j = 60 °C	-	-	44	μΑ
		V_R = 75 V; $t_p \le 300 \mu s$; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	1	4	μΑ
		V_R = 75 V; $t_p \le 300 \mu s$; δ ≤ 0.02; pulsed; T_j = 60 °C	-	-	80	μΑ
		V_R = 100 V; $t_p \le 300 \mu s$; $δ \le 0.02$; pulsed; T_{amb} = 25 °C	-	2	9	μΑ
		V_R = 100 V; $t_p \le 300 \mu s$; $δ \le 0.02$; pulsed; $T_j = 60 °C$	-	-	120	μΑ
		V_R = 100 V; $t_p \le 300 \mu s$; $δ \le 0.02$; pulsed; T_j = 85 °C	-	-	600	μΑ
Ç _d	diode capacitance	V _R = 0 V; f = 1 MHz; T _{amb} = 25 °C	-	-	39	pF
		V _R = 1 V; f = 1 MHz; T _{amb} = 25 °C	-	-	21	pF
rr	reverse recovery time	I_F = 10 mA; I_R = 10 mA; $I_{R(meas)}$ = 1 mA; I_{L} = 100 Ω; I_{L} = 25 °C	-	5.9	-	ns

Schottky barrier diode

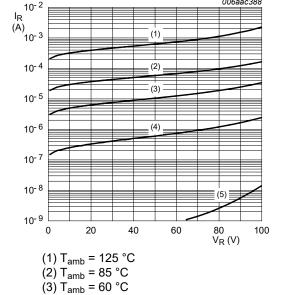
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- (1) T_{amb} = 150 °C
- (2) T_{amb} = 125 °C (3) T_{amb} = 85 °C

- (4) $T_{amb} = 25 ^{\circ}C$ (5) $T_{amb} = -40 ^{\circ}C$

Forward current as a function of forward Fig. 3. voltage; typical values



- (4) $T_{amb} = 25 ^{\circ}C$ (5) $T_{amb} = -40 ^{\circ}C$

Reverse current as a function of reverse Fig. 4. voltage; typical values

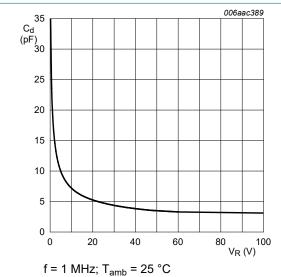
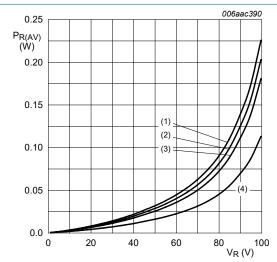


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



T_i = 125 °C $(1) \delta = 1$

 $(2) \delta = 0.9$

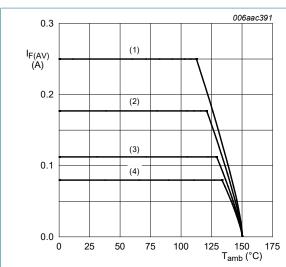
 $(3) \delta = 0.8$

 $(4) \delta = 0.5$

Fig. 6. Average reverse power dissipation as a function of reverse voltage; typical values

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FR4 PCB, standard footprint

T_i = 150 °C

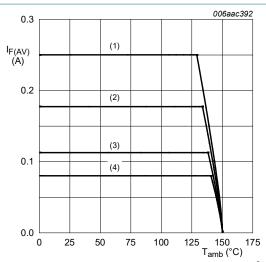
 $(1) \delta = 1; DC$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 7. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

T_i = 150 °C

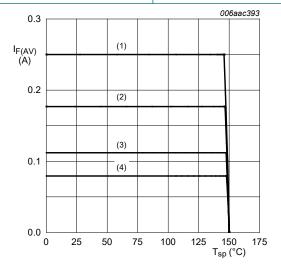
(1) $\delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



T_i = 150 °C

 $(1) \delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

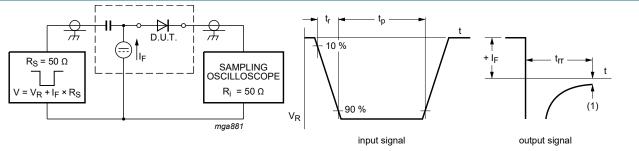
(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 9. Average forward current as a function of solder point temperature; typical values

Schottky barrier diode

11. Test information



(1) $I_R = 1 \text{ mA}$

Input signal: reverse pulse rise time t_r = 0.6 ns; reverse voltage pulse duration t_p = 100 ns; duty cycle δ = 0.05 Oscilloscope: rise time t_r = 0.35 ns

Fig. 10. Reverse recovery time: test circuit and waveforms

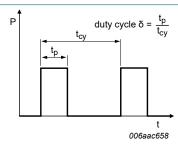


Fig. 11. Duty cycle definition

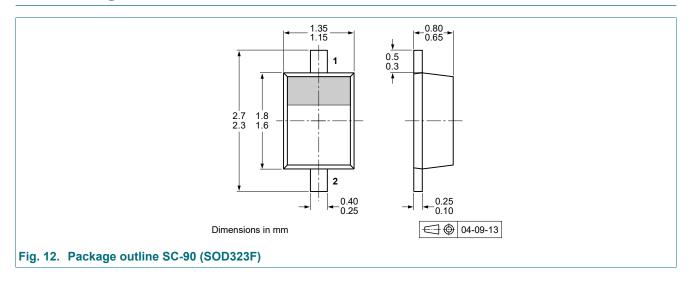
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

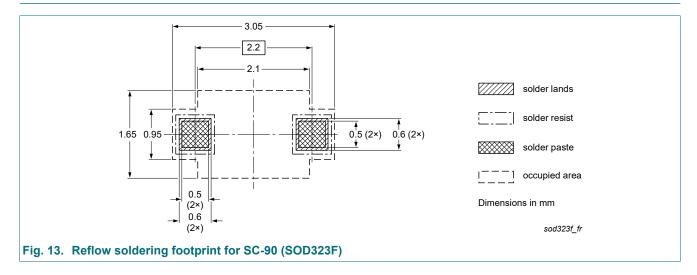
Schottky barrier diode

12. Package outline



Schottky barrier diode

13. Soldering



Schottky barrier diode

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT46WJ-Q v.1	20210823	Product data sheet	-	-

11 / 13

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BAT46WJ-Q

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Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	4
10.	Characteristics	5
11.	Test information	8
12.	Package outline	9
13.	Soldering	10
14.	Revision history	.11
15.	Legal information	.12

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