

**PLVA6xxA** series

Low-voltage avalanche regulator diodes

Rev. 3 — 12 May 2022

**Product data sheet** 

## 1. General description

High performance voltage regulator diodes in a small SOT23 (TO-236AB), Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Very low dynamic impedance at low currents: approximately 5 % of conventional series
- Hard breakdown knee
- Low noise: approximately 10 % of conventional series
- Total power dissipation: max. 250 mW
- Small tolerances of V<sub>Z</sub>
- Working voltage range: nominal 5.00 to 6.80 V
- Non-repetitive peak reverse power dissipation: maximal 30 W at 150 °C
- AEC-Q101 qualified

## 3. Applications

- · Low current, low power, low noise applications
- CMOS RAM back-up circuits
- Voltage stabilizers
- Voltage limiters
- Smoke detector relays



# 4. Quick reference data

#### Table 1. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
V <sub>n</sub>	noise voltage density	f = 1 kHz; B = 1 kHz; I <sub>Z</sub> = 250 μA	-	-	1.0	<u>μ</u> ν √Hz		
ΔV <sub>Z</sub>	line regulation							
	PLVA659A to PLVA668A	I <sub>LO</sub> = 10 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA656A	I <sub>LO</sub> = 50 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA650A	I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.4	V		
	PLVA653A	I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.2	V		
R <sub>Z</sub>	dynamic resistance							
	PLVA650A	1 kHz superimposed;	-	-	700	Ω		
	PLVA653A	I <sub>ZAC</sub> is 10 % of I <sub>ZDC</sub> Iz = 250 μΑ	-	-	250	Ω		
	PLVA656A to PLVA668A	_ 12 = 230 μΑ	-	-	100	Ω		
I <sub>R</sub>	reverse current							
	PLVA650A	V <sub>R</sub> = 50 % V <sub>Z</sub> nominal	-	34	-	nA		
	PLVA653A		-	22	-	nA		
	PLVA656A		-	1.1	-	nA		
	PLVA659A		-	0.9	-	nA		
	PLVA662A		-	0.9	-	nA		
	PLVA665A		-	0.9	-	nA		
	PLVA668A	1	-	0.8	-	nA		

## 5. Pinning information

Table 2. Pinning inform	nation			
Pin	Symbol	Descrition	Simlified outline	Graphic symbol
1	A	anode	3	K
2	n.c.	not connected		
3	К	cathode		aaa-006592

# 6. Ordering information

Type number	Package		
	Name	Description	Version
PLVA650A	TO-236AB	plastic surface-mounted package; 3 leads	<u>SOT23</u>
PLVA653A			
PLVA656A			
PLVA659A			
PLVA662A			
PLVA665A			
PLVA668A			

## 7. Marking

Table 4. Marking codes					
Type number		Marking code			
PLVA650A	[1]	%9A			
PLVA653A	[1]	%9B			
PLVA656A	[1]	%9C			
PLVA659A	[1]	%9D			
PLVA662A	[1]	%9E			
PLVA665A	[1]	%9F			
PLVA668A	[1]	%9G			

[1] % = placeholder for manufacturing site code

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>F</sub>	continuous forward current			-	250	mA
I <sub>ZRM</sub>	repetitive peak working current	t <sub>p</sub> = 100 μs; δ = 10 %		-	250	mA
P <sub>ZSM</sub>	non-repetitive peak reverse power dissipation	t <sub>p</sub> = 100 μs; T <sub>j</sub> = 150 °C		-	30	W
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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# 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	330	K/W

[1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.

# **10. Characteristics**

#### **Table 7. Characteristics**

 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit			
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 mA	-	-	0.9	V			
Vz	working voltage	L							
	PLVA650A		4.80	5.00	5.20	V			
	PLVA653A		5.10	5.30	5.50	V			
	PLVA656A		5.40	5.60	5.80	V			
	PLVA659A	I <sub>Z</sub> = 250 μA	5.70	5.90	6.10	V			
	PLVA662A		6.00	6.20	6.40	V			
	PLVA665A		6.30	6.50	6.70	V			
	PLVA668A		6.60	6.80	7.00	V			
Vz	working voltage		· · ·	1					
	PLVA650A		-	4.30	-	V			
	PLVA653A		-	5.20	-	V			
	PLVA656A		-	5.51	-	V			
	PLVA659A	I <sub>Z</sub> = 10 μA	-	5.85	-	V			
	PLVA662A		-	6.19	-	V			
	PLVA665A		-	6.49	-	V			
	PLVA668A		-	6.80	-	V			
Rz	dynamic resistance								
	PLVA650A		-	-	700	Ω			
	PLVA653A	1 kHz superimposed; I <sub>ZAC</sub> is 10 % of I <sub>ZDC</sub> ;	-	-	250	Ω			
	PLVA656A to PLVA668A	$I_Z = 250 \ \mu A$	-	-	100	Ω			
Sz	temperature coefficient								
	PLVA650A		-	0.20	-	mv/K			
	PLVA653A		-	1.60	-	mv/K			
	PLVA656A		-	1.90	-	mv/K			
	PLVA659A	I <sub>Z</sub> = 250 μA	-	2.40	-	mv/K			
	PLVA662A		-	2.65	-	mv/K			
	PLVA665A		-	2.90	-	mv/K			
	PLVA668A		-	3.40	-	mv/K			

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
I <sub>R</sub>	reverse current							
	PLVA650A		-	-	20000	nA		
	PLVA653A		-	-	5000	nA		
	PLVA656A		-	-	1000	nA		
	PLVA659A	V <sub>R</sub> = 80 % V <sub>Z</sub> nominal	-	-	500	nA		
	PLVA662A		-	-	100	nA		
	PLVA665A		-		50	nA		
	PLVA668A	_	-		10	nA		
R	reverse current							
	PLVA650A		-	34	-	nA		
	PLVA653A	1	-	22	-	nA		
	PLVA656A	_	-	1.1	-	nA		
	PLVA659A	V <sub>R</sub> = 50 % V <sub>Z</sub> nominal	-	0.9	-	nA		
	PLVA662A	_	-	0.9	-	nA		
	PLVA665A	_	-	0.9	-	nA		
	PLVA668A	_	-	0.8	-	nA		
I <sub>R</sub>	reverse current							
	PLVA650A		-	21	-	μA		
	PLVA653A	_	-	3.5	-	μA		
	PLVA656A	-	-	1.3	-	μA		
	PLVA659A	V <sub>R</sub> = 90 % V <sub>Z</sub> nominal	-	1.0	-	μA		
	PLVA662A	-	-	0.05	-	μA		
	PLVA665A	-	-	0.04	-	μA		
	PLVA668A	_	-	0.006	-	μA		
ΔVz	line regulation							
	PLVA650A to PLVA668A	I <sub>LO</sub> = 10 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA656A	I <sub>LO</sub> = 50 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA650A	I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.4	V		
	PLVA653A	$I_{LO} = 100 \ \mu\text{A}; I_{HI} = 1 \ \text{mA}$	-	-	0.2	V		
V <sub>n</sub>	noise voltage density	f = 1 kHz; B = 1 kHz; I <sub>Z</sub> = 250 μA	-	-	1.0	<u>μν</u> √H₂		

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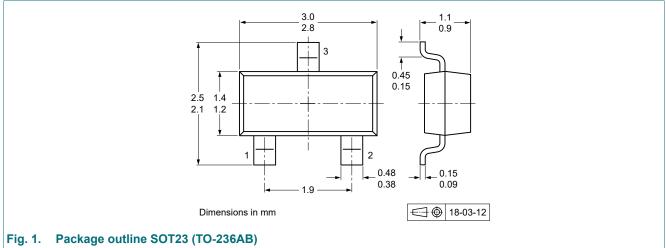
# **11. Test information**

## 11.1. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

### Table 8. Package outline

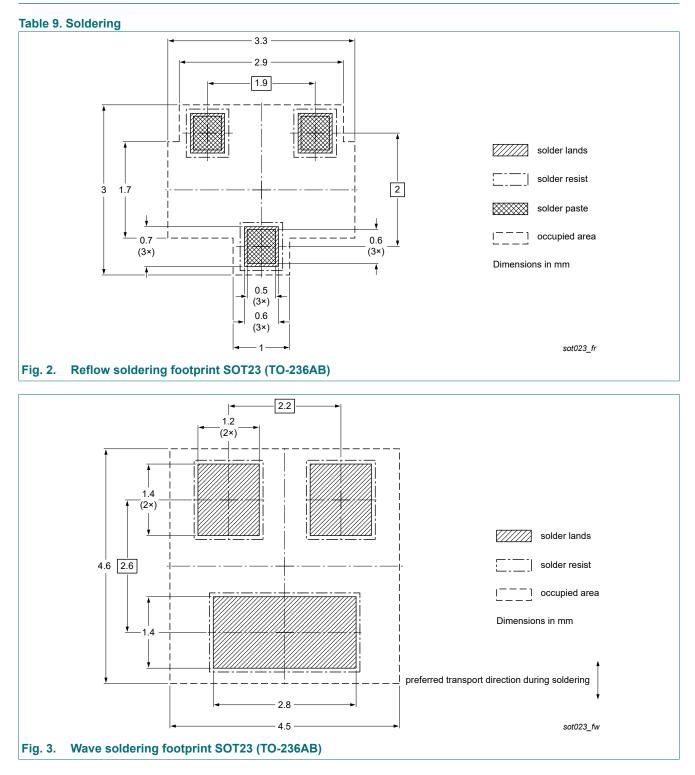


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# 13. Soldering



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# 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
PLVA6XXA_SER v.3	20220512	Product data sheet	-	PLVA6XXA_SERIES v.2			
Modifications:	guidelines o	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>					
PLVA6XXA_SERIES v.2	20040114	Product data sheet	-	PLVA6XXA_SERIES v.1			
PLVA6XXA_SERIES v.1	19990525	Product data sheet	-	-			

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# 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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