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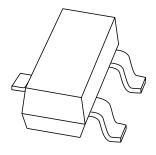
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# DISCRETE SEMICONDUCTORS

# DATA SHEET



# MMBTA92 PNP high-voltage transistor

Product specification Supersedes data of 2000 Apr 11 2004 Jan 16





# PNP high-voltage transistor

#### MMBTA92

#### **FEATURES**

- Low current (max. 100 mA)
- High voltage (max. 300 V).

#### **APPLICATIONS**

- Telephony
- Professional communication equipment.

#### **DESCRIPTION**

PNP high-voltage transistor in a SOT23 plastic package. NPN complement: MMBTA42.

#### **MARKING**

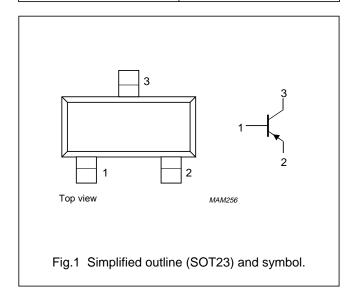
TYPE NUMBER	MARKING CODE <sup>(1)</sup>
MMBTA92	7E*

#### Note

- 1. \* = p: Made in Hong Kong.
  - \* = t : Made in Malaysia.
  - \* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE		PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION	
MMBTA92	_	plastic surface mounted package; 3 leads	SOT23	

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-300	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	-300	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	<b>-</b> 5	V
I <sub>C</sub>	collector current (DC)		_	-100	mA
I <sub>CM</sub>	peak collector current		_	-200	mA
I <sub>BM</sub>	peak base current		_	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		<b>–65</b>	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

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# PNP high-voltage transistor

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	$I_E = 0$ ; $V_{CB} = -200 \text{ V}$	_	-250	nA
I <sub>EBO</sub>	emitter cut-off current	$I_C = 0; V_{EB} = -3 \text{ V}$	_	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = -10 \text{ V; note 1}$			
		$I_C = -1 \text{ mA}$	25	_	
		$I_C = -10 \text{ mA}$	40	_	
		$I_C = -30 \text{ mA}$	25	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -20 \text{ mA}; I_B = -2 \text{ mA}$	_	-500	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -20 \text{ mA}; I_B = -2 \text{ mA}$	_	-900	mV
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = -20 \text{ V}$ ; $f = 1 \text{ MHz}$	_	6	pF
f <sub>T</sub>	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -20 \text{ V};$ f = 100 MHz	50	_	MHz

#### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

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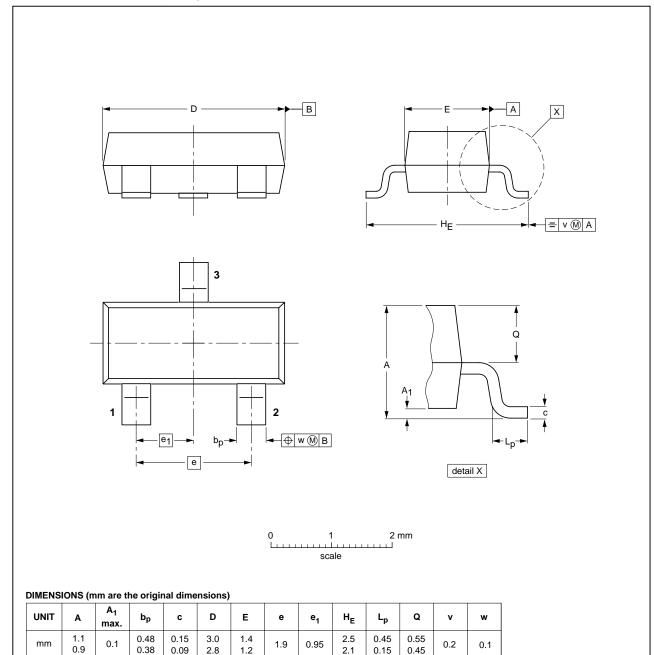
# PNP high-voltage transistor

MMBTA92

#### **PACKAGE OUTLINE**

#### Plastic surface mounted package; 3 leads

SOT23



OUTLINE REFERENCES		EUROPEAN	ISSUE DATE		
IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
	TO-236AB				<del>-97-02-28-</del> 99-09-13
	IEC	IEC JEDEC	IEC JEDEC EIAJ	IEC JEDEC EIAJ	IEC JEDEC EIAJ PROJECTION

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### PNP high-voltage transistor

MMBTA92

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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#### **Contact information**

For additional information please visit http://www.semiconductors.philips.com. Fax: +31 40 27 24825 For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com.

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