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Kind regards,

Team Nexperia



15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor Rev. 02 — 21 April 2009

Product data sheet

1. **Product profile**

1.1 General description

NPN low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in an ultra small SOT416 (SC-75) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS3515E.

1.2 Features

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

1.3 Applications

- DC-to-DC conversion
- MOSFET gate driving
- Motor control
- Charging circuits
- Low power switches (e.g. motors, fans)
- Portable applications

1.4 Quick reference data

Table 1. **Quick reference data**

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	15	V
I _C	collector current		-	-	0.5	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	1	А
R _{CEsat}	collector-emitter saturation resistance	I _C = 500 mA; I _B = 50 mA	<u>[1]</u> _	300	500	mΩ

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.



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2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2	emitter		3
3	collector	1 2	
			sym021

3. Ordering information

Table 3. Orde	ring inform	ation	
Type number	Package		
	Name	Description	Version
PBSS2515E	SC-75	plastic surface-mounted package; 3 leads	SOT416

4. Marking

Table 4. Marking codes	
Type number	Marking code
PBSS2515E	1Q

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

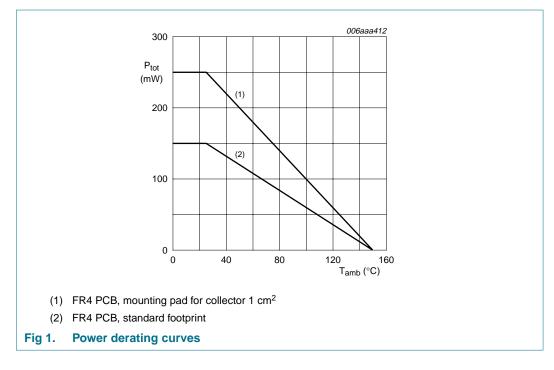
Parameter	Conditions	Min	Max	Unit
collector-base voltage	open emitter	-	15	V
collector-emitter voltage	open base	-	15	V
emitter-base voltage	open collector	-	6	V
collector current		-	0.5	А
peak collector current	single pulse; $t_p \leq 1 \text{ ms}$	-	1	A
peak base current	single pulse; $t_p \leq 1 ms$	-	100	mA
total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> _	150	mW
		[2] _	250	mW
junction temperature		-	150	°C
ambient temperature		-65	+150	°C
storage temperature		-65	+150	°C
	 collector-base voltage collector-emitter voltage emitter-base voltage collector current peak collector current peak base current total power dissipation junction temperature ambient temperature 	collector-base voltageopen emittercollector-emitter voltageopen baseemitter-base voltageopen collectorcollector currentsingle pulse; $t_p \le 1 ms$ peak base currentsingle pulse; $t_p \le 1 ms$ total power dissipationTamb $\le 25 \ ^{\circ}C$ junction temperatureambient temperature	collector-base voltageopen emitter-collector-emitter voltageopen base-emitter-base voltageopen collector-collector currentsingle pulse; $t_p \le 1$ ms-peak base currentsingle pulse; $t_p \le 1$ ms-total power dissipationT amb \le 25 °C[1] -junction temperatureambient temperatureambient temperature	collector-base voltageopen emitter-15collector-emitter voltageopen base-15emitter-base voltageopen collector-6collector current-0.5peak collector currentsingle pulse; $t_p \le 1 ms$ -1peak base currentsingle pulse; $t_p \le 1 ms$ -100total power dissipation $T_{amb} \le 25 \ ^{\circ}C$ [1] -150junction temperature-15012ambient temperature65+150

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor



6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1]</u> _	-	833	K/W
			[2] _	-	500	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		-	-	170	K/W

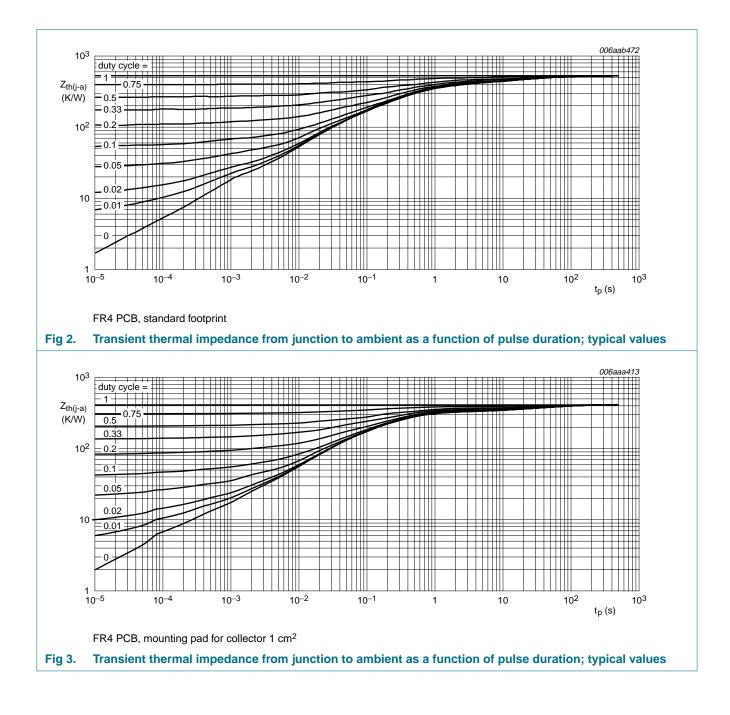
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

7. Characteristics

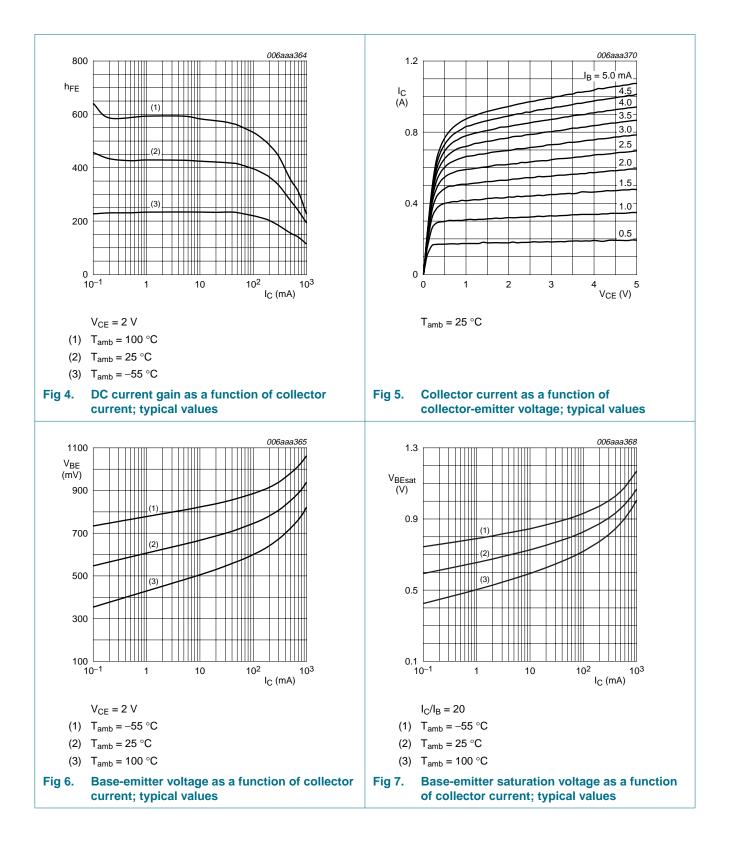
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	$V_{CB} = 15 \text{ V}; I_E = 0 \text{ A}$	-	-	100	nA
	current	$V_{CB} = 15 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 \text{ °C}$	-	-	50	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 V; I_C = 0 A$	-	-	100	nA
h _{FE}	DC current gain	V_{CE} = 2 V; I_C = 10 mA	200	-	-	
		$V_{CE} = 2 \text{ V}; I_{C} = 100 \text{ mA}$	<mark>[1]</mark> 150	-	-	
		$V_{CE} = 2 \text{ V}; I_{C} = 500 \text{ mA}$	<mark>[1]</mark> 90	-	-	
OLOUI	collector-emitter	$I_{C} = 10 \text{ mA}; I_{B} = 0.5 \text{ mA}$	-	-	25	mV
	saturation voltage	$I_{C} = 200 \text{ mA}; I_{B} = 10 \text{ mA}$	-	-	150	mV
		$I_{C} = 500 \text{ mA}; I_{B} = 50 \text{ mA}$	<u>[1]</u> _	-	250	mV
R _{CEsat}	collector-emitter saturation resistance	$I_{\rm C}$ = 500 mA; $I_{\rm B}$ = 50 mA	<u>[1]</u> _	300	500	mΩ
V _{BEsat}	base-emitter saturation voltage	$I_{C} = 500 \text{ mA}; I_{B} = 50 \text{ mA}$	<u>[1]</u> -	-	1.1	V
V _{BEon}	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 100 \text{ mA}$	<u>[1]</u> -	-	0.9	V
t _d	delay time	V _{CC} = 11 V;	-	10	-	ns
t _r	rise time	$I_{\rm C} = 250 \text{ mA};$	-	15	-	ns
t _{on}	turn-on time	− I _{Bon} = 12.5 mA; I _{Boff} = −12.5 mA	-	25	-	ns
ts	storage time		-	215	-	ns
t _f	fall time		-	34	-	ns
t _{off}	turn-off time		-	249	-	ns
f _T	transition frequency	$V_{CE} = 5 V; I_C = 100 mA;$ f = 100 MHz	250	420	-	MHz
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	-	4.4	6	pF

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

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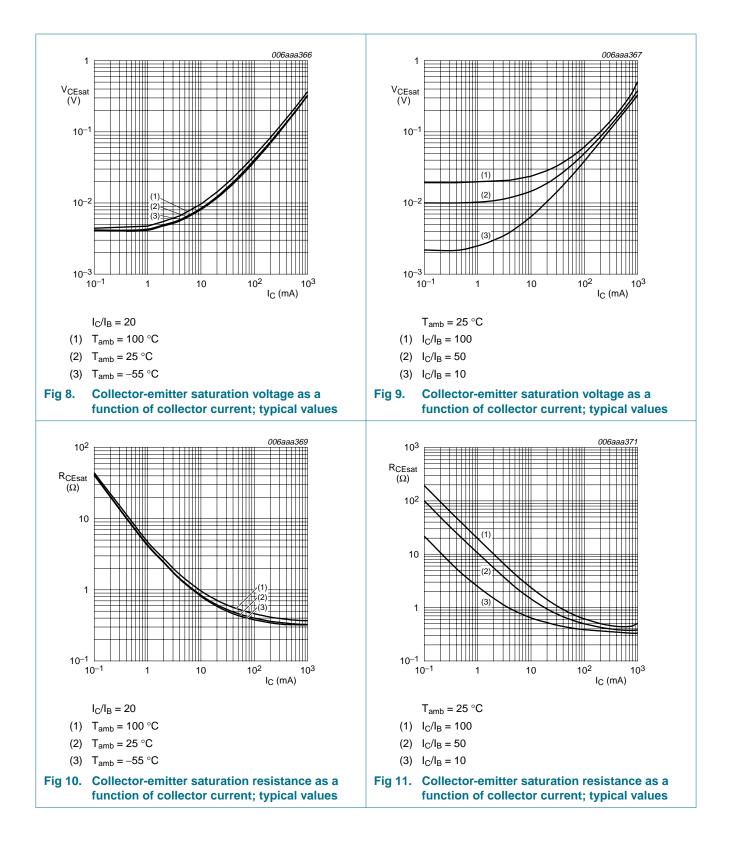
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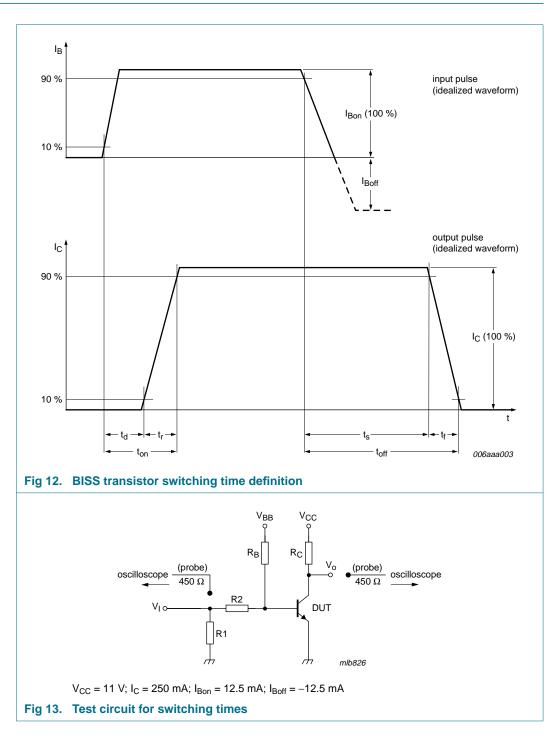
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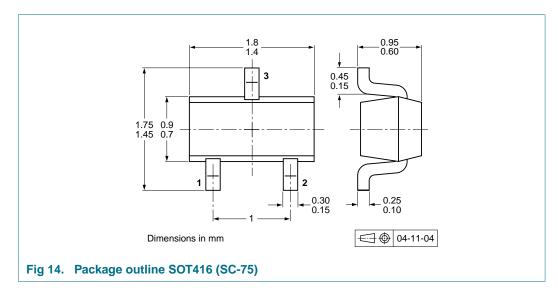
15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

8. Test information



15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

9. Package outline



10. Packing information

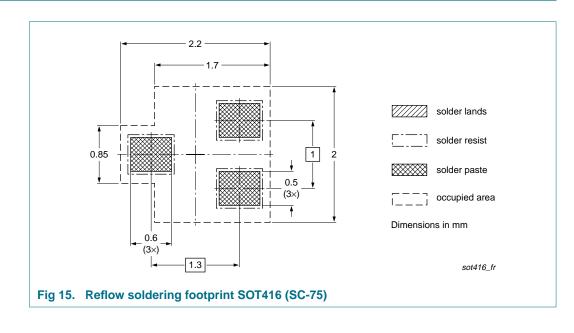
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing	quantity
			3000	10000
PBSS2515E	SOT416	4 mm pitch, 8 mm tape and reel	-115	-135

[1] For further information and the availability of packing methods, see Section 14.

11. Soldering



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Product data sheet

15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
PBSS2515E_2	20090421	Product data sheet	-	PBSS2515E_1	
Modifications:		of this data sheet has been of NXP Semiconductors.	redesigned to comply v	vith the new identity	
	 Legal texts have been adapted to the new company name where appropriate. 				
	• Figure 2: added				
	 Table 6 "Thermal characteristics": enhanced 				
	 Table 7 "Characteristics": switching times added 				
	• Figure 8 and 9: amended				
	Section 13	"Legal information": updated	1		
PBSS2515E 1	20050418	Product data sheet	-	-	

15 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

13. Legal information

13.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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