

BAT74 Schottky barrier double diode Rev. 03 – 19 April 2010

**Product data sheet** 

### 1. Product profile

#### 1.1 General description

Planar Schottky barrier double diode with an integrated guard ring for stress protection. Two electrically isolated Schottky barrier diodes, encapsulated in a small SOT143B Surface-Mounted Device (SMD) plastic package.

#### **1.2 Features and benefits**

- Low forward voltage
- Guard-ring protected
- Small SMD plastic package

#### **1.3 Applications**

- Ultra high-speed switching
- Voltage clamping
- Protection circuits
- Blocking diodes

### 1.4 Quick reference data

#### Table 1.Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
I <sub>F</sub>	forward current		-	-	200	mA
V <sub>R</sub>	reverse voltage		-	-	30	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 100 mA	-	-	800	mV

## 2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	cathode (diode 1)		
2	cathode (diode 2)		
3	anode (diode 2)		
4	anode (diode 1)		



# 3. Ordering information

Table 3.         Ordering information					
Type number	Package				
	Name	Description	Version		
BAT74	-	plastic surface-mounted package; 4 leads	SOT143B		

### 4. Marking

#### Table 4.Marking codes

Type number	Marking code <sup>[1]</sup>
BAT74	*L4

- [1] \* = -: made in Hong Kong
  - \* = p: made in Hong Kong
  - \* = t: made in Malaysia
  - \* = W: made in China

## 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per diode	•				
V <sub>R</sub>	reverse voltage		-	30	V
l <sub>F</sub>	forward current		-	200	mA
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le 1 \text{ s}; \ \delta \le 0.5$	-	300	mA
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> < 10 ms	-	600	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \leq 25 \ ^{\circ}C$	-	230	mW
Tj	junction temperature		-	125	°C
T <sub>amb</sub>	ambient temperature		-65	+125	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C
Double d	iode operation				
V <sub>R</sub>	reverse voltage		-	30	V
			<u>[1]</u> _	60	V
l <sub>F</sub>	forward current		[2] _	110	mA
I <sub>FRM</sub>	repetitive peak forward current	$t_p \leq 1 \text{ s}; \ \delta \leq 0.5$	-	200	mA

[1] Series connection.

[2] If both diodes are in forward operation at the same moment, total device current is max. 110 mA. If one diode is in reverse operation and the other is in forward operation at the same moment, total device current is max. 200 mA.

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# 6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u> _	-	500	K/W

[1] Refer to SOT143B standard mounting conditions.

## 7. Characteristics

Table 7.Characteristics

$T_{-25}$	°C unloss	otherwise	snacifiad
amb - 20	0 1111033	001010130	specificu.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode	)						
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 mA		-	-	240	mV
		I <sub>F</sub> = 1 mA	[1]	-	-	320	mV
		I <sub>F</sub> = 10 mA		-	-	400	mV
		I <sub>F</sub> = 30 mA		-	-	500	mV
		I <sub>F</sub> = 100 mA		-	-	800	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 25 V	[2]	-	-	2	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz		-	-	10	pF
t <sub>rr</sub>	reverse recovery time		<u>[3]</u>	-	-	5	ns

[1] Temperature coefficient of forward voltage -0.6 %/K.

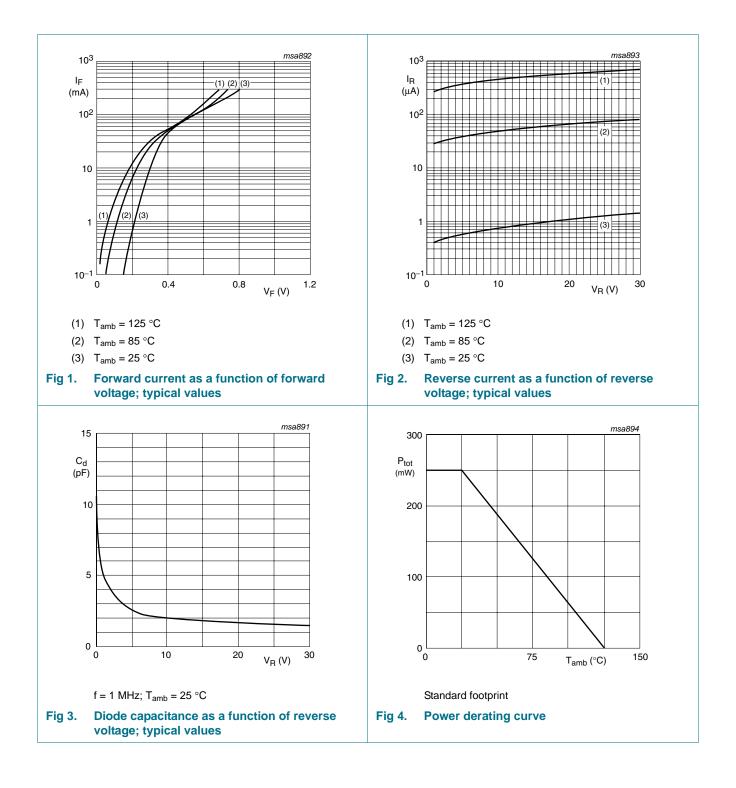
[2] Pulse test:  $t_p = 300 \ \mu s; \ \delta = 0.02$ .

[3] When switched from I<sub>F</sub> = 10 mA to I<sub>R</sub> = 10 mA; R<sub>L</sub> = 100  $\Omega$ ; measured at I<sub>R</sub> = 1 mA.

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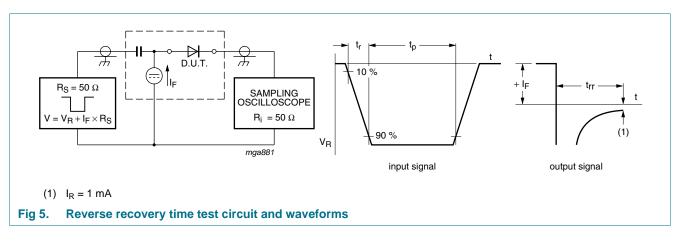
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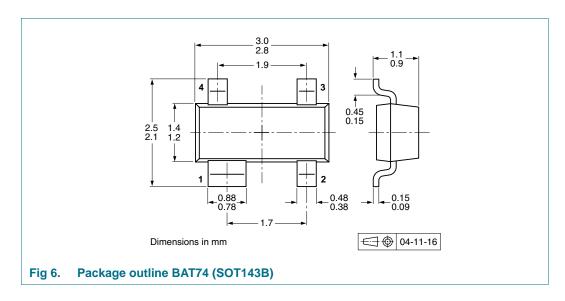
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# 8. Test information



# 9. Package outline



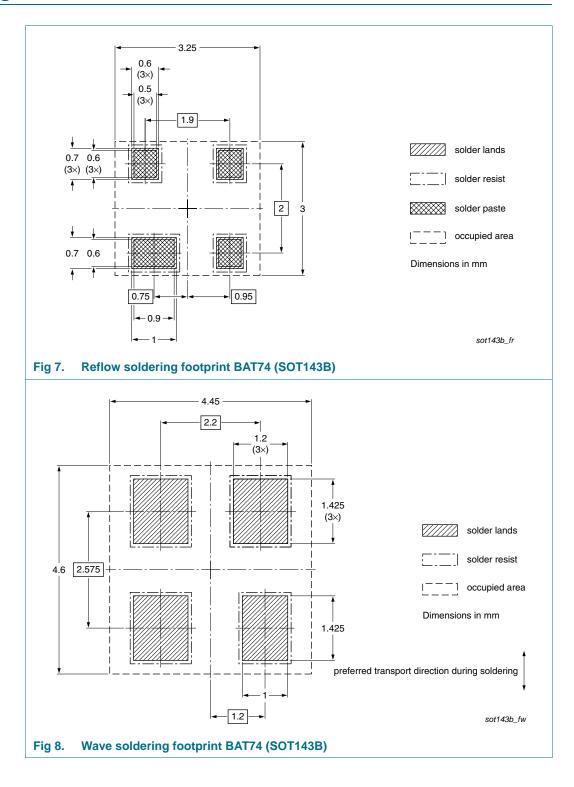
# **10. Packing information**

Please refer to packing information on <u>www.nexperia.com</u>.

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# 11. Soldering



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# 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
BAT74_3	20100419	Product data sheet	-	BAT74_2			
Modifications:		f this data sheet has been NXP Semiconductors.	redesigned to comply	with the new identity			
	<ul> <li>Legal texts h</li> </ul>	<ul> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>					
	<ul> <li>Section 1.1 "General description": amended</li> </ul>						
	<ul> <li><u>Table 1 "Quick reference data</u>": added</li> </ul>						
	<u>Section 4 "Marking"</u> : updated						
	<ul> <li>Section 8 "Test information": added</li> </ul>						
	• Figure 5: enhanced						
	<ul> <li>Figure 6: superseded by minimized package outline drawing</li> </ul>						
	<ul> <li>Section 10 "Packing information": added</li> </ul>						
	<ul> <li>Section 11 "Soldering": added</li> </ul>						
	<ul> <li>Section 13 "I</li> </ul>	_egal information": updated	l				
BAT74_2	20010905	Product specification	-	BAT74_1			
BAT74 1	19960319	Product specification	-	-			

# **13. Legal information**

### **13.1 Data sheet status**

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nexperia.com</u>.

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**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

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