Product data sheet

1. Product profile

1.1. General description

PNP power transistors in a medium power SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		NPN complement
	Nexperia	JEDEC	
BCX51T	SOT89	SC-62	BCX54T
BCX51-10T			BCX54-10T
BCX51-16T			BCX54-16T

1.2. Features and benefits

- High collector current capability I_C and I_{CM}
- · Three current gain selections
- High power dissipation capability
- AEC-Q101 qualified

1.3. Applications

- Linear voltage regulators
- MOSFET drivers
- · High-side switches
- · Power management
- Amplifiers

1.4. Quick reference data

Table 2. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-2	А



45 V, 1 A PNP power bipolar transistors

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h _{FE}	DC current gain						
	BCX51T	$V_{CE} = -2 \text{ V}; I_{C} = -150 \text{ mA}$	[1]	63	-	250	
	BCX51-10T		[1]	63	-	160	
	BCX51-16T		[1]	100	-	250	

^[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

2. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		C
2	С	collector		R —
3	В	base		
			3 2 1	E
				006aaa231

3. Ordering information

Table 4. Ordering information

Type number	Package				
	Name	Description	Version		
BCX51T	SC-62	plastic, surface-mounted package; 3 leads; 1.5 mm pitch;	SOT89		
BCX51-10T		4.5 mm x 2.5 mm x 1.5 mm body			
BCX51-16T					

4. Marking

Table 5. Marking

Type number	Marking code
BCX51T	C5
BCX51-10T	C6
BCX51-16T	C7

Product data sheet

5. Limiting values

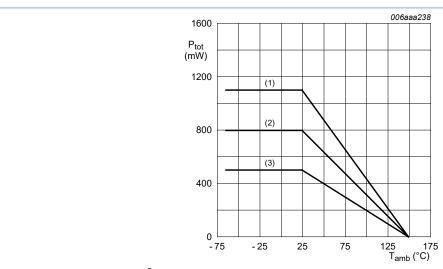
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		-45	V
V _{CEO}	collector-emitter voltage	open base		-	-45	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		-2	Α
l _B	base current			-	-200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	500	mW
			[2]	-	800	mW
			[3]	-	1100	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm².



- (1) FR4 PCB; 6 cm² mounting pad for collector
- (2) FR4 PCB; 1 cm² mounting pad for collector
- (3) FR4 PCB; standard footprint

Fig. 1. Power derating curves

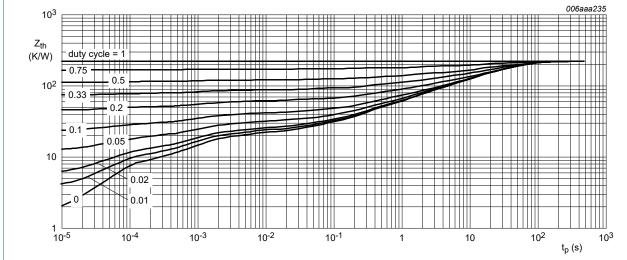
6. Thermal characteristics

Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

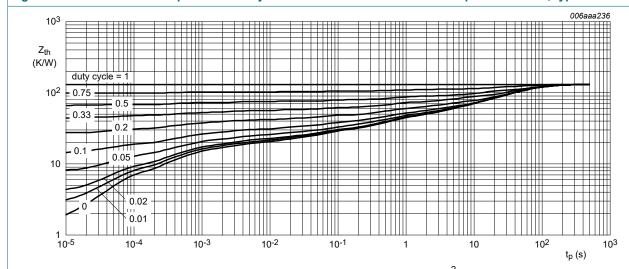
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]	-	-	157	K/W
			[3]	-	-	114	K/W

- [1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm².



FR4 PCB; single-sided copper; tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

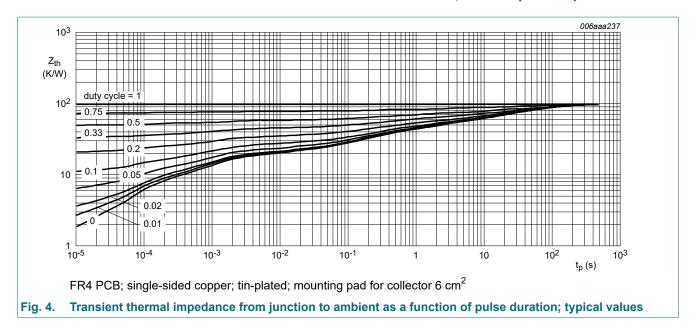


FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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45 V, 1 A PNP power bipolar transistors



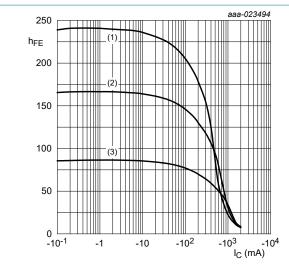
7. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = -100 \ \mu A; I_E = 0 \ A$		-45	-		V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	I _C = -2 mA; I _E = 0 A		-45	-		V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I _E = -100 μA; I _C = 0 A		-5	-		V
I _{CBO}	collector-base	V _{CB} = -30 V; I _E = 0 A		-	-	-100	nA
	cut-off current	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ °C}$		-	-	-10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A		-	-	-100	nA
h _{FE}	DC current gain		'		1		
	BCX51T, -10T, -16T	V _{CE} = -2 V; I _C = -5 mA		63	-	-	
		V _{CE} = -2 V; I _C = -500 mA	[1]	40	-	-	
	BCX51T	V _{CE} = -2 V; I _C = -150 mA	[1]	63	-	250	
	BCX51-10T	V _{CE} = -2 V; I _C = -150 mA	[1]	63	-	160	
	BCX51-16T	V _{CE} = -2 V; I _C = -150 mA	[1]	100	-	250	
V _{CEsat}	collector-emitter saturation voltage	I _C = -500 mA; I _B = -50 mA	[1]	-	-	-500	mV
V _{BE}	base-emitter voltage	V _{CE} = -2 V; I _C = -500 mA	[1]	-	-	-1	V
f _T	transition frequency	V _{CE} = -5 V; I _C = -50 mA; f = 100 MHz		-	140	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	7	-	pF

[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$



$$V_{CE} = -2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values

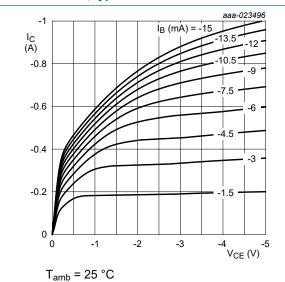
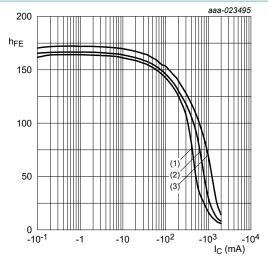


Fig. 7. Collector current as a function of collectoremitter voltage; typical values

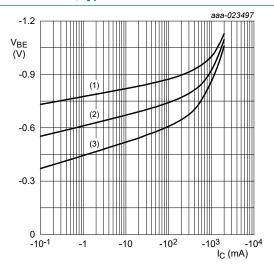


$$(1) V_{CE} = -1 V$$

(2)
$$V_{CE} = -2 V$$

(3)
$$V_{CE} = -5 V$$

Fig. 6. DC current gain as a function of collector current; typical values



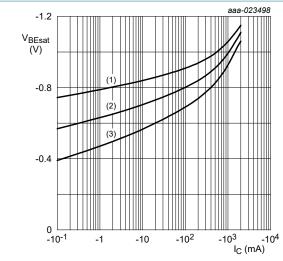
$$V_{CE} = -2 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 8. Base-emitter voltage as a function of collector current; typical values



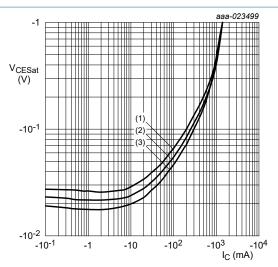
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb}$$
 = 100 °C

Base-emitter saturation voltage as a function of Fig. 10. Collector-emitter saturation voltage as a Fig. 9. collector current; typical values



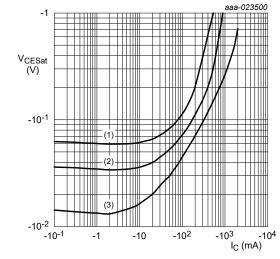
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

function of collector current; typical values



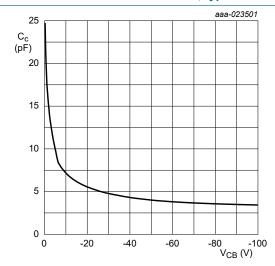
 T_{amb} = 25 °C

(1) $I_C/I_B = 50$

(2) $I_C/I_B = 20$

(3) $I_C/I_B = 5$

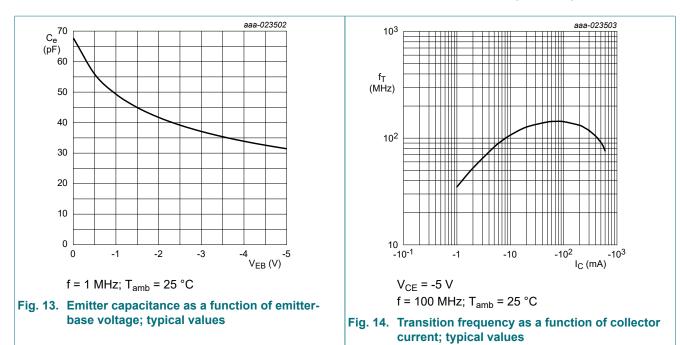
Fig. 11. Collector-emitter saturation voltage as a function of collector current; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ °C}$

Fig. 12. Collector capacitance as a function of collectorbase voltage; typical values

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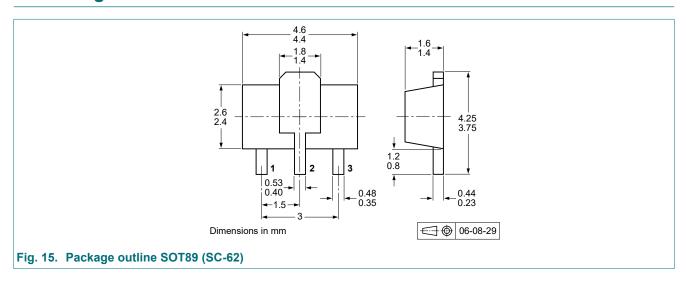


8. Test information

8.1. Quality information

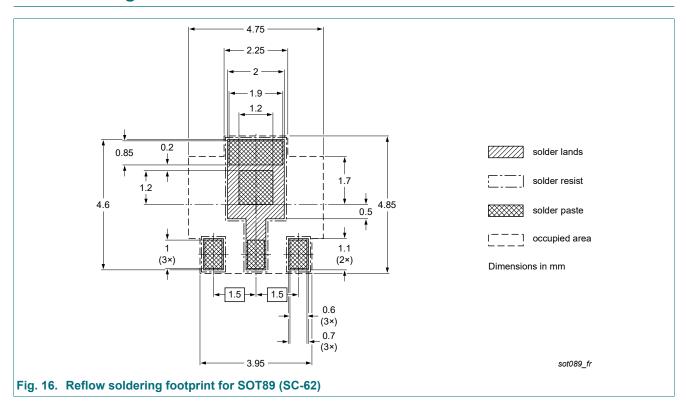
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

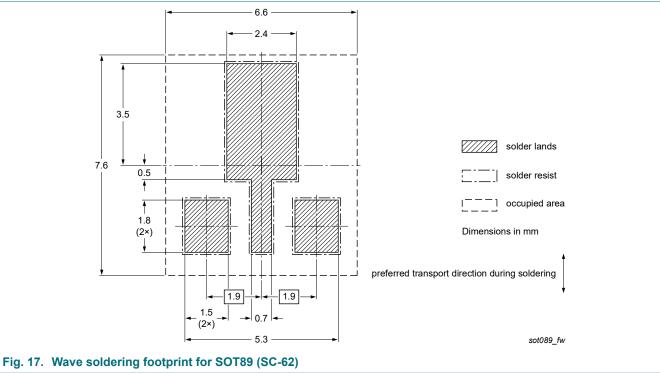
9. Package outline



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10. Soldering





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11. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCX51T_SER v.1	20190822	Product data sheet	-	-

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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