

BC846BMB

65 V, 100 mA NPN general-purpose transistor

27 July 2020

Product data sheet

1. General description

NPN general-purpose transistor in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Power dissipation comparable to SOT23
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	65	V
I _C	collector current			-	-	100	mA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; \text{ I}_{C} = 2 \text{ mA}; \text{ T}_{amb} = 25 \text{ °C}$		200	-	450	



5. Pinning information

Table 2	. Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	1	С
2	E	emitter		
3	С	collector	Transparent top view	B
			DFN1006B-3 (SOT883B)	sym021

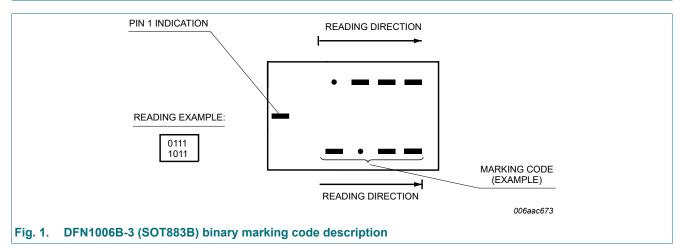
6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BC846BMB	DFN1006B-3	plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B			
BC846BMB/S501	DFN1006B-3	plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B			

7. Marking

Table 4. Marking codes

Type number	Marking code
BC846BMB	0100 1011
BC846BMB/S501	0100 1011



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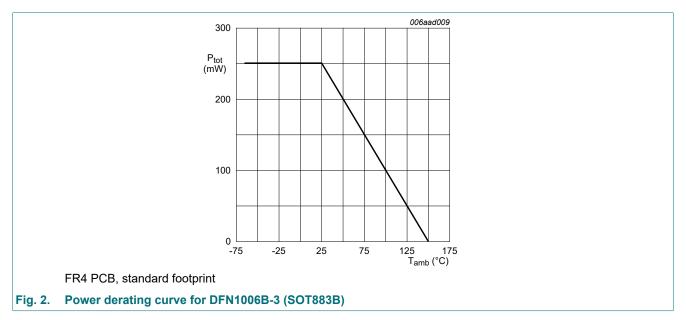
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	80	V
V _{CEO}	collector-emitter voltage	open base		-	65	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

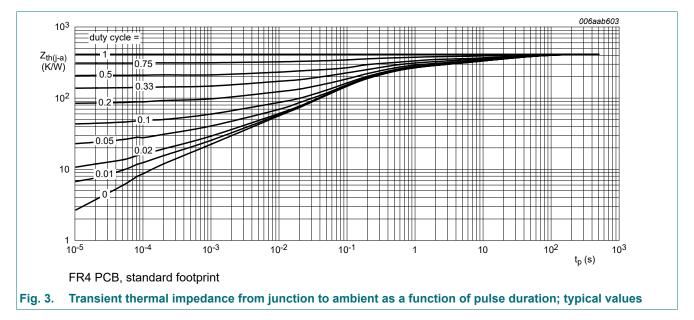


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9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

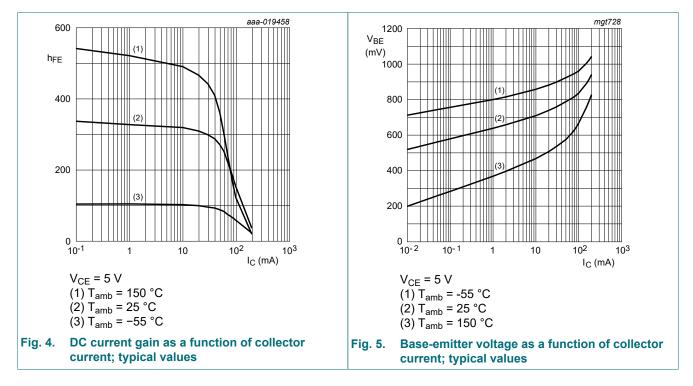
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



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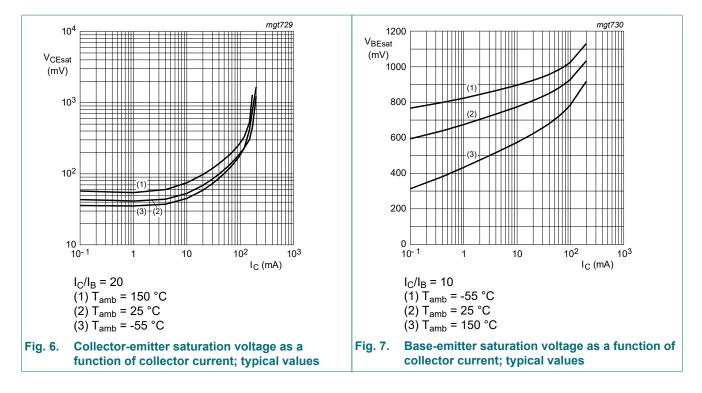
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	200	-	450	
V _{CEsat}	collector-emitter	I_{C} = 10 mA; I_{B} = 0.5 mA; T_{amb} = 25 °C	-	90	200	mV
	saturation voltage	I_C = 100 mA; I_B = 5 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	200	400	mV
V _{BEsat}	base-emitter saturation	I_{C} = 10 mA; I_{B} = 0.5 mA; T_{amb} = 25 °C	-	760	-	mV
	voltage	I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C	-	900	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	580	660	700	mV
		V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C	-	-	770	mV
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	2	3	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	11	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 200 μA; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz; T _{amb} = 25 °C	-	2	10	dB



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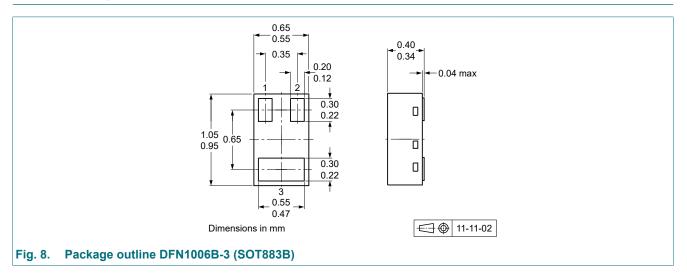


11. Test information

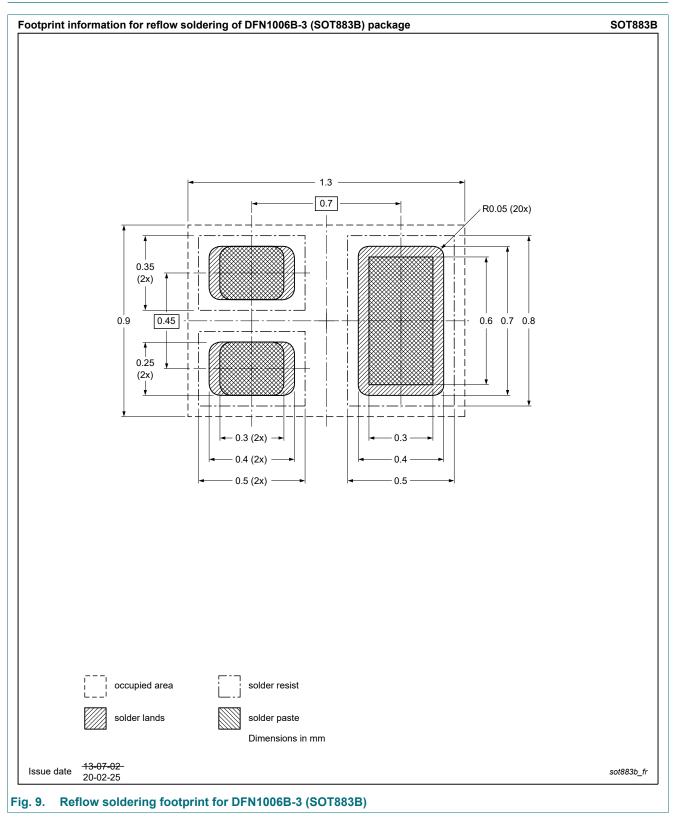
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision histo	ory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
BC846BMB v.2	20200727	Product data sheet	-	BC846BMB v.1				
Modifications:	of Nexperia.	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
BC846BMB v.1	20120515	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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