

#### Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of <a href="http://www.nxp.com">http://www.nxp.com</a>, <a href="http://www.semiconductors.philips.com/">http://www.nxp.com</a>, <a href="http://www.nexperia.com">http://www.nexperia.com</a>, <a href="http://www.nexperia.com">http://www.nexperia.com</a>)

Instead of sales.addresses@www.nxp.com or sales.addresses@www.semiconductors.philips.com, use salesaddresses@nexperia.com (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

Should be replaced with:

- © Nexperia B.V. (year). All rights reserved.

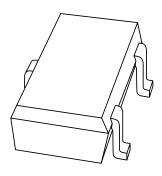
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# PMST2222; PMST2222A NPN switching transistors

Product data sheet Supersedes data of 1997 Jul 14 1999 Apr 22



# **NPN** switching transistors

PMST2222; PMST2222A

#### **FEATURES**

- High current (max. 600 mA)
- Low voltage (max. 40 V).

#### **APPLICATIONS**

• High-speed switching and linear amplification.

#### **DESCRIPTION**

NPN switching transistor in a SOT323 plastic package. PNP complement: PMST2907A.

#### **MARKING**

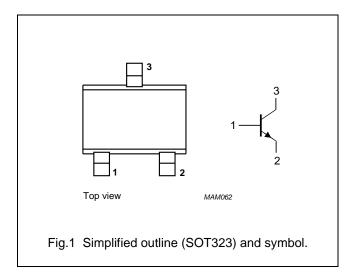
| TYPE NUMBER | MARKING CODE(1) |
|-------------|-----------------|
| PMST2222    | *1B             |
| PMST2222A   | *1P             |

#### Note

\* = - : Made in Hong Kong.
 \* = t : Made in Malaysia.

#### **PINNING**

| PIN | DESCRIPTION |
|-----|-------------|
| 1   | base        |
| 2   | emitter     |
| 3   | collector   |



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

| SYMBOL           | PARAMETER                     | CONDITIONS                       | MIN. | MAX. | UNIT |
|------------------|-------------------------------|----------------------------------|------|------|------|
| V <sub>CBO</sub> | collector-base voltage        | open emitter                     |      |      |      |
|                  | PMST2222                      |                                  | _    | 60   | V    |
|                  | PMST2222A                     |                                  | _    | 75   | V    |
| $V_{CEO}$        | collector-emitter voltage     | open base                        |      |      |      |
|                  | PMST2222                      |                                  | _    | 30   | V    |
|                  | PMST2222A                     |                                  | _    | 40   | V    |
| V <sub>EBO</sub> | emitter-base voltage          | open collector                   |      |      |      |
|                  | PMST2222                      |                                  | _    | 5    | V    |
|                  | PMST2222A                     |                                  | _    | 6    | V    |
| I <sub>C</sub>   | collector current (DC)        |                                  | _    | 600  | mA   |
| I <sub>CM</sub>  | peak collector current        |                                  | _    | 800  | mA   |
| I <sub>BM</sub>  | peak base current             |                                  | -    | 200  | mA   |
| P <sub>tot</sub> | total power dissipation       | T <sub>amb</sub> ≤ 25 °C; note 1 | -    | 200  | mW   |
| T <sub>stg</sub> | storage temperature           |                                  | -65  | +150 | °C   |
| Tj               | junction temperature          |                                  | _    | 150  | °C   |
| T <sub>amb</sub> | operating ambient temperature |                                  | -65  | +150 | °C   |

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

1999 Apr 22

# NPN switching transistors

PMST2222; PMST2222A

#### THERMAL CHARACTERISTICS

| SYMBOL              | PARAMETER                                   | CONDITIONS | VALUE | UNIT |
|---------------------|---|------------|-------|------|
| R <sub>th j-a</sub> | thermal resistance from junction to ambient | note 1     | 625   | K/W  |

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

| SYMBOL             | PARAMETER                            | CONDITIONS   | MIN. | MAX. | UNIT |
|--------------------|--------------------------------------|--|------|------|------|
| I <sub>CBO</sub>   | collector cut-off current            | I <sub>E</sub> = 0; V <sub>CB</sub> = 50 V                                   | _    | 10   | nA   |
|                    | PMST2222                             | I <sub>E</sub> = 0; V <sub>CB</sub> = 50 V; T <sub>j</sub> = 125 °C          | _    | 10   | μΑ   |
|                    | collector cut-off current            | I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V                                   | _    | 10   | nA   |
|                    | PMST2222A                            | I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V; T <sub>j</sub> = 125 °C          | _    | 10   | μΑ   |
| I <sub>EBO</sub>   | collector cut-off current            | I <sub>C</sub> = 0; V <sub>EB</sub> = 3 V                                    | _    | 10   | nA   |
| h <sub>FE</sub>    | DC current gain                      | I <sub>C</sub> = 0.1 mA; V <sub>CE</sub> = 10 V                              | 35   | _    |      |
|                    |                                      | I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 10 V                                | 50   | _    |      |
|                    |                                      | I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 10 V                               | 75   | _    |      |
|                    |                                      | $I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}; T_{amb} = -55 ^{\circ}\text{C}$ | 35   | _    |      |
|                    |                                      | I <sub>C</sub> = 150 mA; V <sub>CE</sub> = 1 V; note 1                       | 50   | _    |      |
|                    |                                      | I <sub>C</sub> = 150 mA; V <sub>CE</sub> = 10 V; note 1                      | 100  | 300  |      |
|                    | DC current gain                      | I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 10 V; note 1                      |      |      |      |
|                    | PMST2222                             |  | 30   | _    |      |
|                    | PMST2222A                            |  | 40   | _    |      |
| V <sub>CEsat</sub> | collector-emitter saturation voltage | I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; note 1                      | _    | 400  | mV   |
|                    | PMST2222                             | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1                      | _    | 1.6  | V    |
|                    | collector-emitter saturation voltage | I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; note 1                      | _    | 300  | mV   |
|                    | PMST2222A                            | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1                      | _    | 1    | V    |
| V <sub>BEsat</sub> | base-emitter saturation voltage      | I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; note 1                      | _    | 1.3  | V    |
|                    | PMST2222                             | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1                      | _    | 2.6  | V    |
|                    | base-emitter saturation voltage      | I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; note 1                      | 0.6  | 1.2  | V    |
|                    | PMST2222A                            | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1                      | _    | 2    | V    |
| C <sub>c</sub>     | collector capacitance                | I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz       | _    | 8    | pF   |
| C <sub>e</sub>     | emitter capacitance                  | $I_C = i_c = 0$ ; $V_{EB} = 0.5 \text{ V}$ ; $f = 1 \text{ MHz}$             |      |      |      |
|                    | PMST2222                             |  | _    | 30   | pF   |
|                    | PMST2222A                            |  | _    | 25   | pF   |
| f <sub>T</sub>     | transition frequency                 | I <sub>C</sub> = 20 mA; V <sub>CE</sub> = 20 V; f = 100 MHz                  |      |      |      |
|                    | PMST2222                             |  | 250  | _    | MHz  |
|                    | PMST2222A                            |  | 300  | _    | MHz  |

1999 Apr 22 3

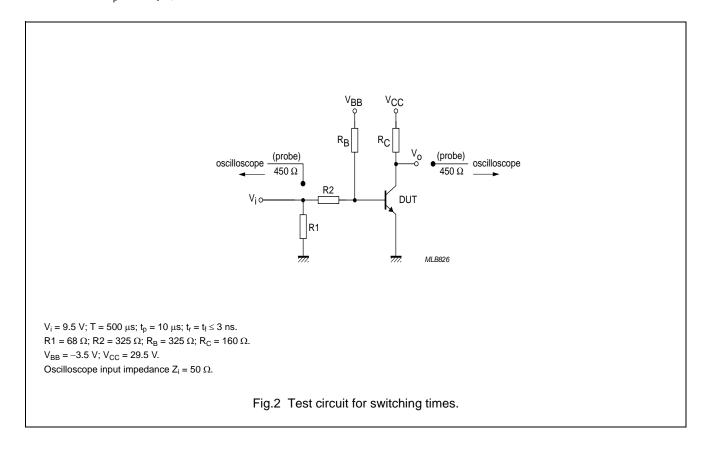
# NPN switching transistors

# PMST2222; PMST2222A

| SYMBOL           | PARAMETER                        | CONDITIONS  | MIN. | MAX. | UNIT |
|------------------|----------------------------------|---|------|------|------|
| F                | noise figure                     | $I_C$ = 200 μA; $V_{CE}$ = 5 V; $R_S$ = 2 kΩ; $f$ = 1 kHz; $B$ = 200 Hz | _    | 4    | dB   |
| Switching ti     | mes (between 10% and 90% levels) | ; (see Fig.2)   |      |      |      |
| t <sub>on</sub>  | turn-on time                     | I <sub>Con</sub> = 150 mA; I <sub>Bon</sub> = 15 mA;                    | _    | 35   | ns   |
| t <sub>d</sub>   | delay time                       | $I_{Boff} = -15 \text{ mA}$   | _    | 15   | ns   |
| t <sub>r</sub>   | rise time                        |   | _    | 20   | ns   |
| t <sub>off</sub> | turn-off time                    |   | _    | 250  | ns   |
| t <sub>s</sub>   | storage time                     |   | _    | 200  | ns   |
| t <sub>f</sub>   | fall time                        |   | _    | 60   | ns   |

#### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 



1999 Apr 22 4

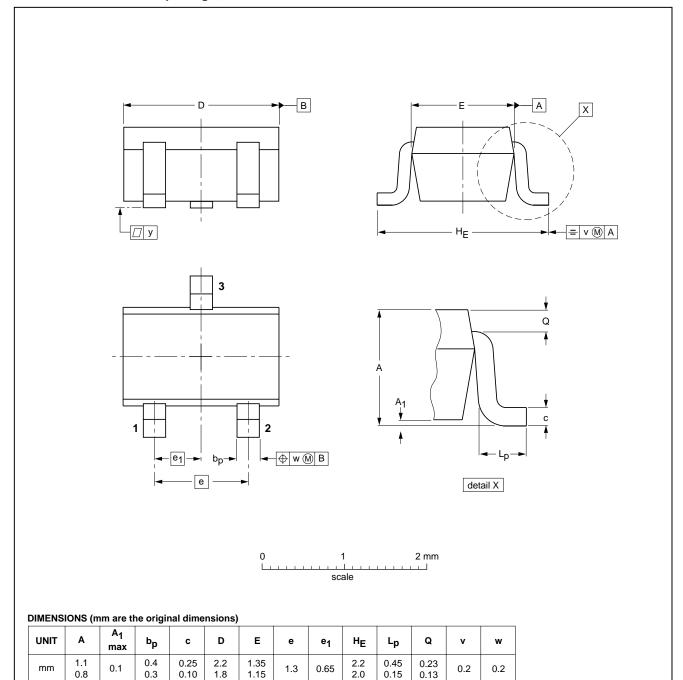
# NPN switching transistors

# PMST2222; PMST2222A

#### **PACKAGE OUTLINE**

Plastic surface mounted package; 3 leads

**SOT323** 



| OUTLINE | TLINE REFERENCES |       | EUROPEAN | ISSUE DATE |            |            |
|---------|------------------|-------|----------|------------|------------|------------|
| VERSION | IEC              | JEDEC | EIAJ     |            | PROJECTION | 1350E DATE |
| SOT323  |                  |       | SC-70    |            |            | 97-02-28   |

1999 Apr 22 5

# **NPN** switching transistors

PMST2222; PMST2222A

#### **DATA SHEET STATUS**

| DOCUMENT<br>STATUS <sup>(1)</sup> | PRODUCT<br>STATUS <sup>(2)</sup> | DEFINITION  |
|-----------------------------------|----------------------------------|---|
| Objective data sheet              | Development                      | This document contains data from the objective specification for product development. |
| Preliminary data sheet            | Qualification                    | This document contains data from the preliminary specification.                       |
| Product data sheet                | Production                       | This document contains the product specification.                                     |

#### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
  and may differ in case of multiple devices. The latest product status information is available on the Internet at
  URL http://www.nxp.com.

6

#### **DISCLAIMERS**

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

# **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

#### **Contact information**

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

 Printed in The Netherlands
 115002/00/02/pp7
 Date of release: 1999 Apr 22
 Document order number: 9397 750 05756

