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OM11085

IAR LPC1114 Evaluation Board

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OM11085,598

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Demo board description

The IAR LPC1114 Evaluation Board allows you to quickly and easily evaluate the LPC111x family of microcontrollers. The microcontroller, board, and accompanying features make it a great starting point for your next Cortex-M0 project.

Populated with the LPC1114 to demonstrate LPC111x features, the LPC1114 board allows the evaluation of the industry's lowest 32-bit active power consumption of the LPC1114.

Order Number: OM11085

Features

- Debug through J-Link-OB, a debug interface mounted on the board
- Standard JTAG connector (with SWD only) and small SWD connector
- Power up the board through USB, J-Link (USB cable included) or external power adapter (not included)
- Small LCD
- Two user buttons and a reset button
- Two user LEDs and a power LED
- Analog trim wheel
- Buzzer
- EXT Connector

Descriptive summary

IAR LPC1114 Evaluation Board

IAR's LPC1114 Evaluation Board lets you get up-and-running quickly in evaluating the superior performance and capability of the LPC1114. The LPC1114 is designed for 8/16-bit microcontroller applications, offering performance, low power, simple instruction set and memory addressing together with reduced code size compared to existing 8/16-bit architectures.

Demo Box Contents

- IAR LPC1114 Evaluation Board
- IAR Embedded Workbench for ARM, 8K KickStart edition
- IAR visualSTATE evaluation edition, 20-state limitation
- Example projects included

Related Items:

- [LPCXpresso Board for LPC1114](#)
- [Keil LPC1114 Evaluation Board](#)
- [Keil ULINK2-JTAG Debugger](#)

For more information:

[IAR HomePage](#)[IAR Product Page](#)

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