ESD protection for ultra high-speed interfaces

Rev. 4 — 8 April 2013

Product data sheet

1. Product profile

1.1 General description

The devices are designed to protect high-speed interfaces such as High-Definition Multimedia Interface (HDMI), DisplayPort, external Serial Advanced Technology Attachment (eSATA) and Low-Voltage Differential Signaling (LVDS) interfaces against ElectroStatic Discharge (ESD).

The devices include four high-level ESD protection diode structures for ultra high-speed signal lines. They are available in three package variants: DFN2510-10 (SOT1165-1), DFN2510A-10 (SOT1176-1) and TSSOP10 (SOT552-1).

All signal lines are protected by a special diode configuration offering ultra low line capacitance of only 0.6 pF. These diodes provide protection to downstream components from ESD voltages up to ± 8 kV contact according to IEC 61000-4-2, level 4.

1.2 Features and benefits

- System ESD protection for HDMI, DisplayPort, eSATA and LVDS
- All signal lines with integrated rail-to-rail clamping diodes for downstream ESD protection of ±8 kV according to IEC 61000-4-2, level 4
- Matched 0.5 mm trace spacing
- Signal lines with ≤ 0.05 pF matching capacitance between signal pairs
- Line capacitance of only 0.6 pF for each channel
- Design-friendly 'pass-thru' signal routing

1.3 Applications

The devices are designed for high-speed receiver and transmitter port protection:

- TVs, monitors
- DVD recorders and players
- Notebooks, main board graphics cards and ports
- Set-top boxes and game consoles

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2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol		
IP42	83CZ10-TBA (SO	T1165-1)				
1	TMDS_CH1-	negative channel 1 ESD protection		1 2 4 5		
2	TMDS_CH1+	positive channel 1 ESD protection	12345	3, 8 _{001aai619}		
3	GND	ground	Transparent top view			
4	TMDS_CH2-	negative channel 2 ESD protection	DFN2510-10			
5	TMDS_CH2+	positive channel 2 ESD protection		001aai619		
6	n.c.	not connected				
7	n.c.	not connected				
8	GND	ground				
9	n.c.	not connected				
10	n.c.	not connected				
IP42	83CZ10-TBR (SO	T1176-1)				
1	TMDS_CH1-	negative channel 1 ESD protection	10 9 8 7 6	1 2 4 5		
2	TMDS_CH1+	positive channel 1 ESD protection				
3	GND	ground	1 2 3 4 5 Transparent top view			
4	TMDS_CH2-	negative channel 2 ESD protection	DFN2510A-10			
5	TMDS_CH2+	positive channel 2 ESD protection		3, 8 001aai619		
6	n.c.	not connected				
7	n.c.	not connected				
8	GND	ground				
9	n.c.	not connected				
10	n.c.	not connected				

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Pin	Symbol	Description	Simplified outline	Graphic symbol		
IP42	83CZ10-TT (SOT5	52-1)				
1	TMDS_CH1-	negative channel 1 ESD protection	10 0 0 0 0 0 0	1 2 4 5		
2	TMDS_CH1+ positive channel 1 ESD protection					
3	GND	ground		3, 8 _{001aai619}		
4	TMDS_CH2-	negative channel 2 ESD protection				
5	TMDS_CH2+	positive channel 2 ESD protection				
6	n.c.	not connected	1 🗌 🗌 🔲 🗍 5			
7	n.c.	not connected	TSSOP10			
8	GND	ground				
9	n.c.	not connected				
10	n.c.	not connected				

3. Ordering information

Type number	Package				
	Name	Description	Version		
IP4283CZ10-TBA	DFN2510-10	plastic extremely thin small outline package; no leads; 10 terminals; body 1 \times 2.5 \times 0.5 mm	SOT1165-1		
IP4283CZ10-TBR	DFN2510A-10	plastic extremely thin small outline package; no leads; 10 terminals; body 1 \times 2.5 \times 0.5 mm	SOT1176-1		
IP4283CZ10-TT	TSSOP10	plastic thin shrink small outline package; 10 leads; body width 3 mm	SOT552-1		

4. Marking

Table 3. Marking codes	
Type number	Marking code
IP4283CZ10-TBA	83
IP4283CZ10-TBR	83
IP4283CZ10-TT	4283

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5. Limiting values

Table 4. Limiting values In accordance with the Absolute Maximum Rating System (IEC 60134).					
Symbol	Parameter	Conditions	Min	Max	Unit
VI	input voltage		-0.5	+5.5	V
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2, level 4	<u>[1]</u>		
		contact discharge	-8	+8	kV
		air discharge	-15	+15	kV
T _{stg}	storage temperature		-55	+125	°C
T _{amb}	ambient temperature		-40	+85	°C

[1] All pins to ground.

6. Characteristics

Table 5.Characteristics

 $T_{amb} = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions	Mir	і Тур	Max	Unit
V _{BR}	breakdown voltage	I _{test} = 1 mA	6	-	9	V
I _{LR}	reverse leakage current	per TMDS channel; V = 3 V	-	-	1	μΑ
V _F	forward voltage	I _{test} = 1 mA	-	0.7	-	V
C _{line}	line capacitance	f = 1 MHz; V _{bias} = 2.5 V	<u>[1]</u> _	0.6	-	pF
ΔC_{line}	line capacitance difference	f = 1 MHz; V _{bias} = 2.5 V	<u>[1]</u> -	0.05	-	рF
Cline(mutual)	mutual line capacitance	f = 1 MHz; V _{bias} = 2.5 V	<u>[1][2]</u> _	0.07	-	рF
r _{dyn}	dynamic resistance	surge	<u>[3]</u>			
		positive transient	-	0.8	-	Ω
		negative transient	-	0.85	-	Ω
V _{CL}	clamping voltage	positive transient; I _{PP} = 3.8 A	<u>[3]</u> _	9.5	-	V
		negative transient; I _{PP} = -2.8 A	<u>[3]</u> _	-3.2	-	V

[1] This parameter is guaranteed by design.

[2] Between signal pin and pin n.c.

[3] According to IEC 61000-4-5 (8/20 μs).

Product data sheet

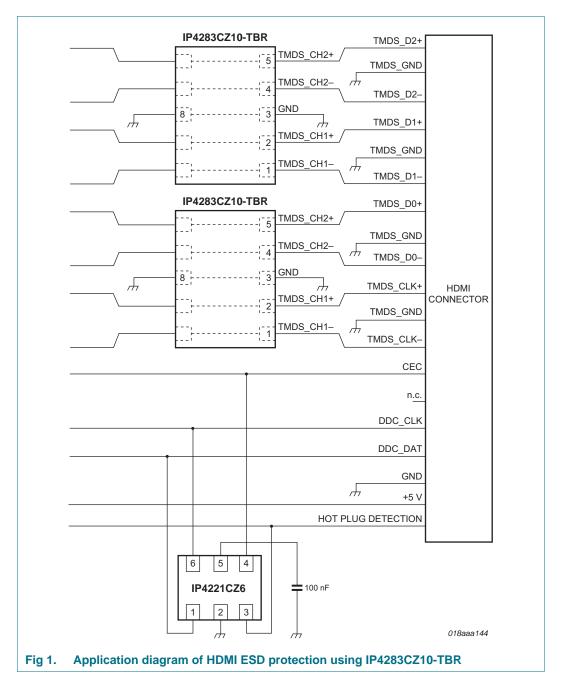
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7. Application information

The devices are designed to provide high-level ESD protection for high-speed serial data buses such as HDMI, DisplayPort, eSATA and LVDS data lines.

When designing the Printed-Circuit Board (PCB), give careful consideration to impedance matching, and signal coupling.

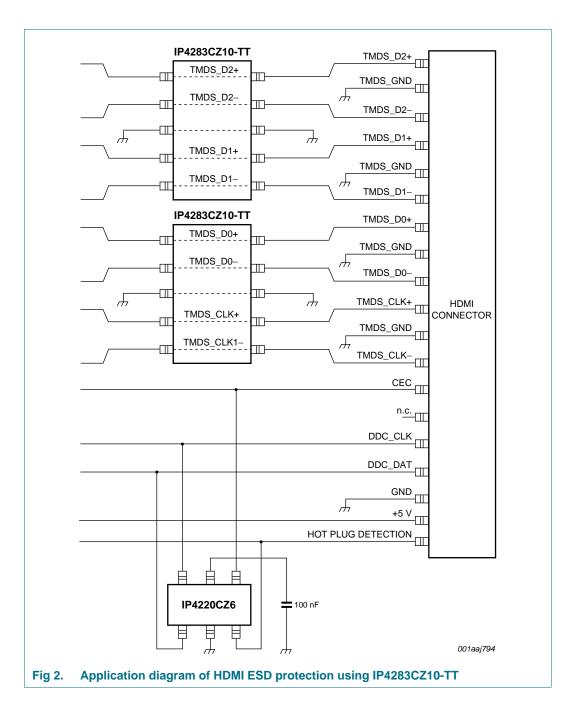
Basic application diagrams for the ESD protection of an HDMI interface are shown in Figure 1 and $\underline{2}$.



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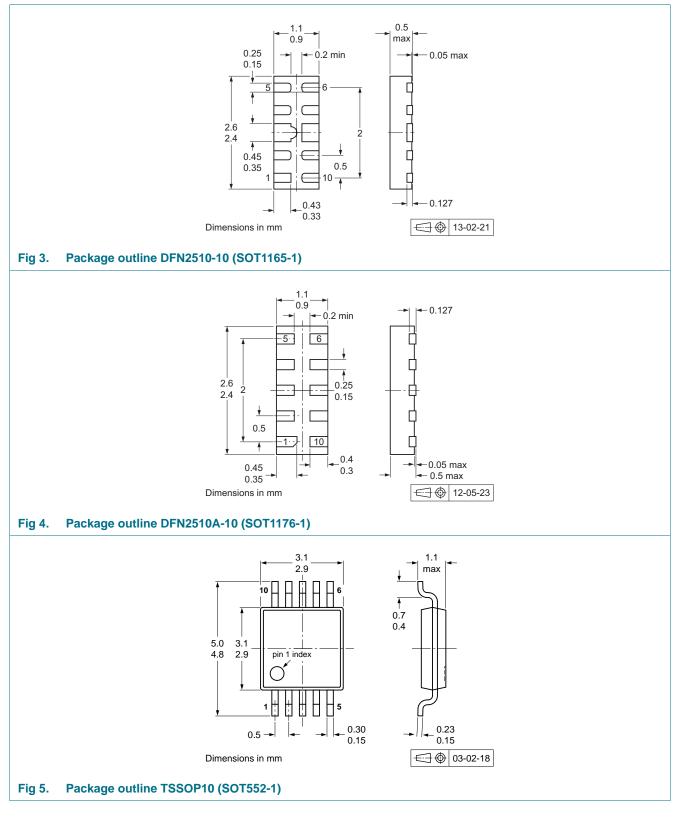
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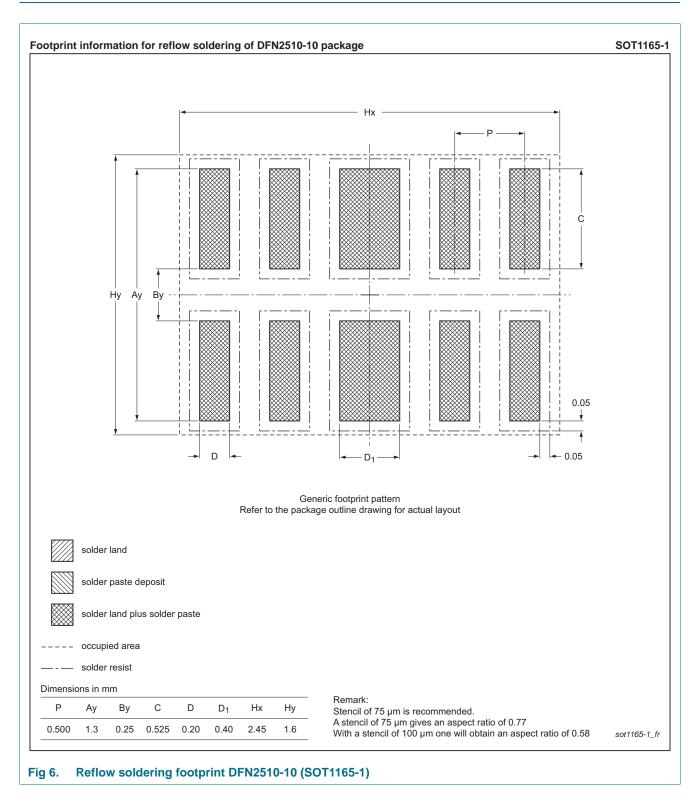
8. Package outline



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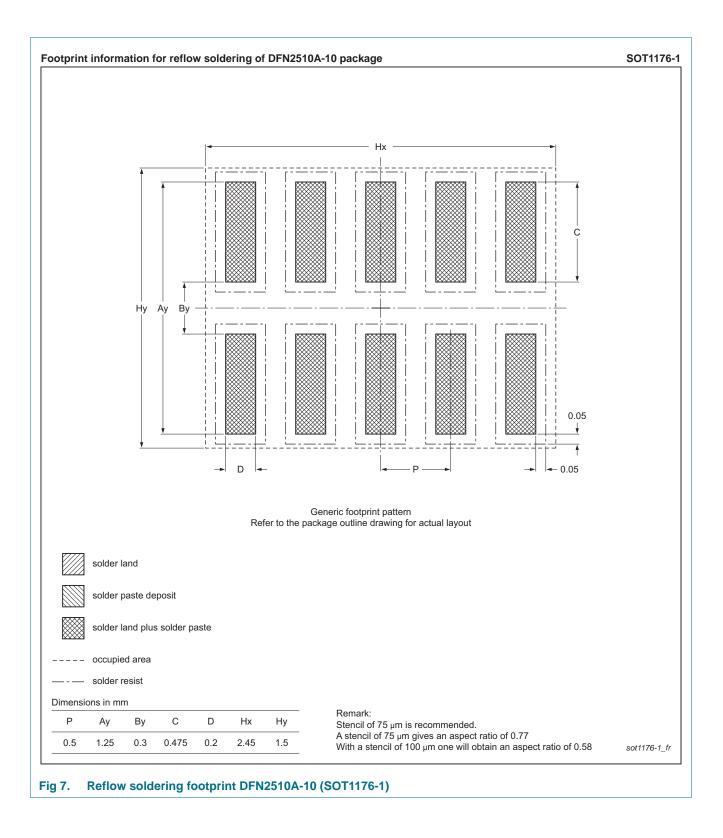
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9. Soldering



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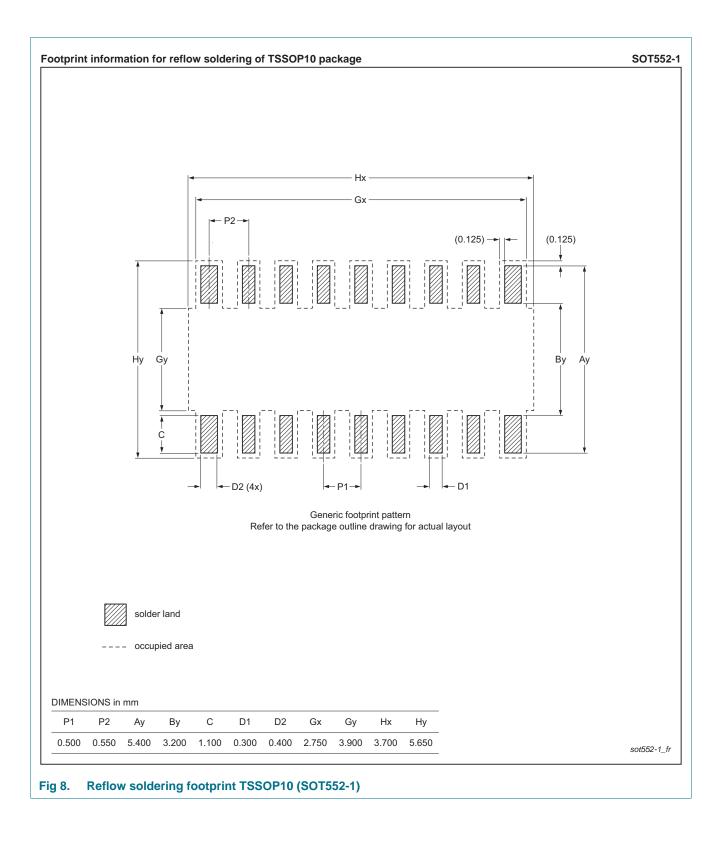
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10. Revision history

Table 6. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
IP4283CZ10_SER v.4	20130408	Product data sheet	-	IP4283CZ10_SER v.3		
Modifications:	Section 1.1	"General description": upd	ated			
	 <u>Section 1.2 "Features and benefits"</u>: updated 					
	Section 2 "I	Pinning information": updat	ed			
	 Section 3 " 	Ordering information": upda	ated			
	Table 5 "Ch	aracteristics": updated; r _{dyr}	value corrected			
	 Section 8 "I 	Package outline": drawings	replaced with minimized	I package outline drawings		
	Section 9 "S	Soldering": updated				
	Section 11	"Legal information": update	d			
IP4283CZ10_SER v.3	20110624	Product data sheet	-	IP4283CZ10_SER v.2		
IP4283CZ10_SER v.2	20100827	Product data sheet	-	IP4283CZ10 v.1		
IP4283CZ10 v.1	20090507	Product data sheet	-	-		

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11. Legal information

11.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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