

Low capacitance bidirectional ESD protection diode

19 May 2022

Product data sheet

1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode in a very small SOD323 (SC-76) SMD plastic package designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- Bidirectional ESD protection of one line
- Max. peak pulse power: P_{ppm} = 500 W
- Low clamping voltage: V_{CL} = 26 V
- Ultra low leakage current: I_{RM} = 0.09 μA
- ESD protection up to 30 kV
- IEC 61000-4-2, level 4 (ESD)
- IEC 61000-4-5 (surge); I_{PPM} = 18 A
- Very small SMD plastic package
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Computers and peripherals
- Communication systems
- Audio and video equipment
- Data lines
- CAN bus protection

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
C _d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C		-	101	-	pF



5. Pinning information

Table 2. Pinning information						
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	K1	cathode (diode 1)	1 2			
2	K2	cathode (diode 2)				
			SOD323	sym045		

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PESD3V3L1BA-Q	SOD323	plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body	SOD323			

7. Marking

Table 4. Marking codes				
Type number	Marking code			
PESD3V3L1BA-Q	AB			

8. Limiting values

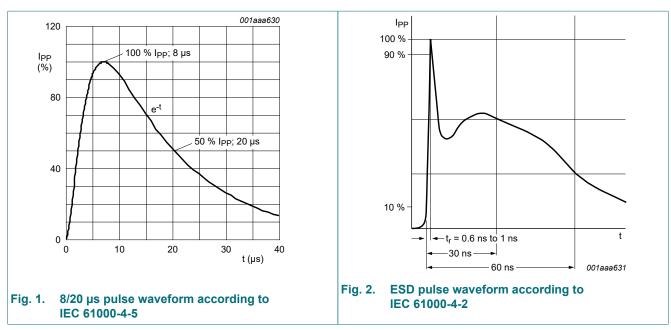
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1]	-	500	W
I _{PPM}	rated peak pulse current		[1]	-	18	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	ım ratings					
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge; T _{amb} = 25 °C	[2]	-	30	kV
		IEC 61000-4-2; air discharge		-	15	kV
		MIL-STD-883; human body model (HBM); T _{amb} = 25 °C		-	10	kV

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

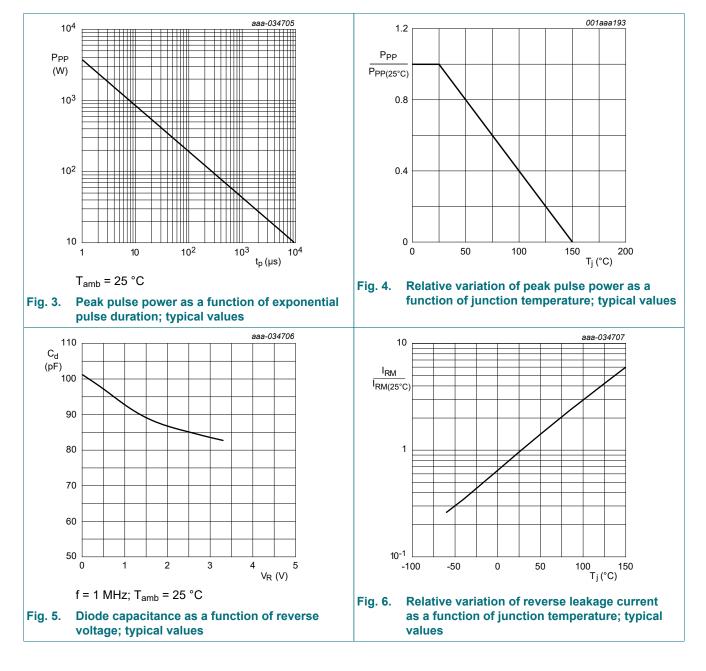
[2] Device stressed with ten non-repetitive ESD pulses.



9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
V _{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C		5.8	6.4	6.9	V
I _{RM}	reverse leakage current	V _{RWM} = 3.3 V; T _{amb} = 25 °C		-	0.09	2	μA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	101	-	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; T _{amb} = 25 °C	[1]	-	-	8	V
		I _{PPM} = 18 A; T _{amb} = 25 °C	[1]	-	-	26	V
R _{diff}	differential resistance	I _R = 1 mA; T _{amb} = 25 °C		-	-	400	Ω

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

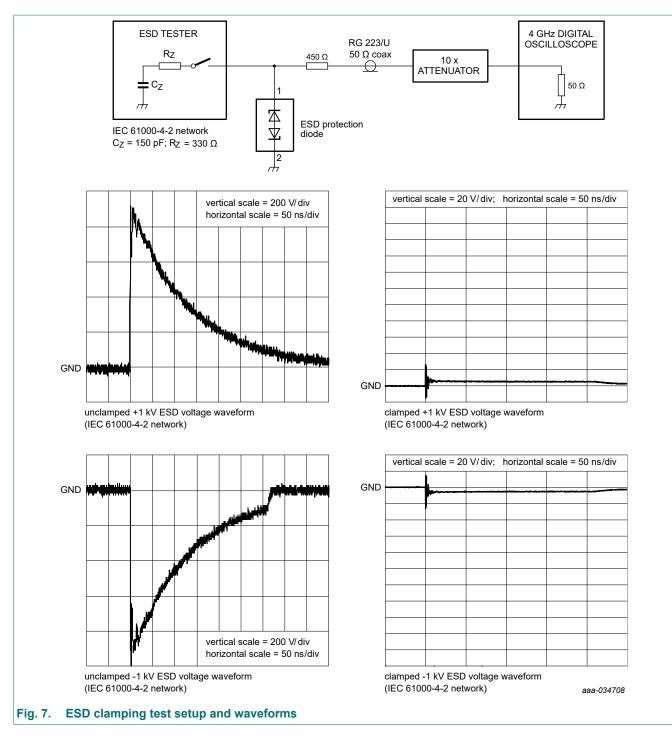


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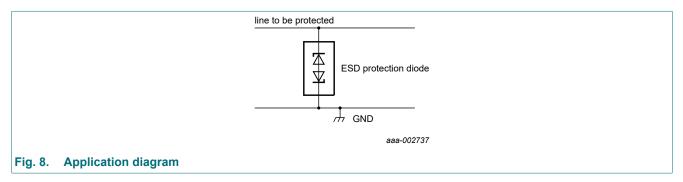
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10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

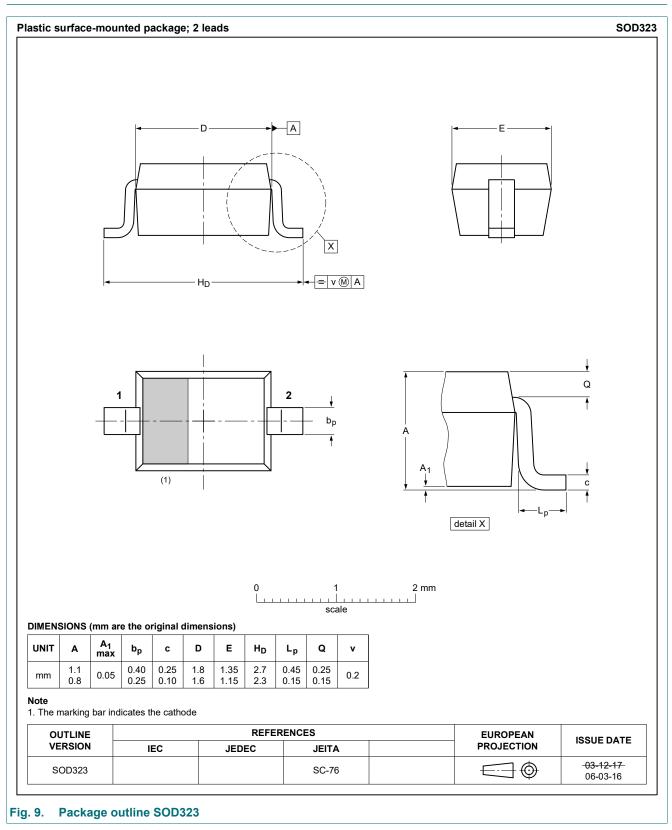
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline

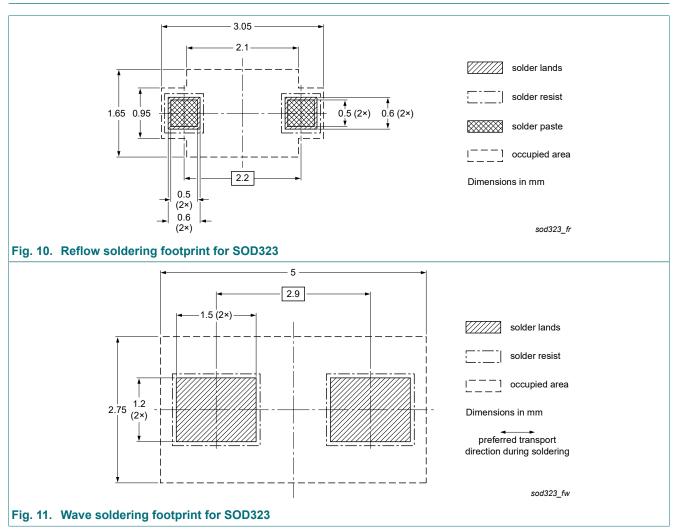


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13. Soldering



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14. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD3V3L1BA-Q v.1	20220519	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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