

OM13034,598: IAR KickStart Kit Featuring NXP Semiconductors LPC1347 MCU

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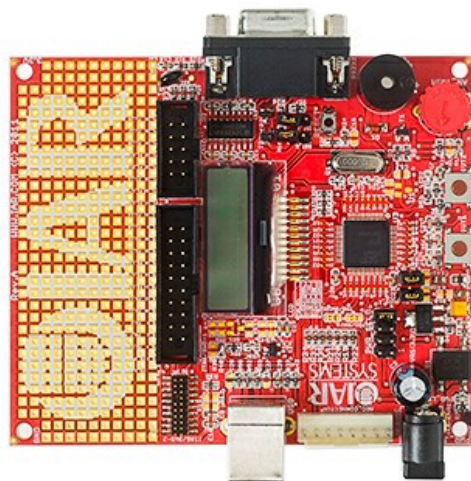
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Overview

- 12 bit ADC, 4 kB on-chip EEPROM, 64 kB on-chip flash program memory with a 256 byte page erase function
- J-Link Lite debug adapter included
- Standard 19-pin JTAG/SWD connector and 9-pin SWD connector
- Board can be powered via JTAG/SWD, USB, or external 6-9 V DC power adapter (not included)

OM13034 Image



Features

- The board is RoHS compliant, and features:
 - Small LCD
 - Two user buttons
 - Reset button
 - Two user LEDs
 - Power LED
 - Analog trimpot wheel
 - Buzzer
 - Prototyping area
 - RS232, DB9 connector
 - ACC connector
 - Temp sensor
 - UXT connector

Supported Devices

- [LPC1315FBD48](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1315FHN33](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1316FBD48](#): 48kB Flash, 8kB SRAM
- [LPC1316FHN33](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1317FBD48](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1317FBD64](#): Entry-level 32-bit

Microcontroller (MCU) based on ARM Cortex-M3 Core

- [LPC1317FHN33](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1345FBD48](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1345FHN33](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1346FBD48](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1346FHN33](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1347FBD48](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1347FBD64](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core
- [LPC1347FHN33](#): Entry-level 32-bit Microcontroller (MCU) based on ARM Cortex-M3 Core

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