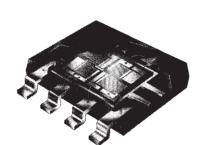


VISHAY RMKM- \$408 - \$508 **S714**

S816 S914

small outline resistor networks USIZ FILM®

- thin film technology



The RMK series of small outline surface mount style molded package can accommodate resistor network to your particular application requirements in compact circuit integration.

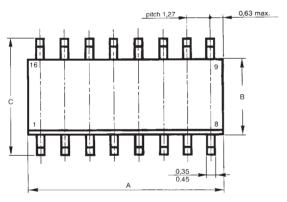
The resistor element is a proprietary nickel chromium film formulation on oxydized silicon.

Utilizing those networks will enable you to take advantage of parametric performances which will introduce in your circuitry high thermal and load life stability together with the added benefits of low noise and rapid rise time.

These S08/14/16 leads package will bring you:

- LOW T.C.
- TIGHT T.C. TRACKING
- MONOLITHIC RELIABILITY
- LOW NOISE
- RAPID RISE TIME
- Low cost

RMKM S...



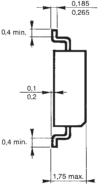


Table 1

Case	А	В	С
S08	4,75/4,95	3,9/4	5,9/6,2
S014	8,55/8,75	3,9/4	5,9/6,2
S016	9,8/10	3,9/4	6,3/6,6

Dimensions in mm

SPECIFICATIONS

MECHANICAL

MECHANICAL PROTECTION... epoxy molded assembly RESISTIVE ELEMENT... nickel chromium film

TERMINAL LEADS... tinned

UNIT WEIGHT S08... 0,070 g case S08

0,146 g cases S014, S016

Fig. 1

ELECTRICAL

RESISTANCE VALUE RANGE... 500 Ω to 200 k Ω ABSOLUTE TOLERANCE... ±0,1% to ±1% TOLERANCE RATIO... 0,5% to 0,05%

POWER RATING... case S08 250 mW at +70°C

cases S014 500 mW at +70°C

NOMINAL TEMPERATURE

COEFFICIENT... ±15 ppm/°C (-55°C to +125°C)

±10 ppm/°C (0°C to +70°C)

TCR TRACKING... 5 ppm/°C max.

(-55°C to +125°C)

LIMITING ELEMENT VOLTAGE... 50 VDC max.

VOLTAGE COEFFICIENT OF RESISTANCE... <0,1 ppm/V

NOISE INDEX... -45 dB typical, -35 dB max.

in accordance with MIL-STD 202 Method 308

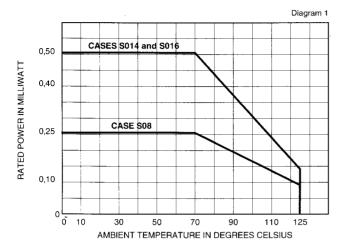
ENVIRONMENTAL

OPERATING TEMPERATURE

RANGE... -55°C to +125°C CLIMATIC CATEGORY... 55 / 125 / 56

POWER RATING CHART

Thermal performances of S0 packages are dependent upon substrate material, chip size, mounting process, chip area, die attach and lead frame material characteristics.



SOLDERING

The device can withstand a wave soldering temperature of +260°C for 10 seconds or vapor phase soldering temperature which are somehow lower.

MARKING

Printed:

 SFERNICE trademark, series, ohmic value, tolerance, manufacturing date.

TOLERANCE CODE

Table 2

CODE	А	В	D	F
ABSOLUTE and	±0,1%	±0,1%	±0,5%	±1%
RATIO TOLERANCES	S 0,05%	0,1 %	0,1 %	0,5%

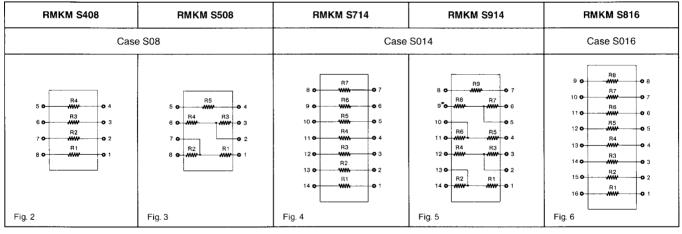
PERFORMANCES

Table 3

TESTS	CONDITIONS	TYPICAL DRIFTS
RESISTANCE TO SOLDERING HEAT	260°C / 10 s	0,04%
LOAD LIFE	2000 hrs Pr at 70°C	0,075%

FUNCTIONAL DIAGRAM

Table 4



For other configurations, please consult SFERNICE.

ORDERING PROCEDURE













Legal Disclaimer Notice



Vishay

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